

MIPI DSI

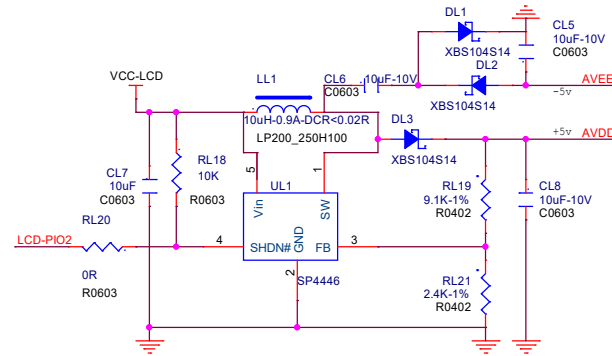
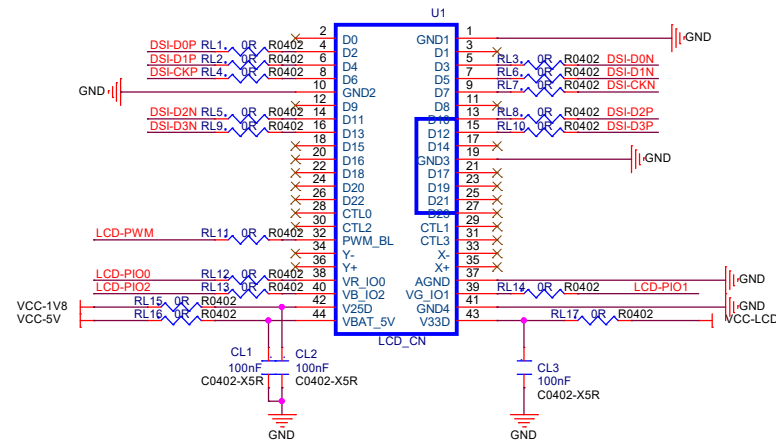
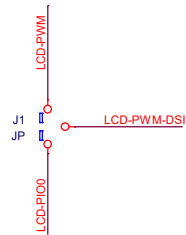
控制IO说明:

LCD PWR EN: LCD-PIO1

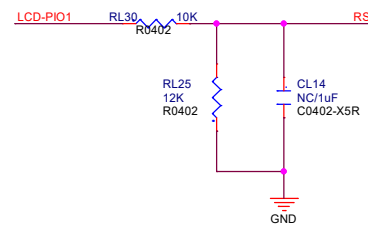
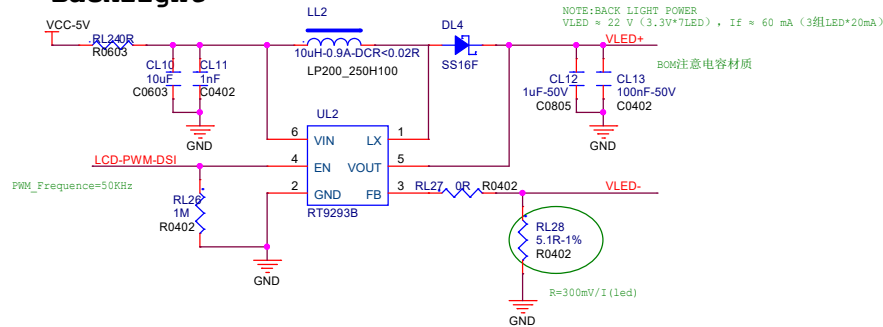
LCD BL: LCD-PIO0

LCD PWM: NULL

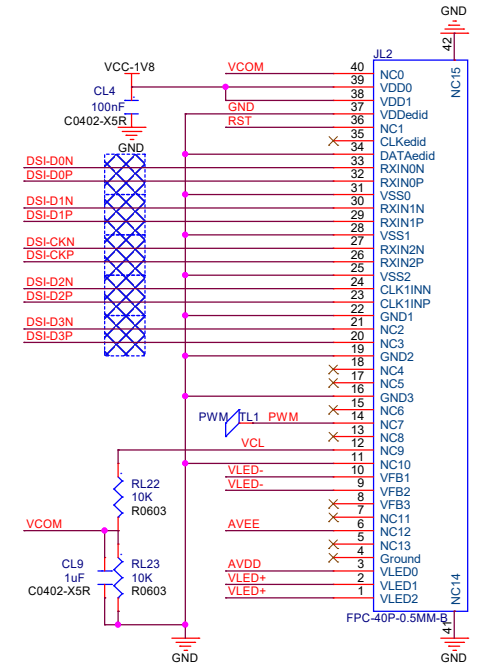
LCD RST: LCD-PIO2



Backlight



FPC 座放在PCB 背面时, 连接异面排线,下接触的FPC座pin 顺序要与屏座pin 顺序相反



DSI-D0, DSI-D1, DSI-D2, DSI-D3, DSI-CLK 差分布线, 100R阻抗匹配