

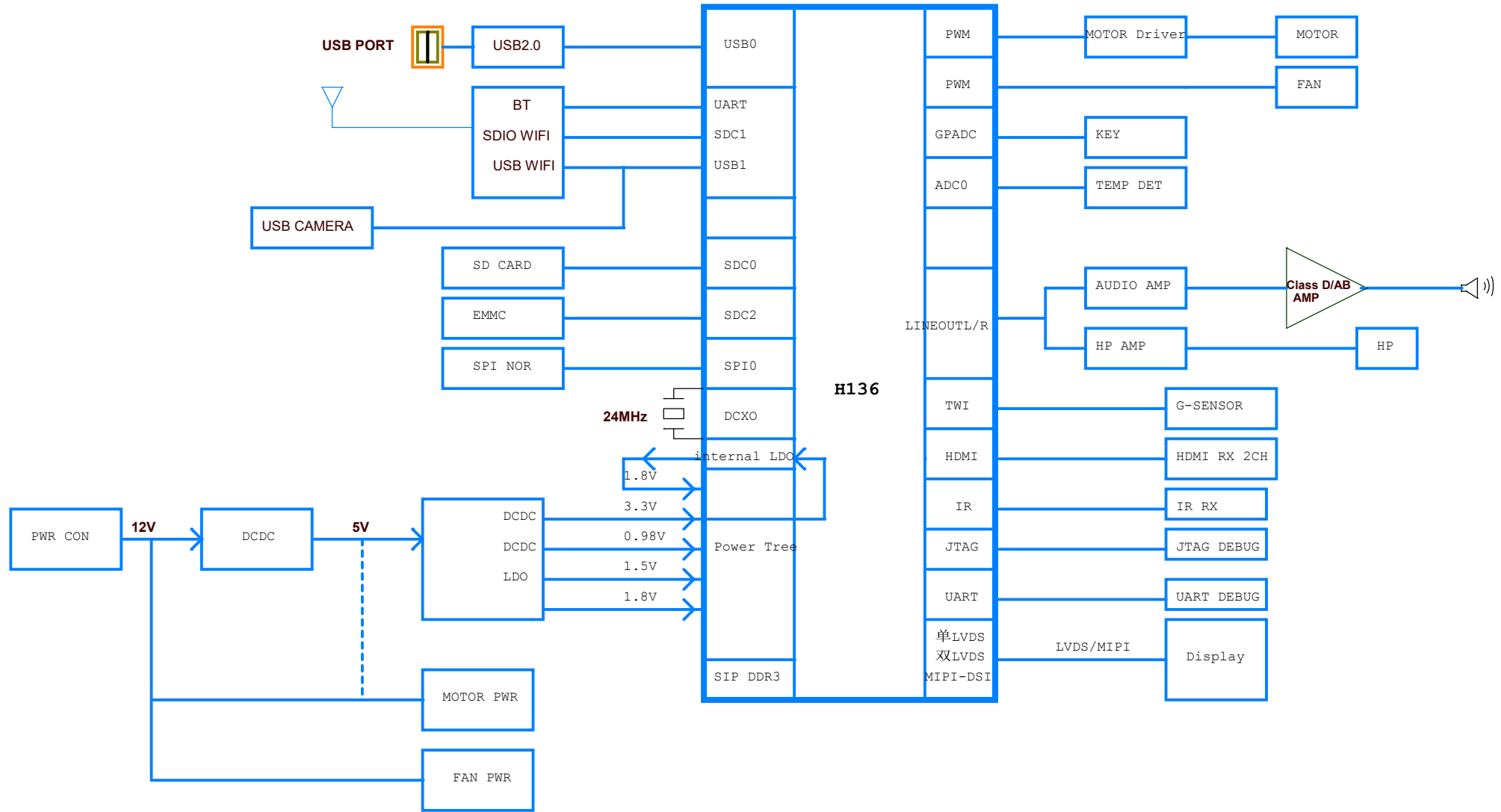
VERSION HISTORY

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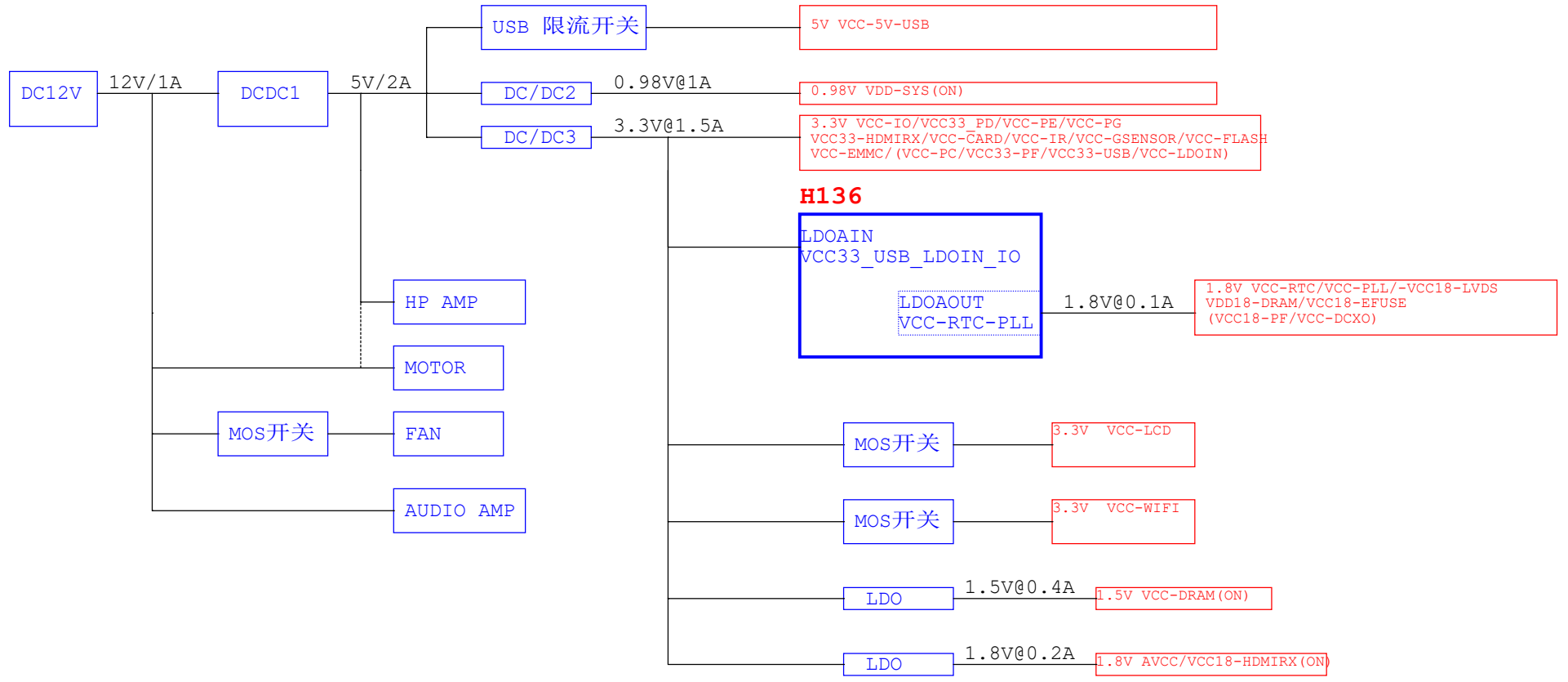
- P01 VERSION HISTORY
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- P05 POWER
- P06 SOC-1
- P07 SOC-2
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- P09 LCD
- P10 USB/USB CAM/SD CARD
- P11 AUDIO
- P12 HDMI IN
- P13 IR/KEY/G-SENSOR/UART
- P14 MOTOR/FAN/LED
- P15 SDIO WIFI

Revision	Description	Date	Drawn	Checked	Approved
Ver 1.0	initial version	2025/2/20			

BLOCK



DEFAULT POWER ON
 DEFAULT POWER OFF



GPIO ASSIGNMENT

Pin	Define	CFG	Function
PA0	ADC0	2	NTC
PA1	LCD-PWR	1	LCD
PA2	PWR-LED	1	LED

Pin	Define	CFG	Function
PC0	S-CLK/e-MMC-CLK	2/3	SPI NOR /EMMC
PC1	S-CS0/e-MMC-CMD	2/3	
PC2	S-MISO/e-MMC-D2	2/3	
PC3	S-MOSI/e-MMC-D0	2/3	
PC4	S-WP/e-MMC-D1	2/3	
PC5	S-HOLD/e-MMC-D3	2/3	
PC6	PI-IO	14	PI
PC7	BL-PWM	3	LCD
PC8	Motor-IN1	3	MOTOR
PC9	Motor-IN2	3	
PC10	Motor-IN3	3	
PC11	Motor-IN4	3	
PC12	IR-RX	7	IR Receive

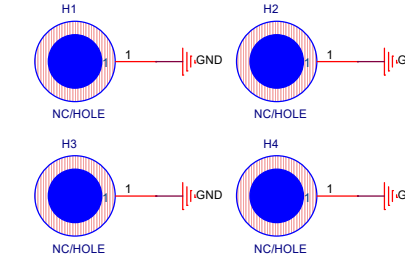
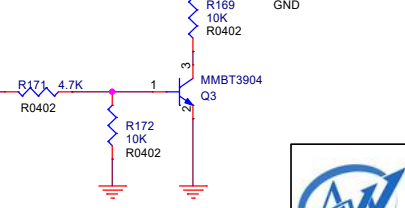
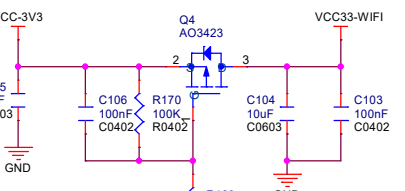
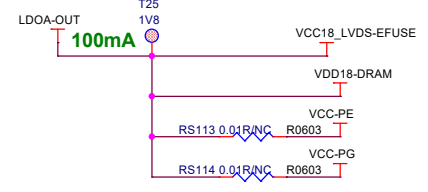
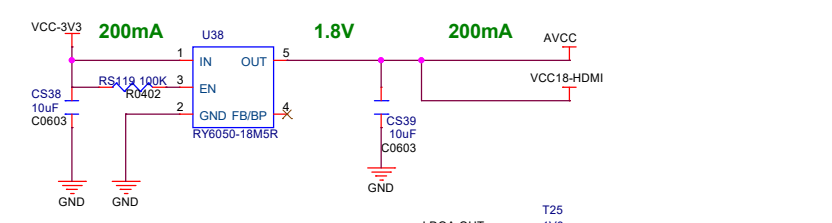
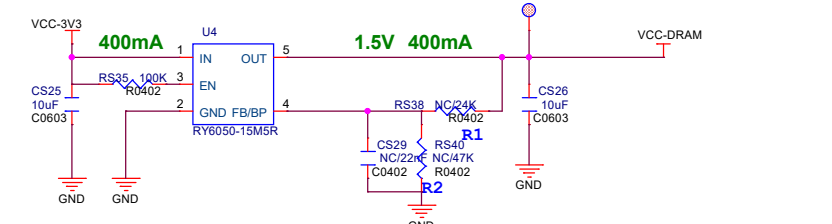
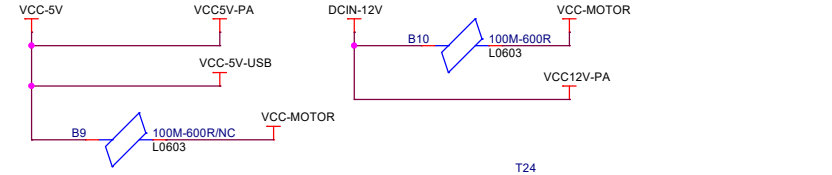
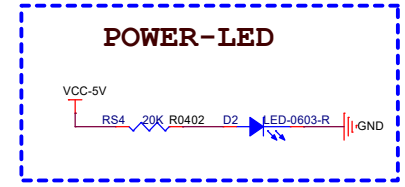
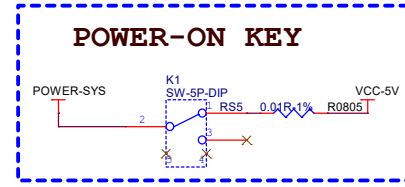
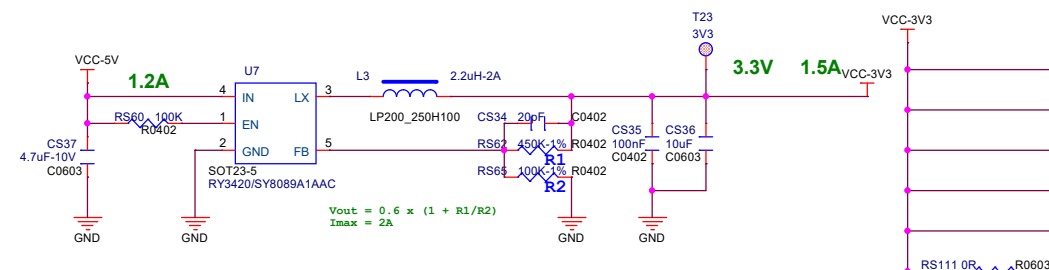
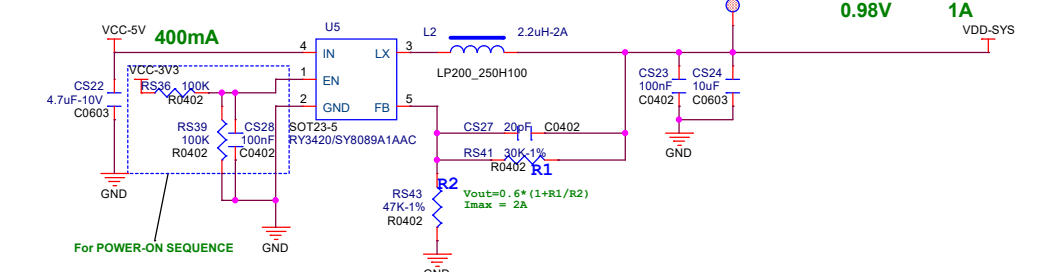
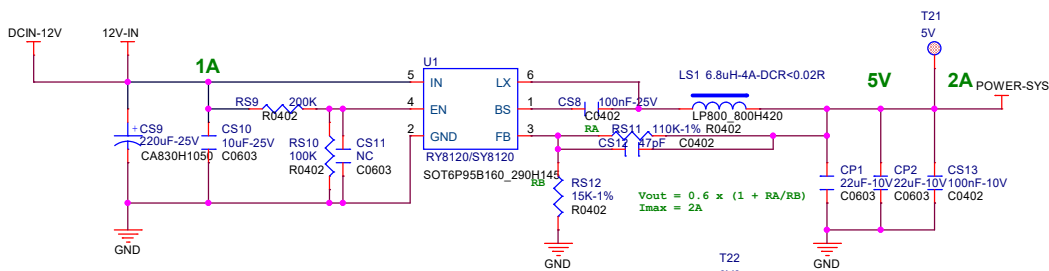
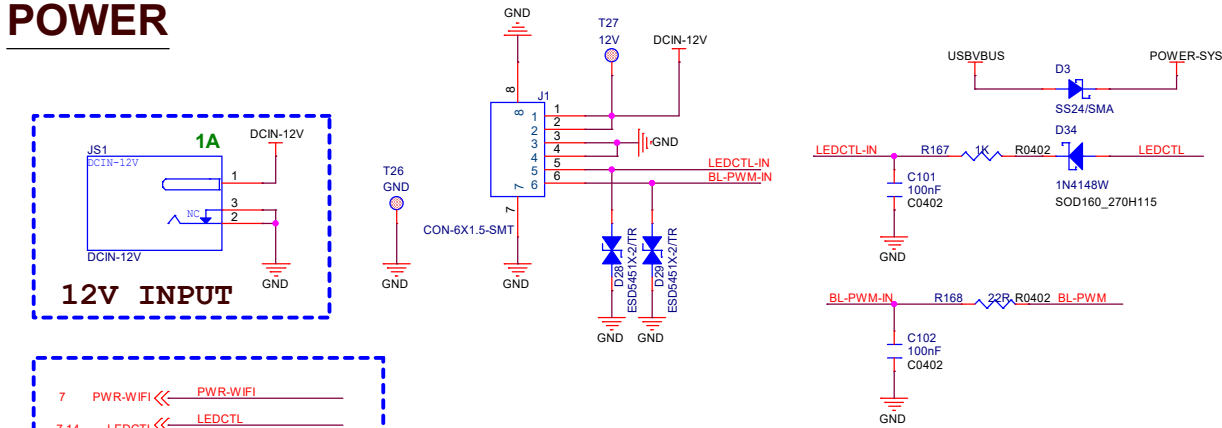
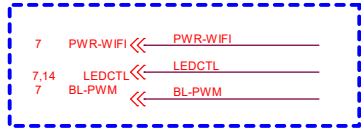
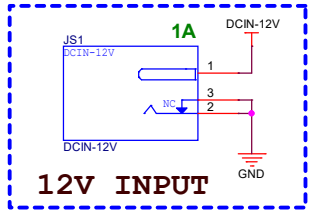
Pin	Define	CFG	Function
PD0	LVDS0-D0P	3	Dual-LVDS -1080P
PD1	LVDS0-D0N	3	
PD2	LVDS0-D1P	3	
PD3	LVDS0-D1N	3	
PD4	LVDS0-D2P	3	
PD5	LVDS0-D2N	3	
PD6	LVDS0-CKP	3	
PD7	LVDS0-CKN	3	
PD8	LVDS0-D3P	3	
PD9	LVDS0-D3N	3	
PD10	LVDS1-D0P	3	
PD11	LVDS1-D0N	3	
PD12	LVDS1-D1P	3	
PD13	LVDS1-D1N	3	
PD14	LVDS1-D2P	3	
PD15	LVDS1-D2N	3	
PD16	LVDS1-CKP	3	
PD17	LVDS1-CKN	3	
PD18	LVDS1-D3P	3	
PD19	LVDS1-D3N	3	

Pin	Define	CFG	Function
PE0	PA-SHDN	1	PA
PE1	LEDCTL	1	LED
PE2	HP-DET/UART0-TX	14	HP/UART
PE3	FAN-FG/UART0-RX	14	FAN/UART
PE4	PWR-WIFI/PE-JTAG-MS	1/7	WiFi/JTAG
PE5	LCD-STBYB/PE-JTAG-DI	1/7	LCD/JTAG
PE6	LCD-UD/PE-JTAG-DO	1/7	LCD/JTAG
PE7	LCD-LR/PE-JTAG-CK	1/7	LCD/JTAG
PE8	LCD-RESET	1	LCD
PE9	TWI2-SDA	7	GSENSOR
PE10	USB-DRVVBUS/SDC0-DET	1/14	USB/CARD
PE11	TWI2-SCK	7	GSENSOR
PE12	FAN-PWM	4	FAN
PE13	BT-REG-ON	1	BT
PE12	HP-SD	1	HP
PE13	GSENSOR-INT	14	GSENSOR

Pin	Define	CFG	Function
PF0	SDC0-D1	2	CARD0
PF1	SDC0-D0	2	
PF2	SDC0-CLK	2	
PF3	SDC0-CMD	2	
PF4	SDC0-D3	2	
PF5	SDC0-D2	2	

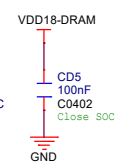
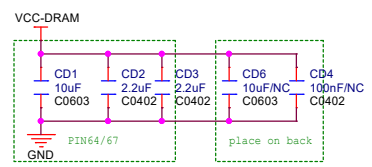
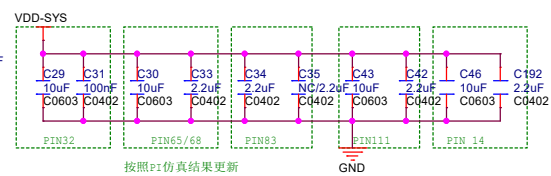
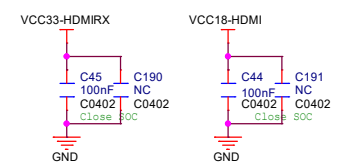
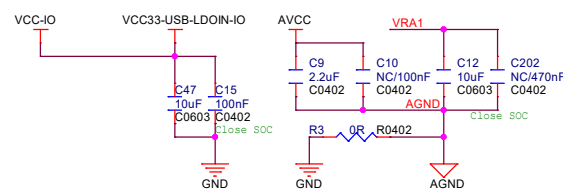
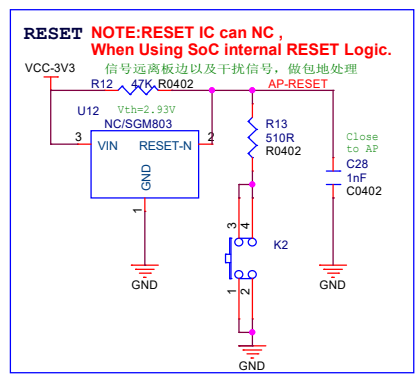
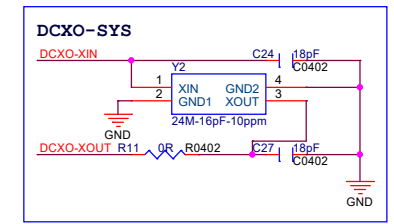
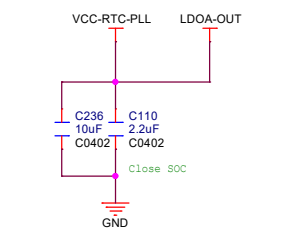
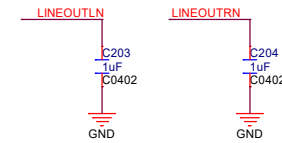
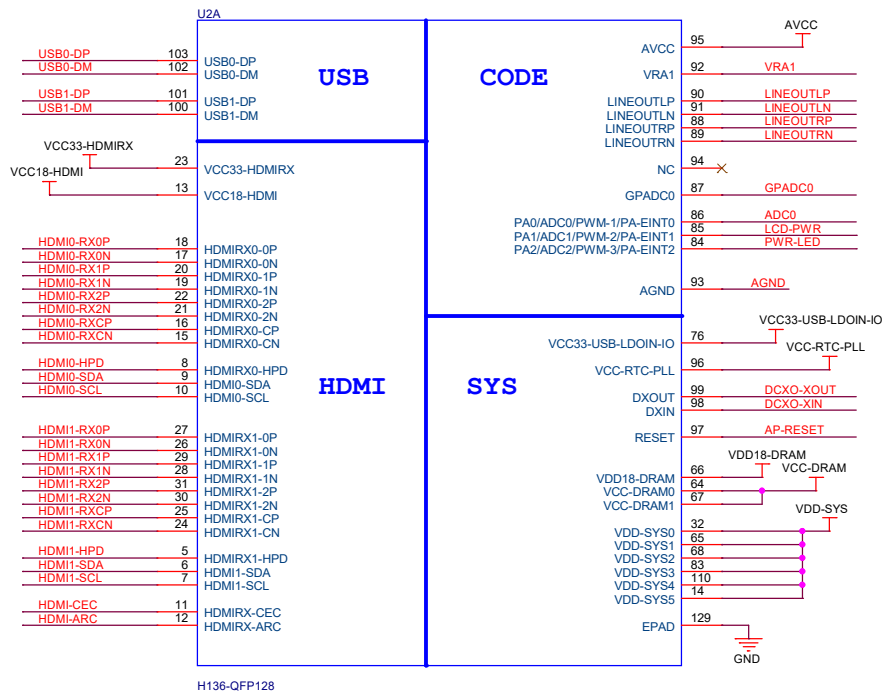
Pin Number	Pin Name	CFG	Function
PG0	WL-SDIO-D1	2	WIFI+BT
PG1	WL-SDIO-D0	2	
PG2	WL-SDIO-CLK	2	
PG3	WL-SDIO-CMD	2	
PG4	WL-SDIO-D3	2	
PG5	WL-SDIO-D2	2	
PG6	BT-UART-RX	2	
PG7	BT-UART-TX	2	
PG8	BT-UART-CTS	2	
PG9	BT-UART-RTS	2	
PG10	BT-WAKE-AP	14	
PG11	WL-WAKE-AP	14	
PG12	AP-WAKE-BT	1	

POWER



AllWinner Technology Co., Ltd		
Design Name: H136-M3-HXX-STD-V1_0		
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SOC



SOC

U2B

PC

PC0/SPI0-CLK/SDC2-CLK/PC-EINT0
PC1/SPI0-CS0/SDC2-CMD/PC-EINT1
PC2/SPI0-MISO/SDC2-D2/BOOT-SEL0/PC-EINT2
PC3/SPI0-MOSI/SDC2-D0/BOOT-SEL1/PC-EINT3
PC4/SPI0-WP/SDC2-D1/UART3-TX/TWI3-SCK/PC-EINT4
PC5/SPI0-HOLD/SDC2-D3/UART3-RX/TWI3-SDATCON/FSYNC/PC-EINT5
PC6/UART2-TX/SDC2-RST/TWI2-SCK/LEDC/PC-EINT6
PC7/UART2-RX/PWM-4/TWI2-SDA/SPI1-WP/DBI-TE/PC-EINT7
PC8/DMIC-DATA3/PWM-5/TWI2-SCK/SPI1-HOLD/DBI-DCX/DBI-WRX/UART0-TX/UART1-TX/PC-EINT8
PC9/DMIC-DATA2/PWM-6/TWI2-SDA/SPI1-MISO/DBI-SDI/DBI-TE/DBI-DCX/UART0-RX/UART1-RX/PC-EINT9
PC10/DMIC-DATA1/PWM-7/TWI0-SCK/SPI1-MOSI/DBI-SDO/CLK-FANOUT0/UART1-RTS/PC-EINT10
PC11/DMIC-DATA0/PWM-2/TWI0-SDA/SPI1-CLK/DBI-CLK/CLK-FANOUT1/UART1-CTS/PC-EINT11
PC12/DMIC-CLK/PWM-0/OWA-IN/SPI1-CS0/DBI-CSX/CLK-FANOUT2/IR-RX/PC-EINT12

PD

VCC18-LVDS-EFUSE
VCC33-PD
VCC18-LVDS-EFUSE
VCC33-PD
PD0/LCD0-D2/LVDS0-D0P/DSI-D0P/TWI0-SCK/PD-EINT0
PD1/LCD0-D3/LVDS0-D0N/DSI-D0N/UART2-TX/PD-EINT1
PD2/LCD0-D4/LVDS0-D1P/DSI-D1P/UART2-RX/PD-EINT2
PD3/LCD0-D5/LVDS0-D1N/DSI-D1N/UART2-RTS/PD-EINT3
PD4/LCD0-D6/LVDS0-D2P/DSI-D2P/UART2-CTS/PD-EINT4
PD5/LCD0-D7/LVDS0-D2N/DSI-D2N/UART5-TX/PD-EINT5
PD6/LCD0-D8/LVDS0-D2P/DSI-D2P/UART5-TX/PD-EINT6
PD7/LCD0-D11/LVDS0-D3N/DSI-D3N/UART4-RX/PD-EINT7
PD8/LCD0-D12/LVDS0-D3P/DSI-D3P/UART4-RX/PD-EINT8
PD9/LCD0-D13/LVDS0-D3N/DSI-D3N/PWM-6/PD-EINT9
PD10/LCD0-D14/LVDS1-D0P/SPI1-CS0/DBI-CSX/UART3-TX/PD-EINT10
PD11/LCD0-D15/LVDS1-D0N/SPI1-CLK/DBI-SCLK/UART3-RX/PD-EINT11
PD12/LCD0-D18/LVDS1-D1P/SPI1-MOSI/DBI-SDO/TWI0-SDA/PD-EINT12
PD13/LCD0-D19/LVDS1-D1N/SPI1-MISO/DBI-SDI/DBI-TE/DBI-DCX/UART3-RTS/PD-EINT13
PD14/LCD0-D20/LVDS1-D2P/SPI1-HOLD/DBI-DCX/DBI-WRX/UART3-CTS/PD-EINT14
PD15/LCD0-D21/LVDS1-D2N/SPI1-WP/DBI-TE/IR-RX/PD-EINT15
PD16/LCD0-D22/LVDS1-D3N/PWM-0/PD-EINT16
PD17/LCD0-D23/LVDS1-D3N/PWM-1/PD-EINT17
PD18/LCD0-CLK/LVDS1-D3P/PWM-2/PD-EINT18
PD19/LCD0-DE/LVDS1-D3N/PWM-3/PD-EINT19

PE

PE0/NC/SIO-HSYNCLCD0-D0/TWI1-SCK/UART2-TX/UART4-RX/RGMII0-EPHY-25/50M/PE-EINT0
PE1/NC/SIO-VSYNCLCD0-D1/TWI1-SDA/UART2-RX/UART4-RX/RGMII0-CLKIN/RMII0-RXER/PE-EINT1
PE2/NC/SIO-MCLK/LCD0-D8/TWI0-SCK/CLK-FANOUT0/UART0-TX/RGMII0-RXCTL/RMII0-CRS-DV/PE-EINT2
PE3/NC/SIO-PCLK/LCD0-D9/TWI0-SDA/CLK-FANOUT1/UART0-RX/RGMII0-MDIO/PE-EINT3
PE4/NC/SIO-D1/LCD0-D16/TWI2-SCK/CLK-FANOUT2/RJTAG-MS/RGMII0-MDC/PE-EINT4
PE5/NC/SIO-D7/LCD0-D17/TWI2-SDA/LEDC/RJTAG-DI/RGMII0-TXCTL/RMII0-TXEN/PE-EINT5
PE6/NC/SIO-D6/UART2-TX/TWI3-SCK/OWA-IN/HDMI0-SDA/RJTAG-D0/RGMII0-TXD1/RMII0-TXD1/PE-EINT6
PE7/NC/SIO-D0/UART2-RX/TWI3-SDA/OWA-OUT/HDMI0-SCL/RJTAG-CK/RGMII0-TXD0/RMII0-TXD0/PE-EINT7
PE8/NC/SIO-D5/UART2-RTS/PWM-2/UART3-TX/RGMII0-TXCK/RMII0-TXCK/PE-EINT8
PE9/PWM-5/UART2-CTS/PWM-3/UART3-RX/HDMI-CEC/TWI2-SDA/RGMII0-RXD1/RMII0-RXD1/PE-EINT9
PE10/NC/SIO-D4/UART4-TX/PWM-4/IR-RX/RGMII0-RXD0/RMII0-RXD0/PE-EINT10
PE11/NC/SIO-FIELD/UART4-RX/IR-TX/I2S0-MCLK/DMIC-CLK/TWI2-SCK/RGMII0-RXCK/RMII0-NULL/PE-EINT11
PE12/NC/SIO-D3/UART5-TX/PWM-7/I2S0-BCLK/DMIC-DATA0/LCD0-HSYNCRGMII0-RXD2/RMII0-NULL/PE-EINT12
PE13/NC/SIO-D2/UART5-RX/PWM-6/I2S0-LRCK/DMIC-DATA1/LCD0-VSYNCRGMII0-RXD3/RMII0-NULL/PE-EINT13
PE14/TWI1-SCK/UART1-TX/I2S0-DOUT/I2S0-DIN0/DMIC-DATA2/UART2-TX/RGMII0-TXD2/RMII0-NULL/PE-EINT14
PE15/TWI1-SDA/UART1-RX/I2S0-DOUT0/I2S0-DIN1/DMIC-DATA3/UART2-RX/RGMII0-TXD3/RMII0-NULL/PE-EINT15

PF

PF0/SDC0-D1/DJTAG-MS/RJTAG-MS/LCD0-D0/PF-EINT0
PF1/SDC0-D0/DJTAG-DI/RJTAG-DI/LCD0-D1/PF-EINT1
PF2/SDC0-CLK/UART0-TX/TWI0-SCK/LEDC/OWA-IN/LCD0-D8/PF-EINT2
PF3/SDC0-CMD/DJTAG-DO/RJTAG-DO/TWI1-SCK/LCD0-D9/PF-EINT3
PF4/SDC0-D3/UART0-RX/TWI0-SDA/PWM-6/IR-TX/LCD0-D16/PF-EINT4
PF5/SDC0-D2/DJTAG-CK/RJTAG-CK/TWI1-SDA/LCD0-D17/PF-EINT5

PG

PG0/SDC1-D1/UART3-TX/RMII1-EPHY-25/50M/PWM-7/SDC1-D3/SDC1-D1/PG-EINT0
PG1/SDC1-D0/UART3-RX/RMII1-RXER/PWM-6/SDC1-D1/SDC1-D3/PG-EINT1
PG2/SDC1-CLK/UART3-RTS/RMII1-CRS-DV/UART4-TX/SDC1-CLK/PG-EINT2
PG3/SDC1-CMD/UART3-CTS/RMII1-MDIO/UART4-RX/SDC1-D0/SDC1-D2/PG-EINT3
PG4/SDC1-D3/UART5-TX/RMII1-MDC/PWM-5/SDC1-CMD/SDC1-CMD/PG-EINT4
PG5/SDC1-D2/UART5-RX/RMII1-TXEN/PWM-4/SDC1-D2/SDC1-D0/PG-EINT5
PG6/UART1-TX/TWI2-SCK/RMII1-TXD1/PWM-1/PG-EINT6
PG7/UART1-RX/TWI2-SDA/RMII1-TXD0/OWA-IN/PG-EINT7
PG8/UART1-RTS/TWI1-SCK/RMII1-TXCK/UART3-TX/HDMI1-SCL/PG-EINT8
PG9/UART1-CTS/TWI1-SDA/RMII1-RXD1/UART3-RX/HDMI1-SDA/PG-EINT9
PG10/PWM-3/TWI3-SCK/RMII1-RXD0/CLK-FANOUT1/IR-RX/PG-EINT10
PG11/TWI3-SDA/CLK-FANOUT1/CON-FSYNCPG-EINT11
PG12/CLK-FANOUT2/PWM-0/PG-EINT12

82 PC0 R29 33R R0402 S-CLK
81 PC1 R30 0R R0402 S-CS0
80 PC2 R31 0R R0402 S-MISO
78 PC3 R32 0R R0402 S-MOSI
79 PC4 R33 0R R0402 S-WP
77 PC5 R34 0R R0402 S-HOLD
75 PC6 R309 0R R0402 PHO
74 PC7 R310 0R R0402 BL-PWM
73 R291 0R R0402 Motor-IN1
72 R292 0R R0402 Motor-IN2
71 R293 0R R0402 Motor-IN3
70 R294 0R R0402 Motor-IN4
69 PC12 IR-RX

VCC18-LVDS-EFUSE
VCC33-PD
128 C237 100nF C0402 GND
115 C261 100nF C0402 GND

122 LVDS0-D0P
123 LVDS0-D0N
124 LVDS0-D1P
125 LVDS0-D1N
126 LVDS0-D2P
127 LVDS0-D2N
1 LVDS0-CKP
2 LVDS0-CKN
3 LVDS0-D3P
4 LVDS0-D3N
111 LVDS1-D0P
112 LVDS1-D0N
113 LVDS1-D1P
114 LVDS1-D1N
116 LVDS1-D2P
117 LVDS1-D2N
118 LVDS1-D3P
119 LVDS1-D3N
120 LVDS1-D3P
121 LVDS1-D3N

VCC-PE
VCC-PE
50 C184 100nF C0402 GND

47 R311 0R R0402 PA-SHDN
48 R312 0R R0402 LEDCTL
49 R313 0R R0402 HP-DET
50 R314 0R R0402 FAN-FG
51 PE-JTAG-MS R302 0R R0402 PWR-WIFI
52 PE-JTAG-DI R316 0R R0402 LCD-STBYB
53 PE-JTAG-DO R317 0R R0402 LCD-UD
54 PE-JTAG-CK R315 0R R0402 LCD-LR
55 R336 0R R0402 LCD-RESET
56 R337 0R R0402 USB-DRVVBUS
57 R391 0R R0402 TWI2-SCK
58 R392 0R R0402 TWI2-SDA
59 R393 0R R0402 TWI2-SCK
60 R394 1K R0402 FAN-FG
61 BT-REG-ON
62 HP-SD
63 R403 0R R0402 GSENSOR-INT

104 R105 0R R0402 SDC0-D1
105 R106 0R R0402 SDC0-D0
106 R107 3R R0402 SDC0-CLK
107 R108 0R R0402 SDC0-CMD
108 R109 0R R0402 SDC0-D3
109 R110 0R R0402 SDC0-D2

VCC-PG
VCC-PG
41 C185 100nF C0402 GND

33 R279 22R R0402 WL-SDIO-D1
34 R281 22R R0402 WL-SDIO-D0
35 R280 33R R0402 WL-SDIO-CLK
36 R282 22R R0402 WL-SDIO-CMD
37 R284 22R R0402 WL-SDIO-D3
38 R283 22R R0402 WL-SDIO-D2
39 BT-UART-RX
40 BT-UART-TX
41 BT-UART-CTS
42 BT-UART-RTS
43 BT-WAKE-AP
44 BT-WAKE-AP
45 WL-WAKE-AP
46 AP-WAKE-BT

PC0 R384 NC/0R R0402 e-MMC-CLK
PC1 R386 0R/NC R0402 e-MMC-CMD
PC2 R388 0R/NC R0402 e-MMC-D0
PC3 R387 0R/NC R0402 e-MMC-D1
PC4 R389 0R/NC R0402 e-MMC-D2
PC5 R388 0R/NC R0402 e-MMC-D3

VCC-PE
R398 NC/10K R0402 PE-JTAG-MS
R397 NC/10K R0402 PE-JTAG-DI
R400 NC/10K R0402 PE-JTAG-DO
R399 NC/10K R0402 PE-JTAG-CK
R402 2.2K R0402 TWI2-SCK
R401 10K R0402 TWI2-SDA
T39 MS
T41 DI
T40 DO
T42 CK

HP-DET R394 1K R0402 UART0-TX
FAN-FG R395 1K R0402 UART0-RX

USB-DRVVBUS R221 0R R0402 SDC0-DET

H136-QFP128

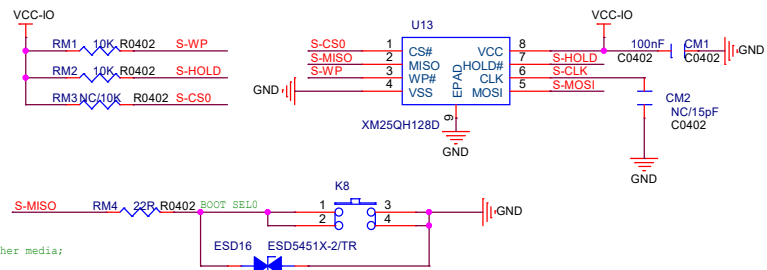
AllWinner Technology Co., Ltd
Design Name: H136-M3-HXX-STD-V1_0
Size: A3
Page Name: 07 SOC-2
Date: Tuesday, March 18, 2025
Sheet: 7 of 15

SPI-NOR

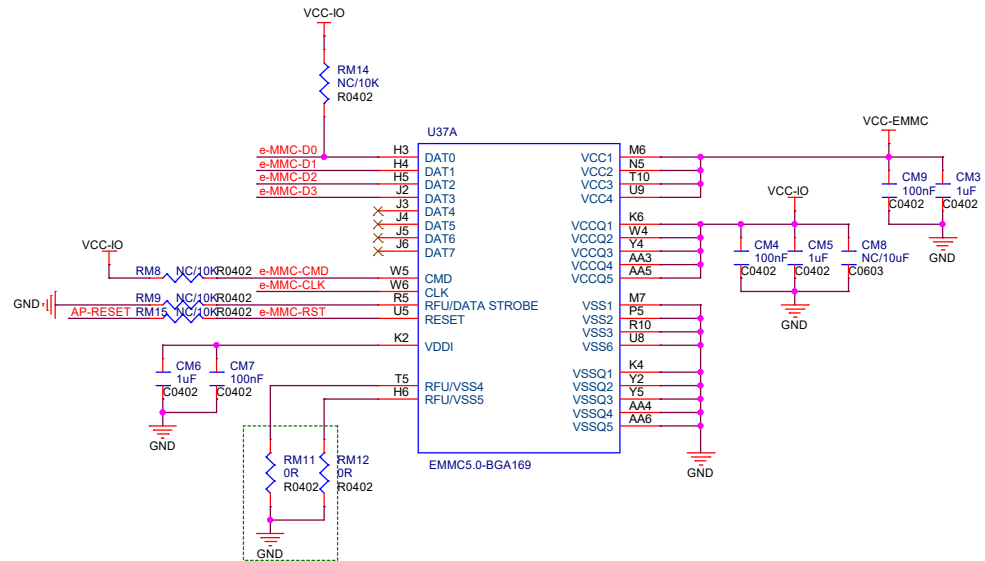
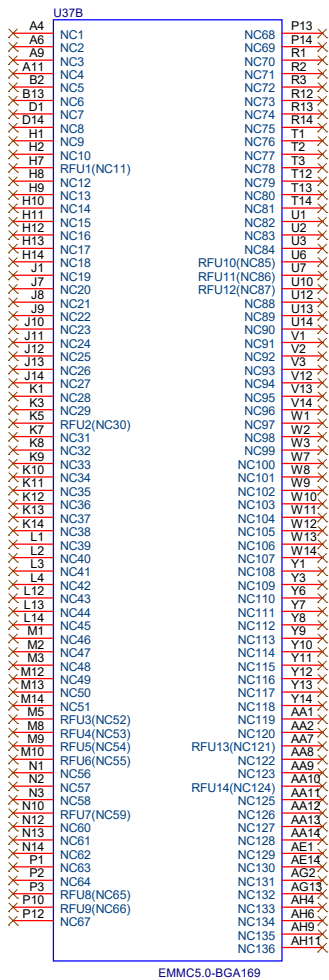
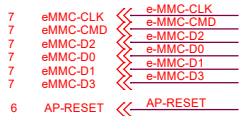


```

boot_sel[1:0]
FC3 FC2
0 0
0 1
1 0
1 1
Default 1 1
SPI NOR->SPI NAND;
SMHC0->SPI NOR->other media;
SMHC0->SPI NAND->other media;
SMHC0->EMMC2_BOOT->EMMC2_USB->other media;
    
```



EMMC



If eMMC is not v5.0/v5.1, then NC this two resistors.

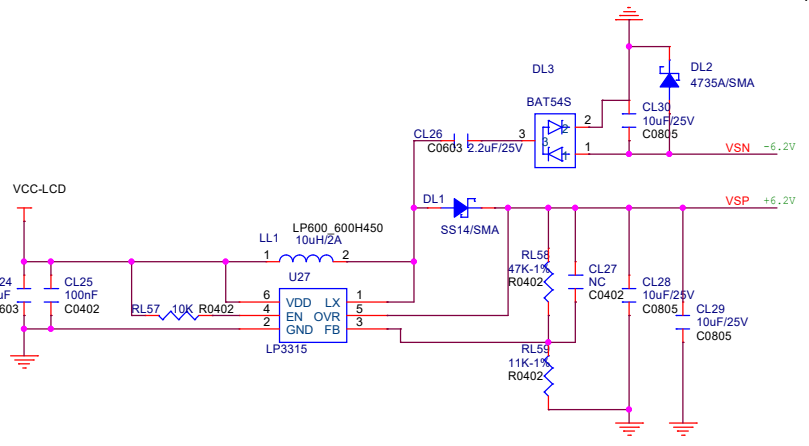
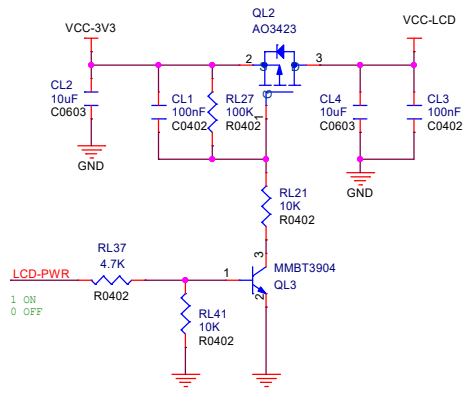
7 LVDS0-CKP << LVDS0-CKP
 7 LVDS0-CKN << LVDS0-CKN
 7 LVDS0-D0P << LVDS0-D0P
 7 LVDS0-D0N << LVDS0-D0N
 7 LVDS0-D1P << LVDS0-D1P
 7 LVDS0-D1N << LVDS0-D1N
 7 LVDS0-D2P << LVDS0-D2P
 7 LVDS0-D2N << LVDS0-D2N
 7 LVDS0-D3P << LVDS0-D3P
 7 LVDS0-D3N << LVDS0-D3N

7 LVDS1-CKP << LVDS1-CKP
 7 LVDS1-CKN << LVDS1-CKN
 7 LVDS1-D0P << LVDS1-D0P
 7 LVDS1-D0N << LVDS1-D0N
 7 LVDS1-D1P << LVDS1-D1P
 7 LVDS1-D1N << LVDS1-D1N
 7 LVDS1-D2P << LVDS1-D2P
 7 LVDS1-D2N << LVDS1-D2N
 7 LVDS1-D3P << LVDS1-D3P
 7 LVDS1-D3N << LVDS1-D3N

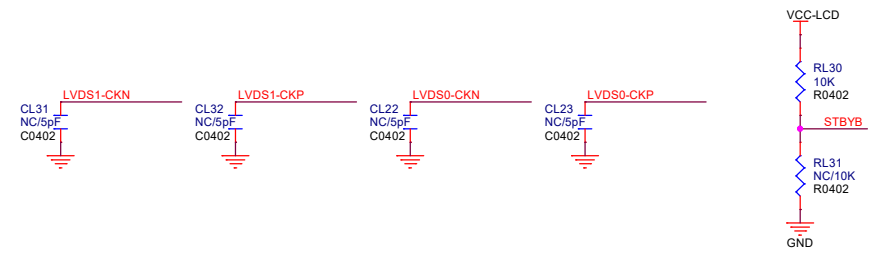
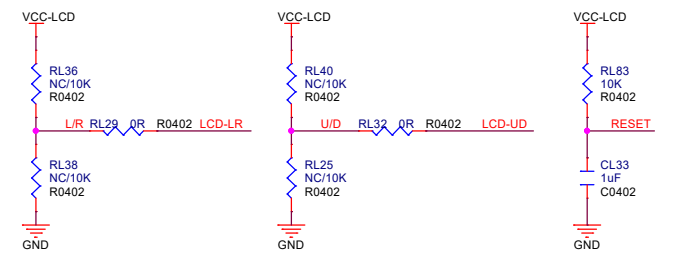
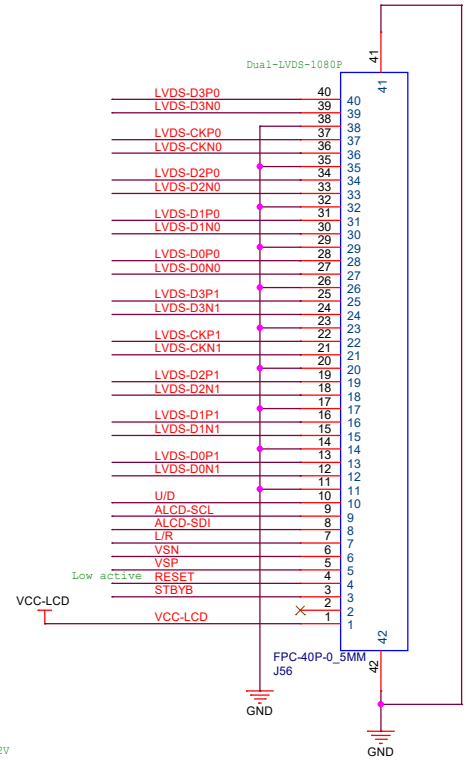
6 LCD-PWR << LCD-PWR
 7 LCD-STBYB << LCD-STBYB
 7 LCD-RESET << LCD-RESET
 7 LCD-LR << LCD-LR
 7 LCD-UD << LCD-UD

7,13 TWI2-SCK << TWI2-SCK
 7,13 TWI2-SDA << TWI2-SDA

LVDS

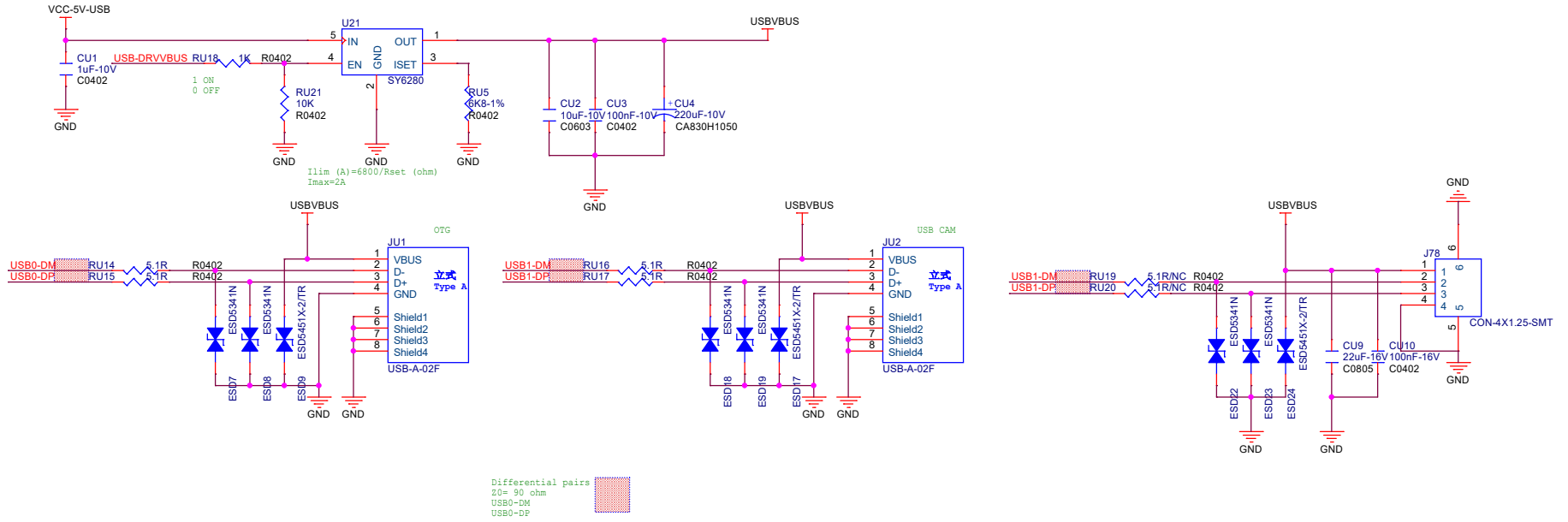


LVDS0-CKP	RL1	22R	R0402	LVDS-CKP0	LVDS1-CKP	RL48	22R	R0402	LVDS-CKP1
LVDS0-CKN	RL2	22R	R0402	LVDS-CKN0	LVDS1-CKN	RL53	22R	R0402	LVDS-CKN1
LVDS0-D0P	RL3	22R	R0402	LVDS-D0P0	LVDS1-D0P	RL52	22R	R0402	LVDS-D0P1
LVDS0-D0N	RL4	22R	R0402	LVDS-D0N0	LVDS1-D0N	RL47	22R	R0402	LVDS-D0N1
LVDS0-D1P	RL5	22R	R0402	LVDS-D1P0	LVDS1-D1P	RL55	22R	R0402	LVDS-D1P1
LVDS0-D1N	RL6	22R	R0402	LVDS-D1N0	LVDS1-D1N	RL49	22R	R0402	LVDS-D1N1
LVDS0-D2P	RL7	22R	R0402	LVDS-D2P0	LVDS1-D2P	RL54	22R	R0402	LVDS-D2P1
LVDS0-D2N	RL8	22R	R0402	LVDS-D2N0	LVDS1-D2N	RL50	22R	R0402	LVDS-D2N1
LVDS0-D3P	RL9	22R	R0402	LVDS-D3P0	LVDS1-D3P	RL51	22R	R0402	LVDS-D3P1
LVDS0-D3N	RL10	22R	R0402	LVDS-D3N0	LVDS1-D3N	RL56	22R	R0402	LVDS-D3N1
TWI2-SCK	RL43	0R	R0402	ALCD-SCL					
TWI2-SDA	RL44	0R	R0402	ALCD-SDI					
LCD-STBYB	RL45	0R	R0402	STBYB					
LCD-RESET	RL81	22R	R0402	RESET					



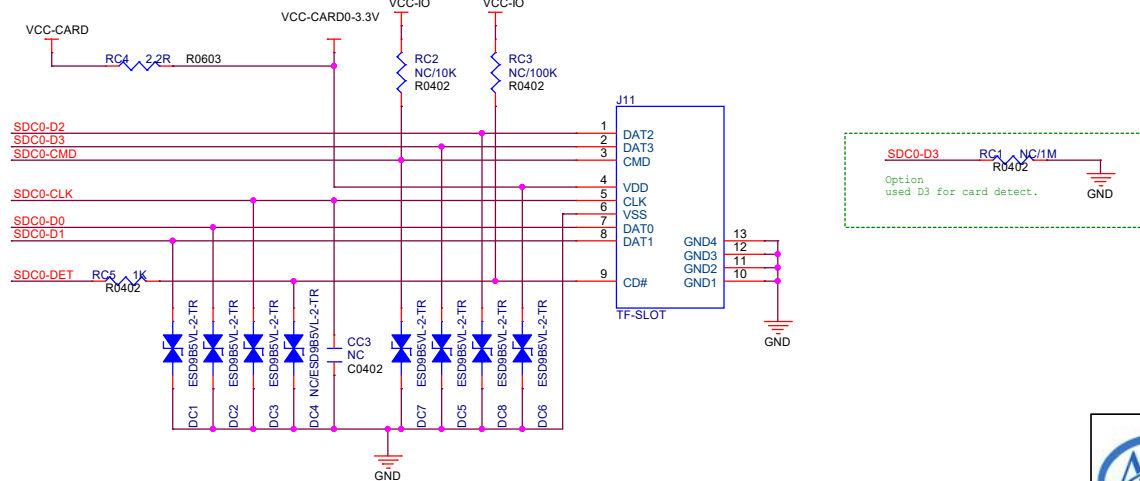
USB

- 7 USB-DRVVBUS << USB-DRVVBUS
- 6 USB0-DP << USB0-DP
- 6 USB0-DM << USB0-DM
- 6 USB1-DP << USB1-DP
- 6 USB1-DM << USB1-DM

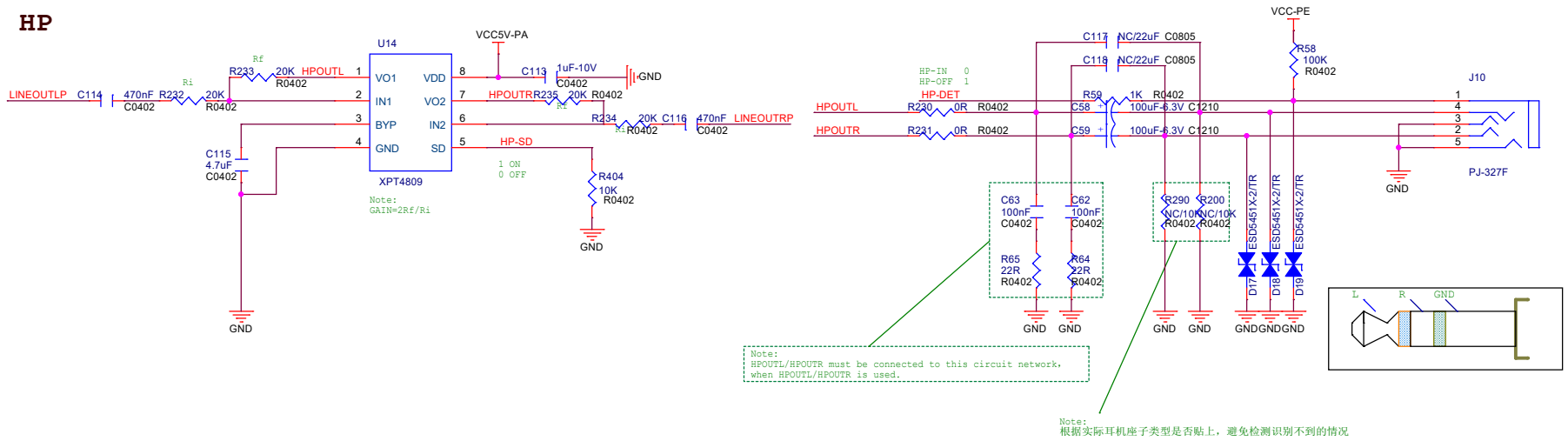


SDC0

- 7 SDC0-D1 << SDC0-D1
- 7 SDC0-D0 << SDC0-D0
- 7 SDC0-CLK << SDC0-CLK
- 7 SDC0-CMD << SDC0-CMD
- 7 SDC0-D3 << SDC0-D3
- 7 SDC0-D2 << SDC0-D2
- 7 SDC0-DET << SDC0-DET



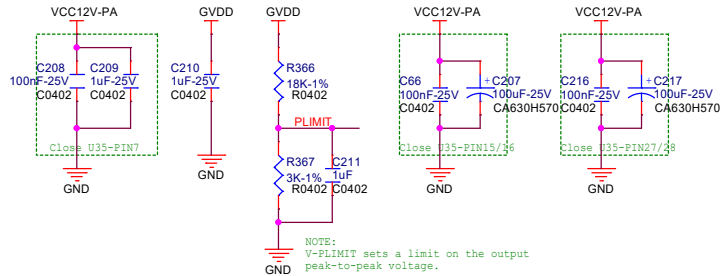
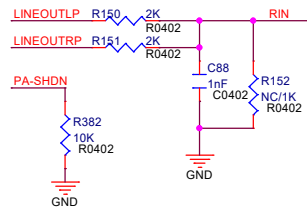
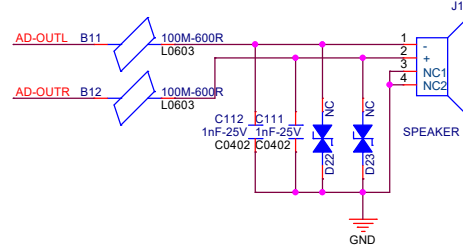
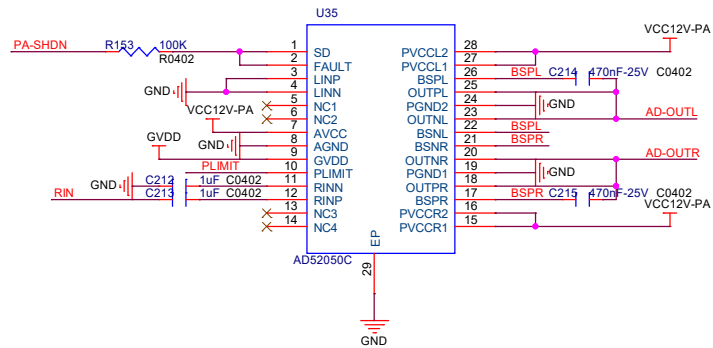
HP



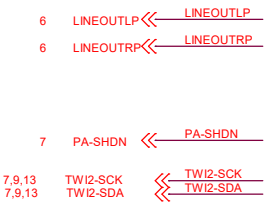
Note:
HPOUTL/HPOUTR must be connected to this circuit network,
when HPOUTL/HPOUTR is used.

Note:
根据实际耳机座子类型是否贴土，避免检测识别不到的情况

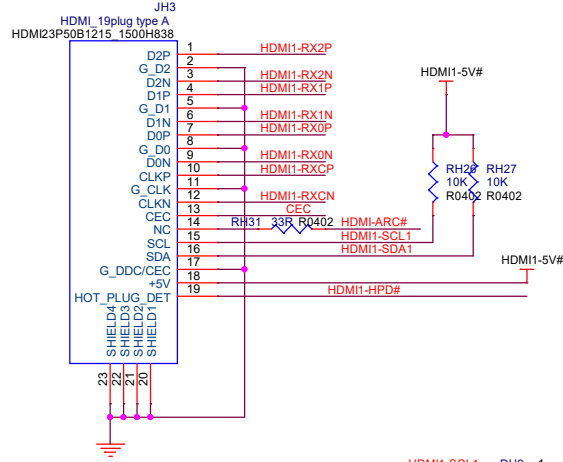
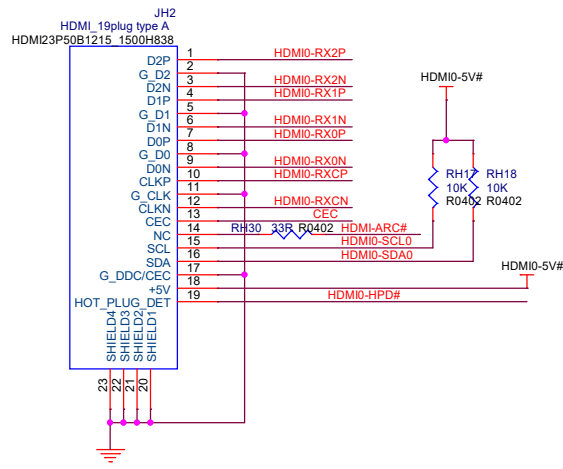
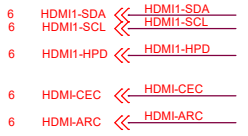
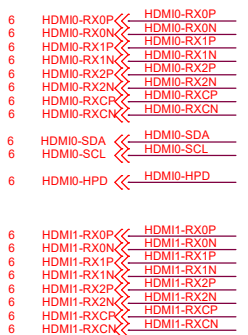
LINEOUT



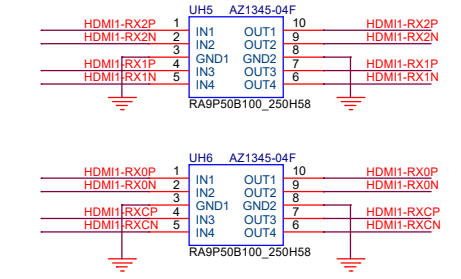
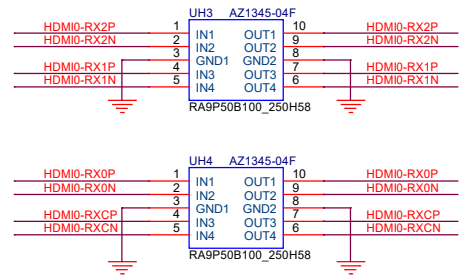
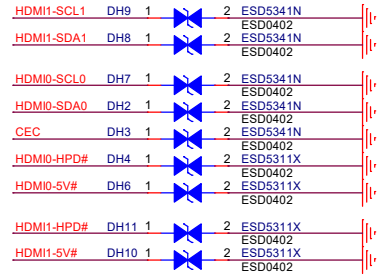
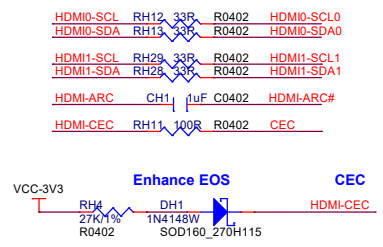
NOTE:
V-PLIMIT sets a limit on the output
peak-to-peak voltage.



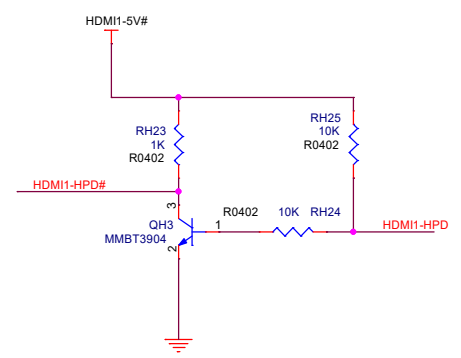
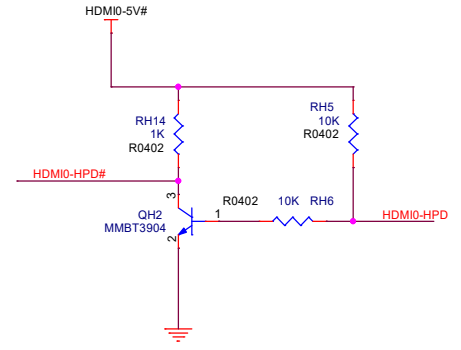
HDMI IN



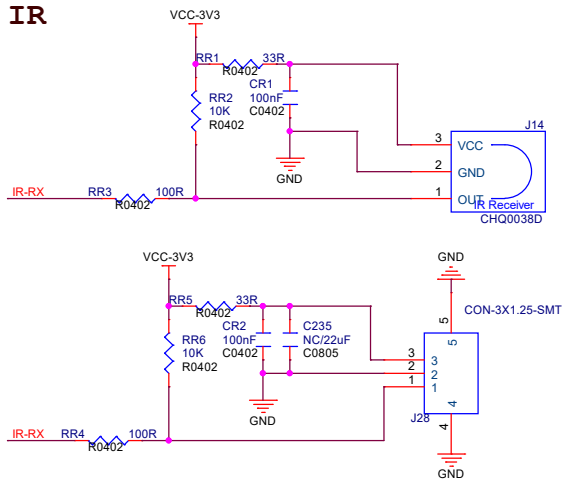
HDMI DDC/CEC/ARC



ESD器件寄生电容 0.5 pF 以下

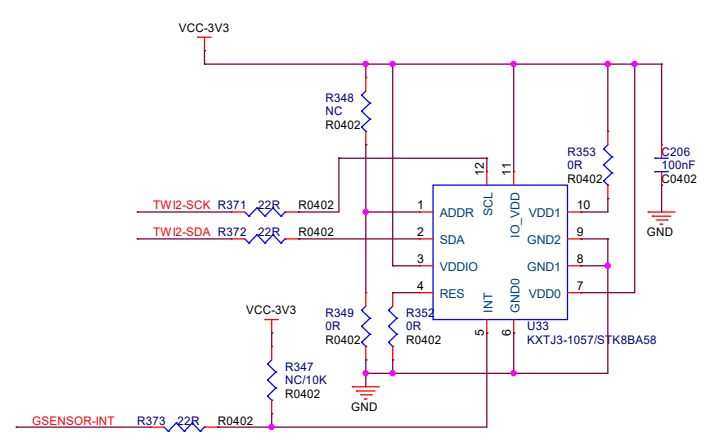


IR

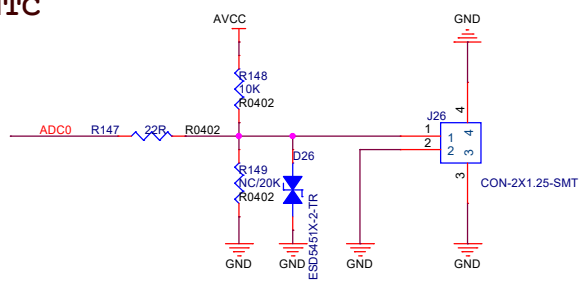


- 7 IR-RX << IR-RX
- 7,9 TWI2-SCK << TWI2-SCK
- 7,9 TWI2-SDA << TWI2-SDA
- 7 GSENSOR-INT << GSENSOR-INT

G-SENSOR

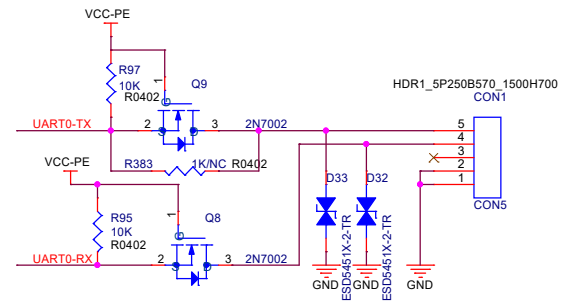


NTC

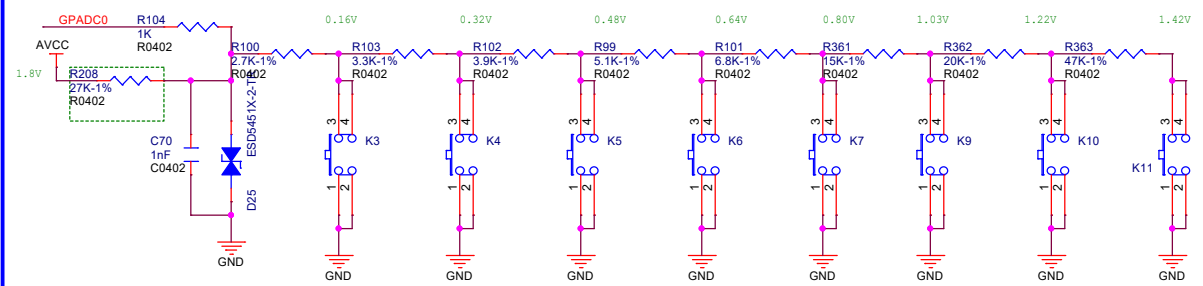


- 6 ADC0 << ADC0
- 6 GPADC0 << GPADC0

UART DEBUG



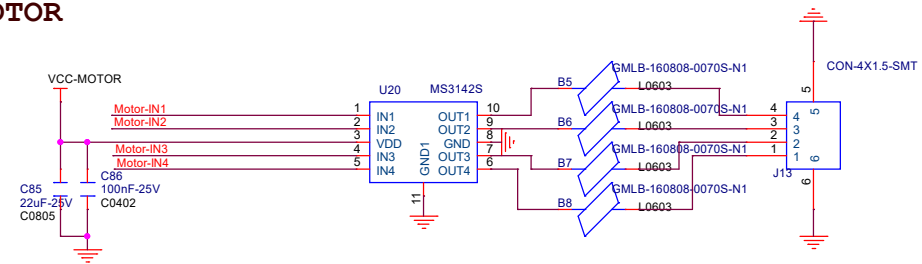
KEY



- 7 UART0-TX << UART0-TX
- 7 UART0-RX << UART0-RX

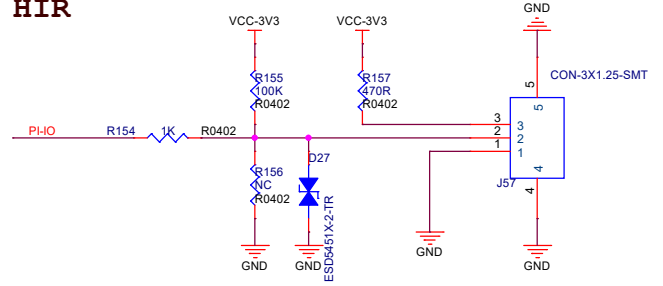
- 7 Motor-IN1 << Motor-IN1
- 7 Motor-IN2 << Motor-IN2
- 7 Motor-IN3 << Motor-IN3
- 7 Motor-IN4 << Motor-IN4

MOTOR



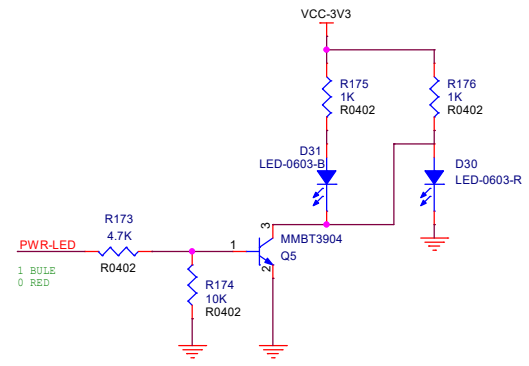
- 7 PHIO << PHIO
- 7 FAN-PWM << FAN-PWM
- 7 FAN-FG << FAN-FG

HIR

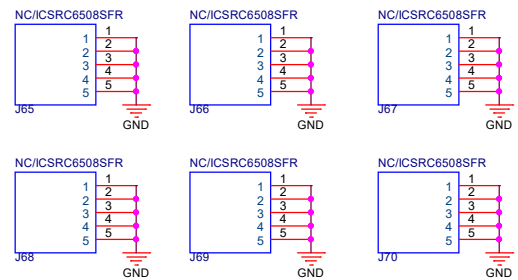


- 5,7 LEDCTL << LEDCTL
- 6 PWR-LED << PWR-LED

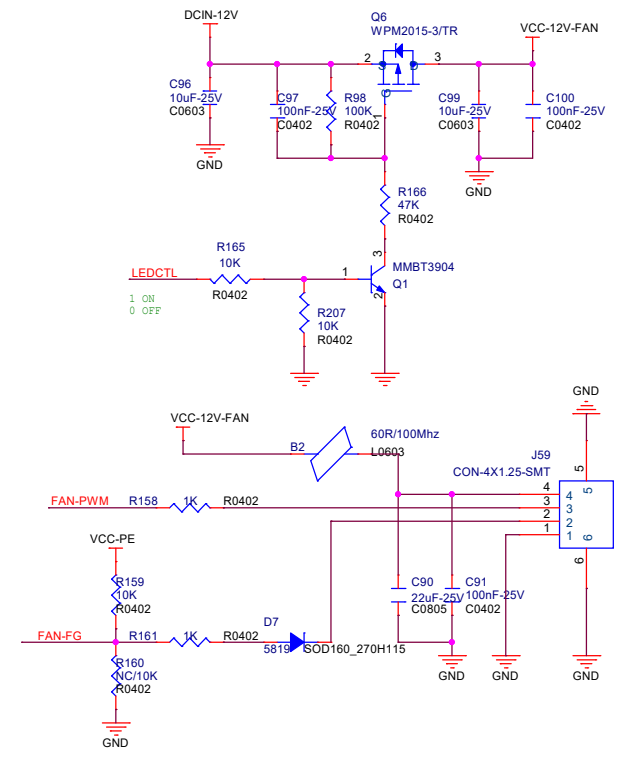
LED



SHIELD



FAN

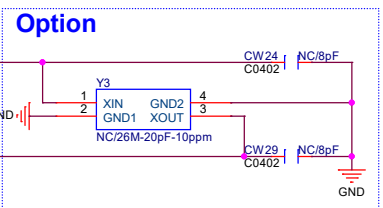
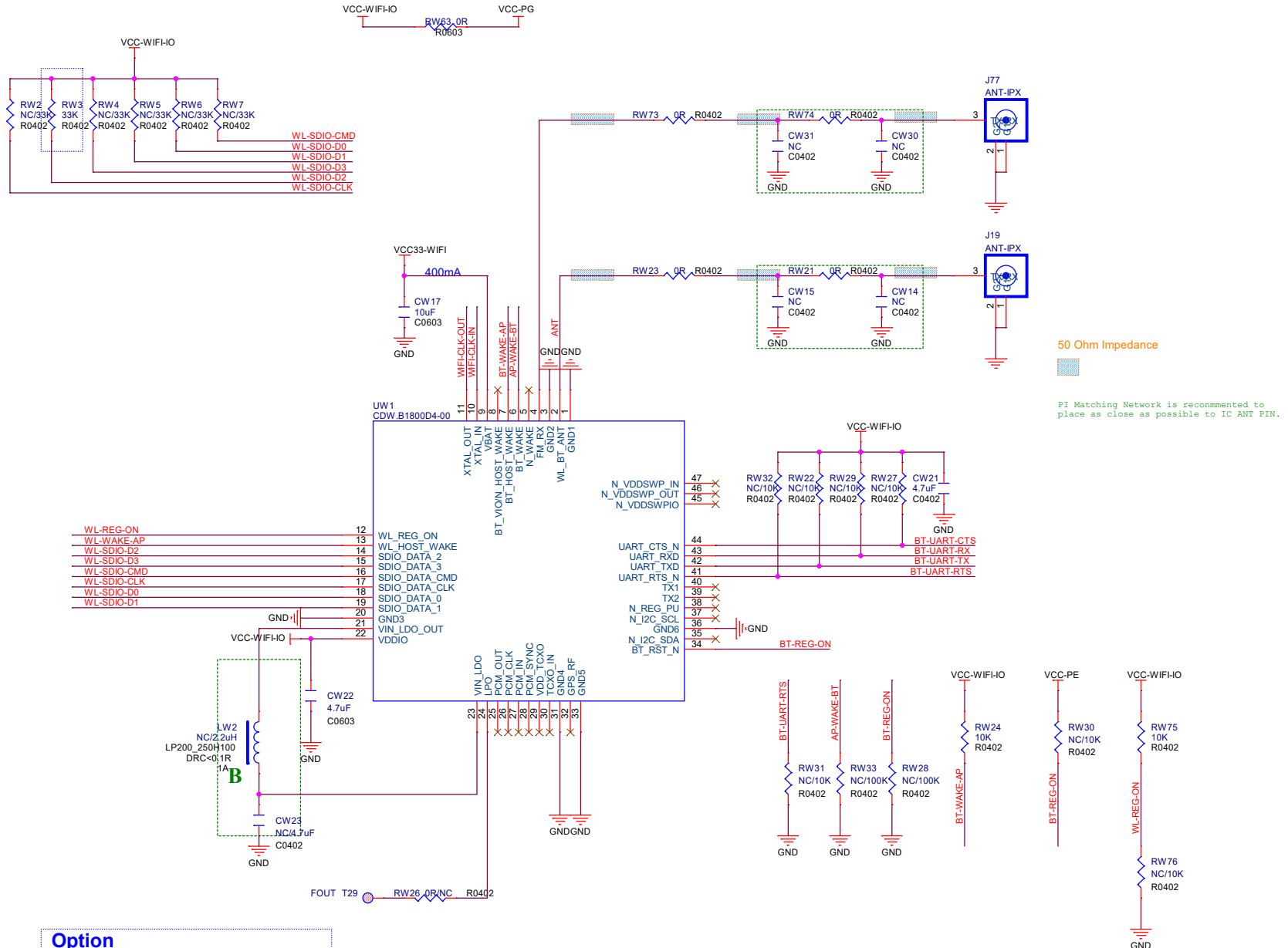


SDIO WIFI

- 7 WL-SDIO-D2 <-> WL-SDIO-D2
- 7 WL-SDIO-D3 <-> WL-SDIO-D3
- 7 WL-SDIO-CMD <-> WL-SDIO-CMD
- 7 WL-SDIO-CLK <-> WL-SDIO-CLK
- 7 WL-SDIO-D0 <-> WL-SDIO-D0
- 7 WL-SDIO-D1 <-> WL-SDIO-D1

- 7 WL-WAKE-AP <-> WL-WAKE-AP
- 7 BT-REG-ON <-> BT-REG-ON
- 7 AP-WAKE-BT <-> AP-WAKE-BT
- 7 BT-WAKE-AP <-> BT-WAKE-AP

- 7 BT-UART-RX <-> BT-UART-RX
- 7 BT-UART-TX <-> BT-UART-TX
- 7 BT-UART-RTS <-> BT-UART-RTS
- 7 BT-UART-CTS <-> BT-UART-CTS



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