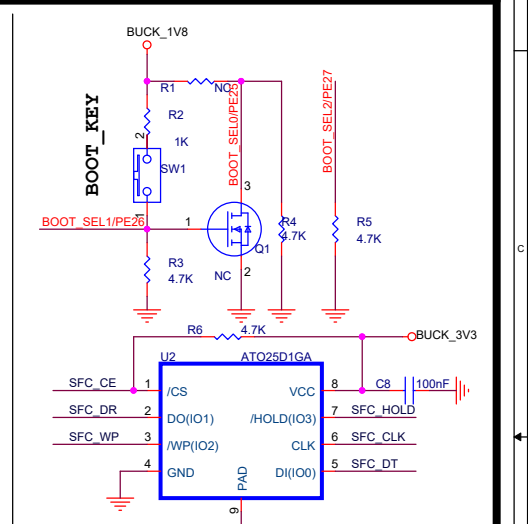
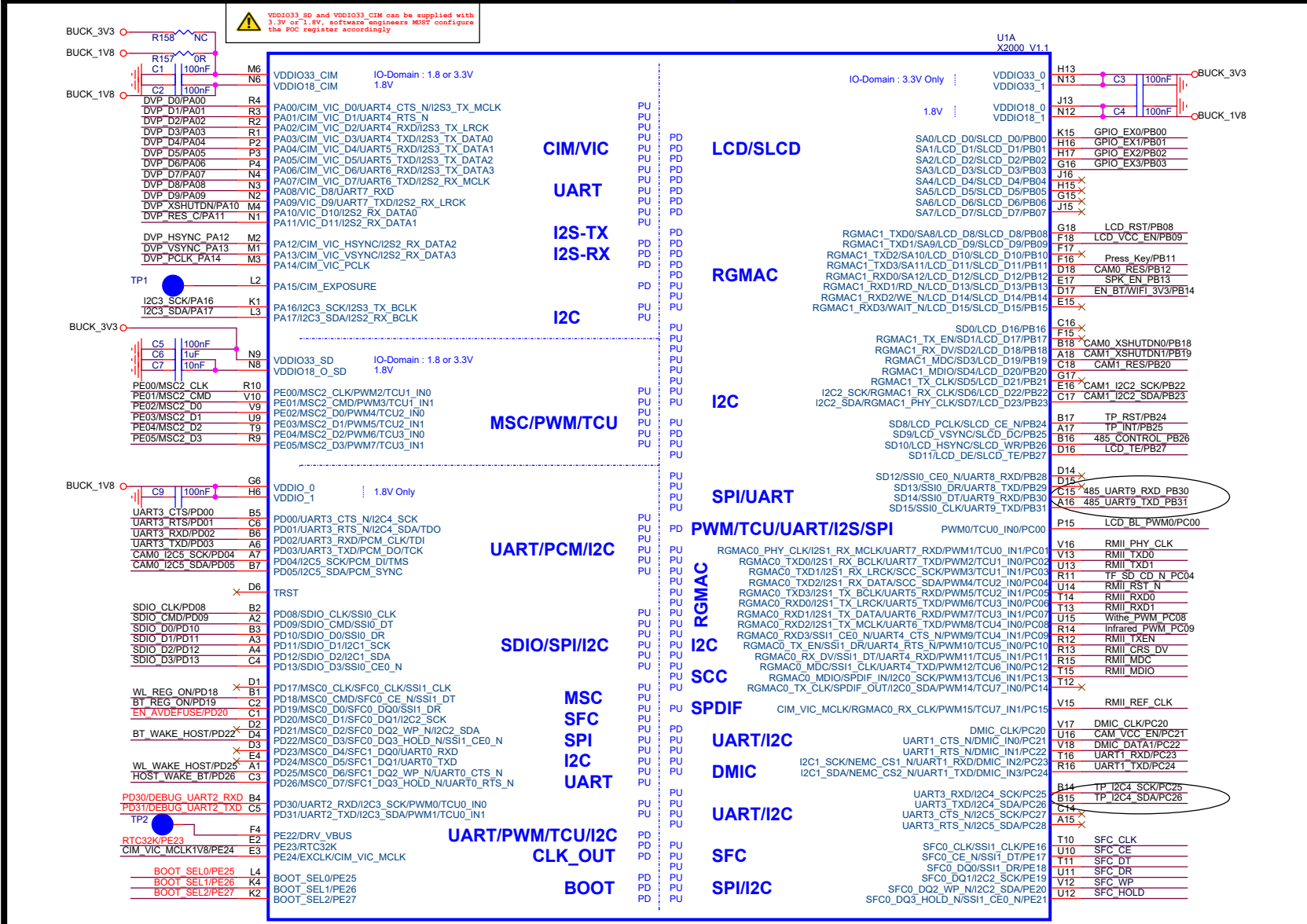
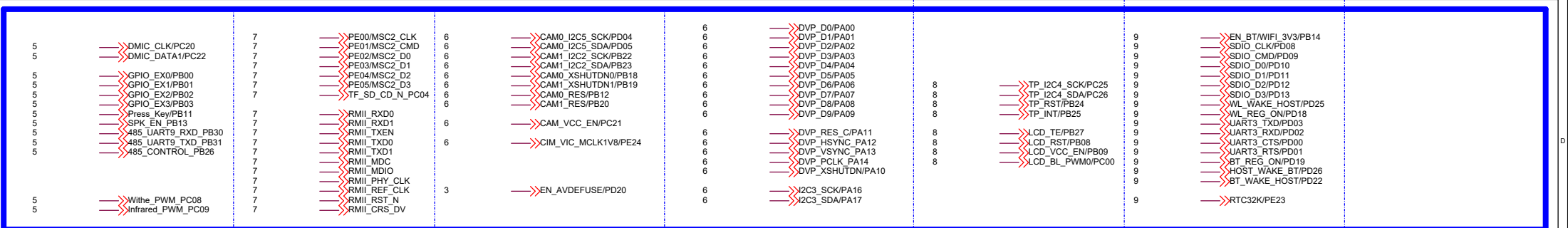
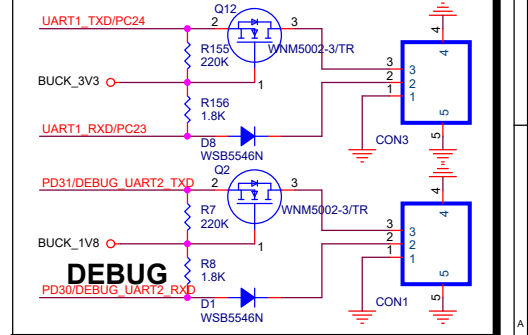


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Darwin_X2000_V2.1.1		
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A3	System_Diagram	V2.1.1
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BOOT CONFIGURATION				
BOOT_SEL2	BOOT_SEL1	BOOT_SEL0	BOOT_MODE	
0	0	0	SFC0@PE3.3V	
0	0	1	MSC2@PE[00:05].3.3V	
X	1	0	USB (DOWNLOAD)	



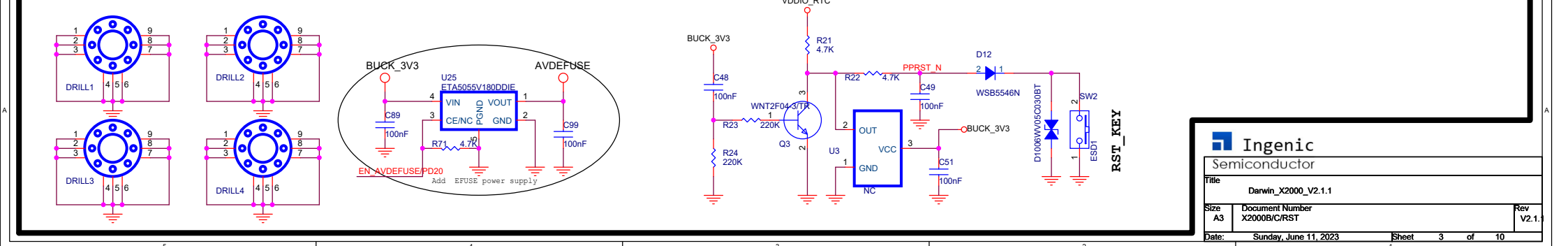
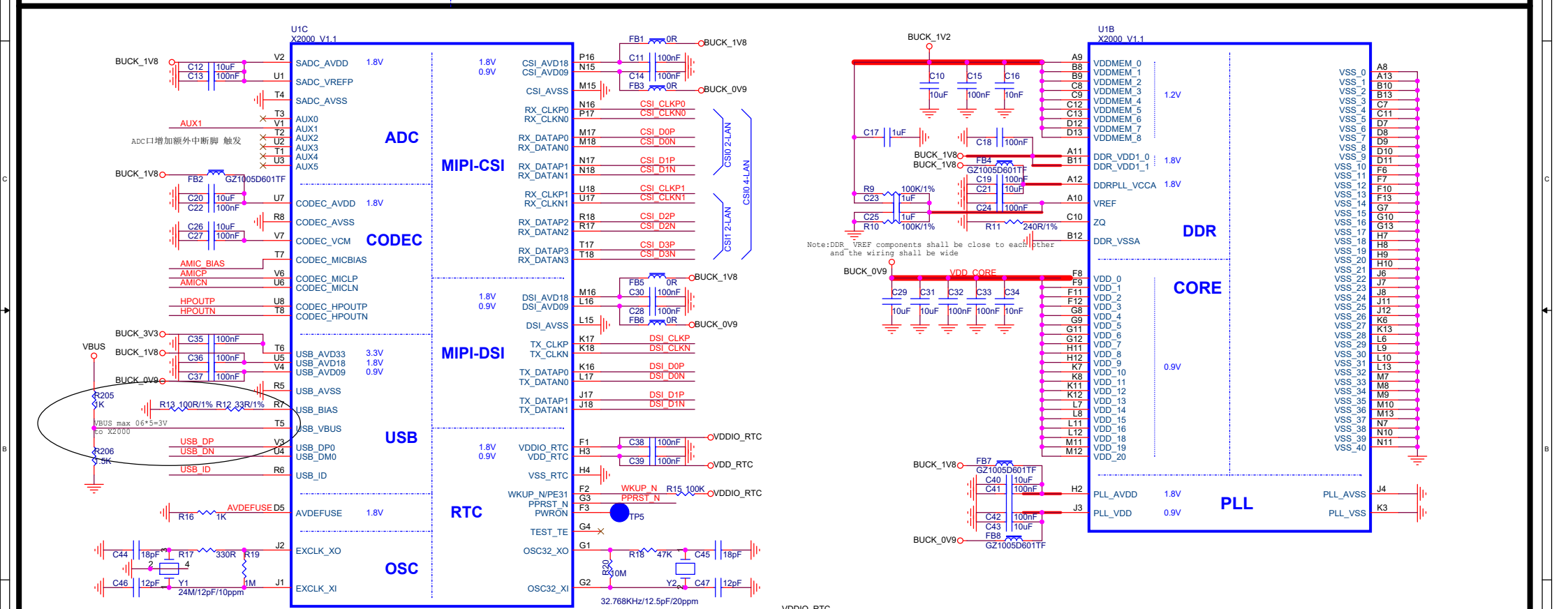
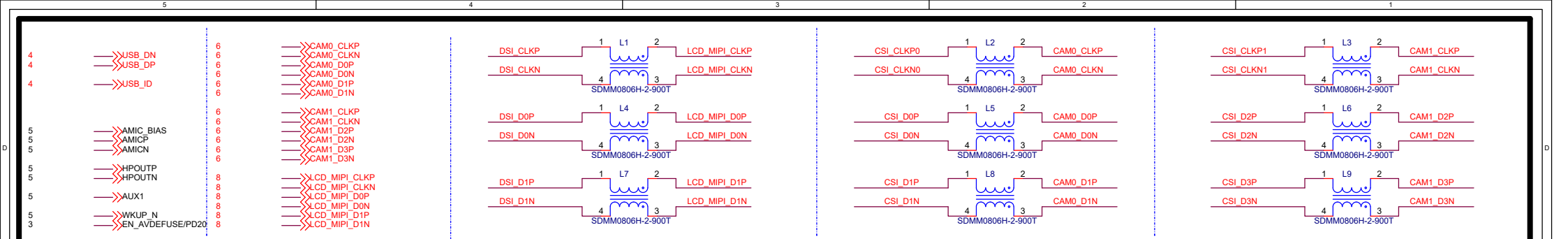
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Darwin_X2000_V2.1.1

Title: Darwin_X2000_V2.1.1

Size A3 Document Number X2000A/DEBUG/FLASH Rev 2.1.1

Date: Sunday, June 11, 2023 Sheet 2 of 10

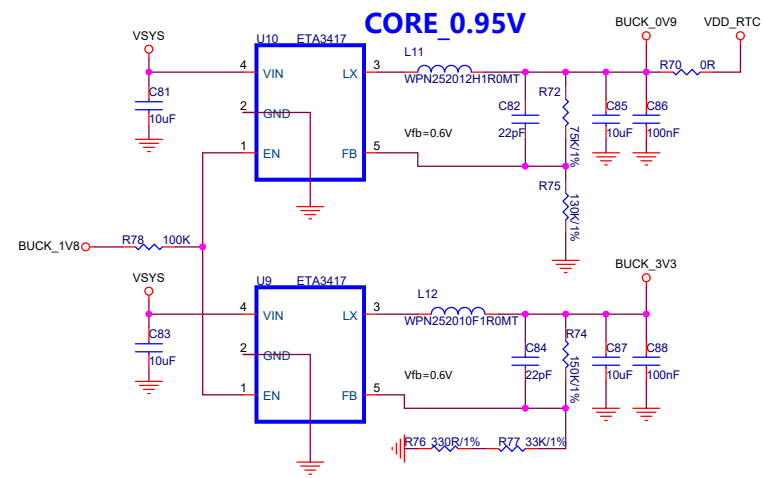
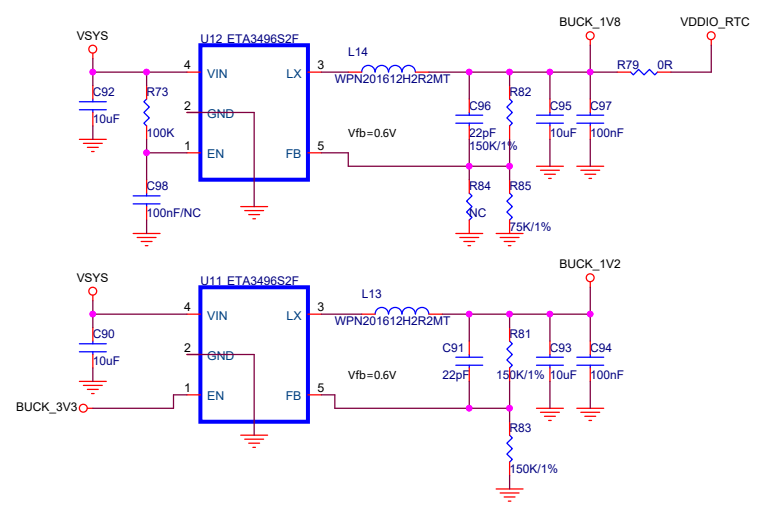
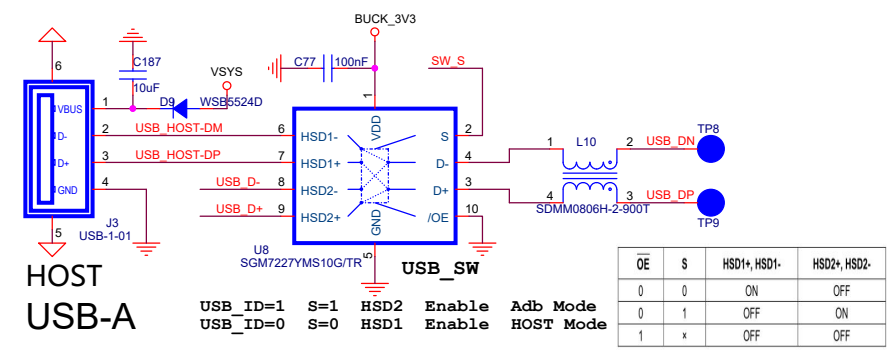
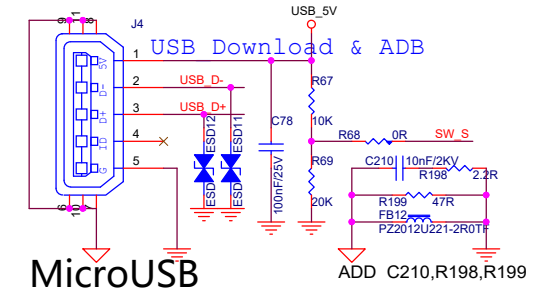
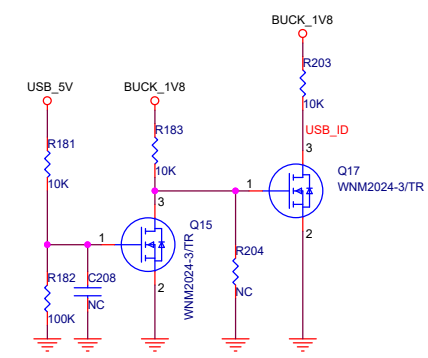
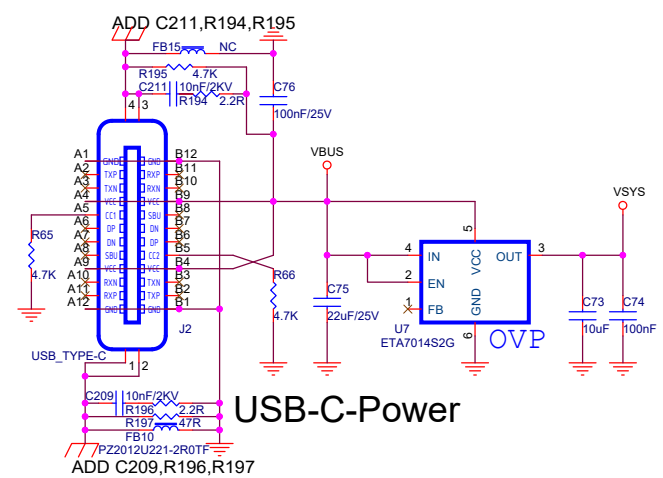
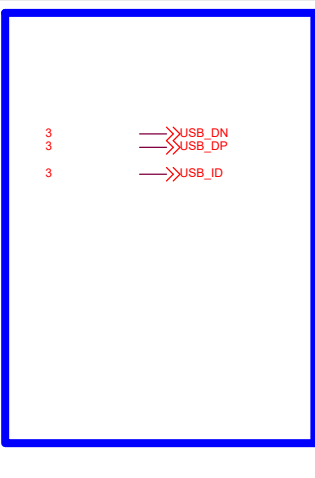


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Darwin_X2000_V2.1.1

Size A3 Document Number X2000B/C/RST Rev V2.1.1

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BUCK_1V8 > BUCK_3V3 & BUCK_0.9V > BUCK_1V2

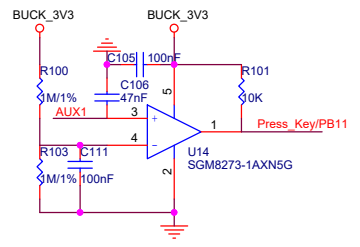
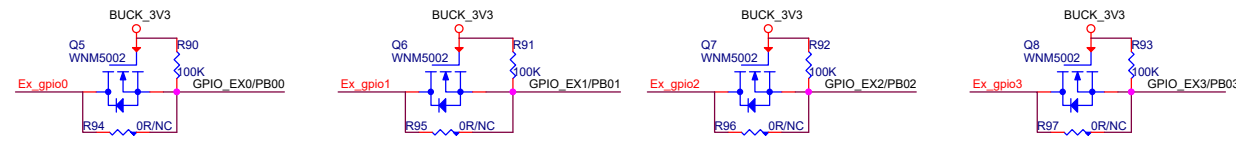
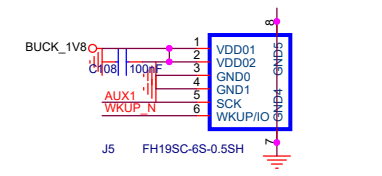
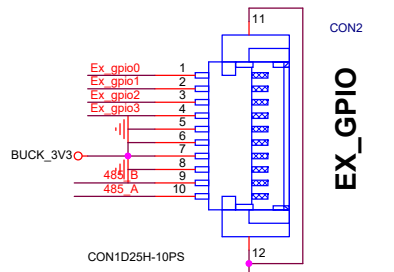
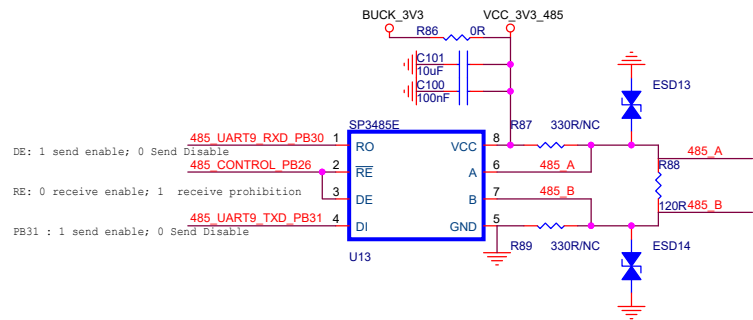
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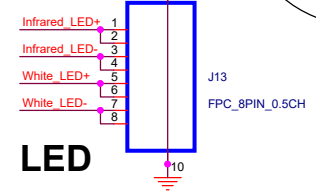
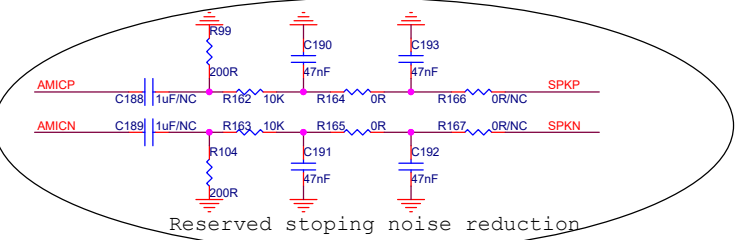
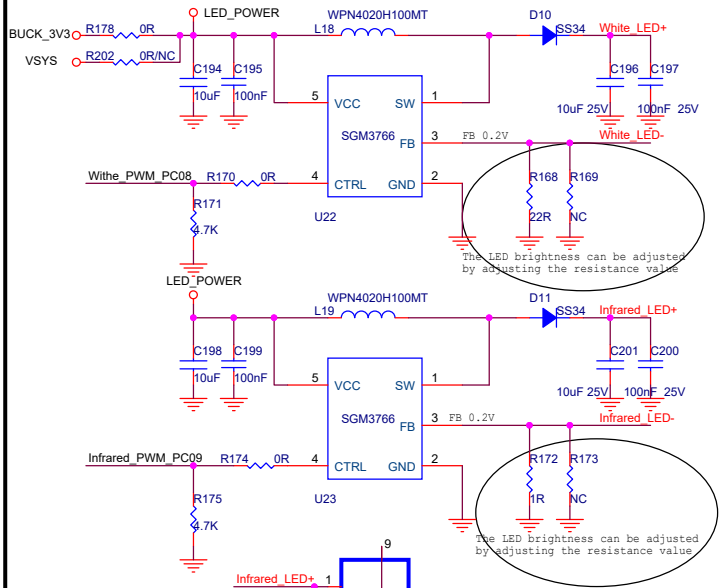
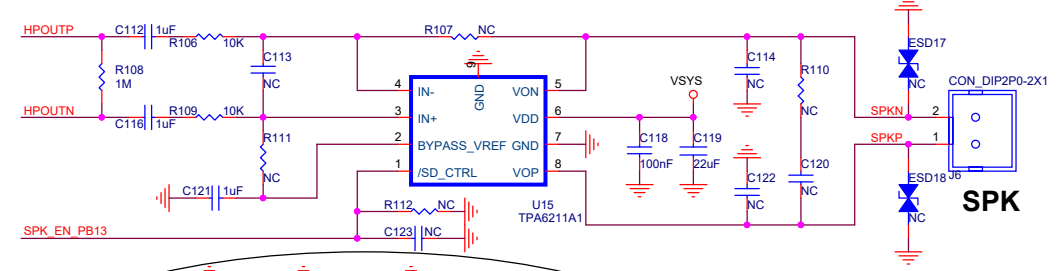
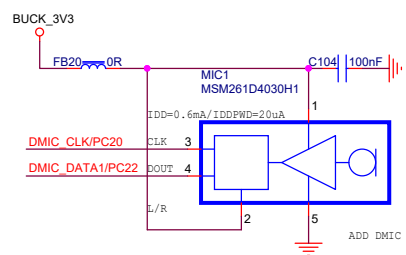
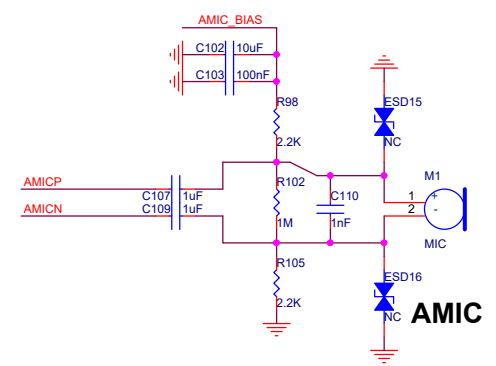
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- 3 >>> AMIC_BIAS
- 3 >>> AMICP
- 3 >>> AMICN
- 3 >>> HPOUTP
- 3 >>> HPOUTN
- 2 >>> SPK_EN_PB13
- 2 >>> 485_UART9_RXD_PB30
- 2 >>> 485_UART9_TXD_PB31
- 2 >>> 485_CONTROL_PB26
- 2 >>> DMIC_CLK/PC20
- 2 >>> DMIC_DATA1/PC22
- 2 >>> GPIO_EX0/PB00
- 2 >>> GPIO_EX1/PB01
- 2 >>> GPIO_EX2/PB02
- 2 >>> GPIO_EX3/PB03
- 3 >>> AUX1
- 2 >>> Press_Key/PB11
- 3 >>> WKUP_N
- 2 >>> Withe_PWM_PC08
- 2 >>> Infrared_PWM_PC09



ADC_KEY



LED

Ingenic		
Semiconductor		
Title	Darwin_X2000_V2.1.1	
Size	Document Number	Rev
A3	MIC/SPK/EP/ADC_KEYLED/485	V2.1.1
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- >> DVP_D0/PA00
- >> DVP_D1/PA01
- >> DVP_D2/PA02
- >> DVP_D3/PA03
- >> DVP_D4/PA04
- >> DVP_D5/PA05
- >> DVP_D6/PA06
- >> DVP_D7/PA07
- >> DVP_D8/PA08
- >> DVP_D9/PA09

- >> DVP_RES_C/PA11
- >> DVP_HSYNC_PA12
- >> DVP_VSYNC_PA13
- >> DVP_PCLK_PA14
- >> DVP_XSHUTDN/PA10

- >> I2C3_SCK/PA16
- >> I2C3_SDA/PA17

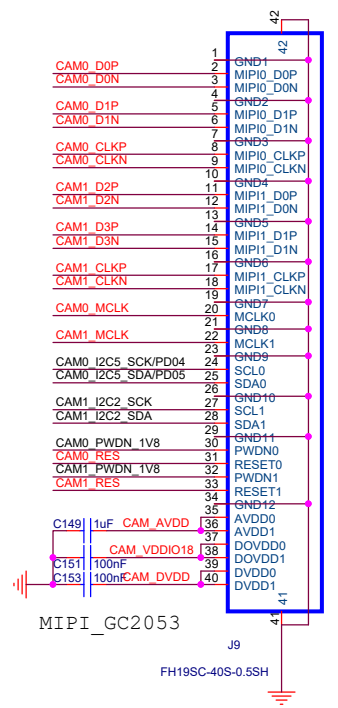
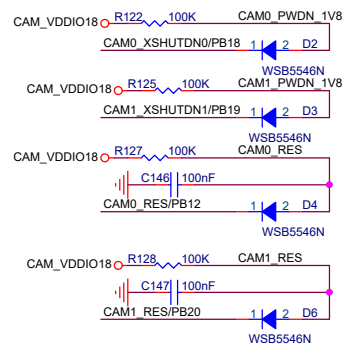
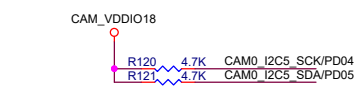
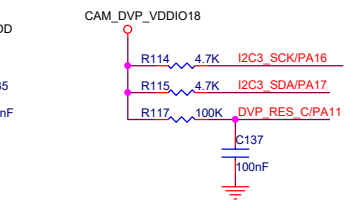
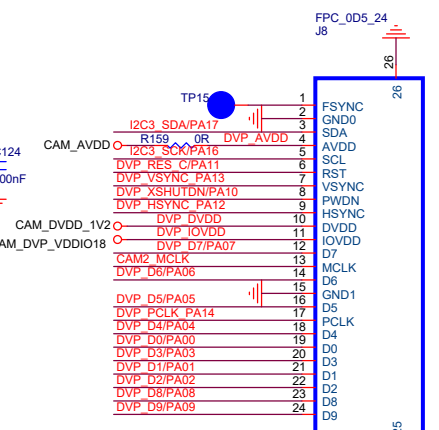
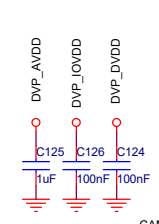
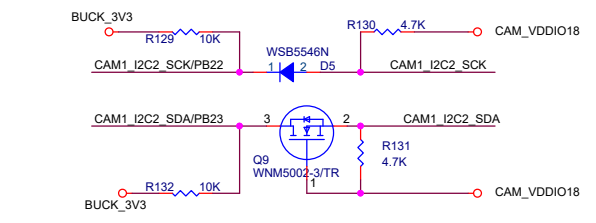
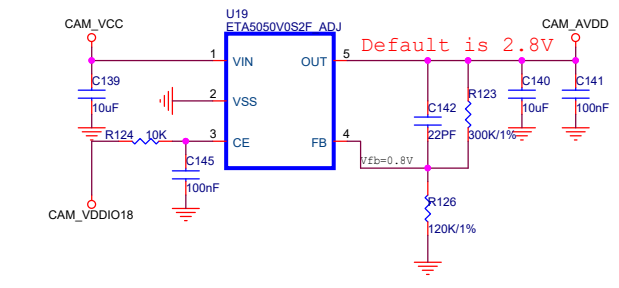
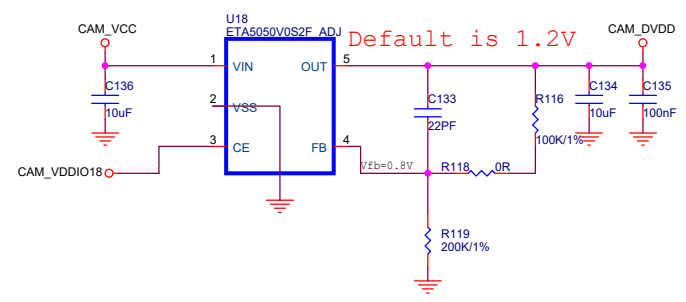
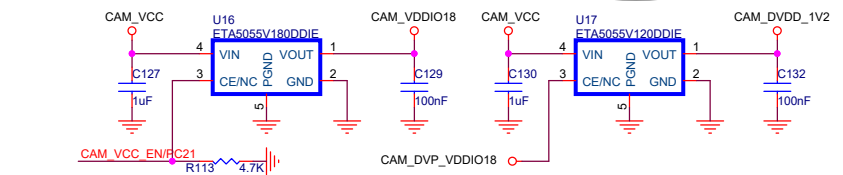
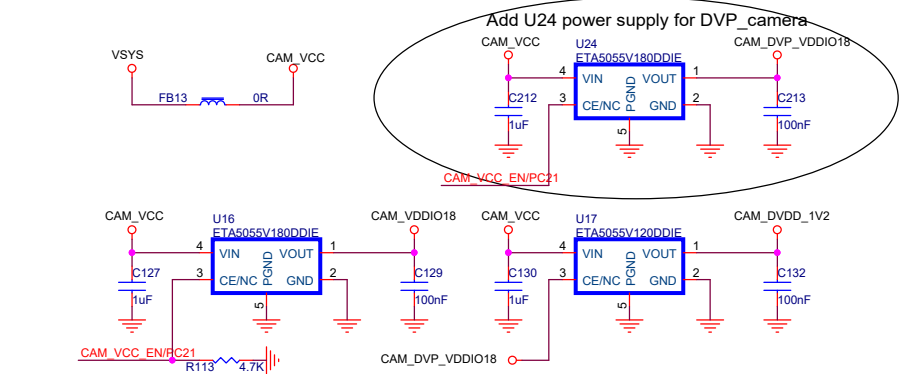
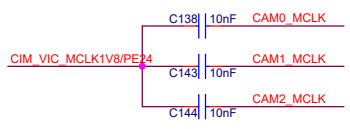
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- >> CAM1_I2C2_SDA/PB23

- >> CAM0_XSHUTDN/PB18
- >> CAM1_XSHUTDN1/PB19
- >> CAM0_RES/PB12
- >> CAM1_RES/PB20

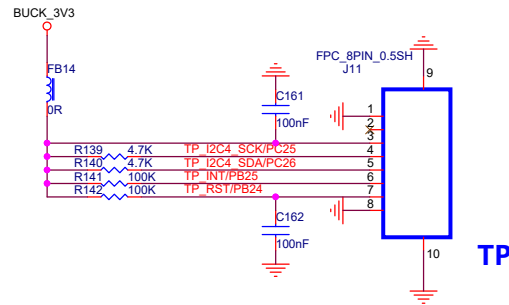
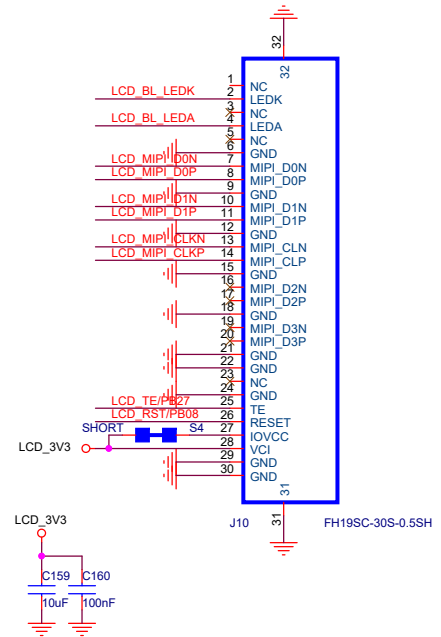
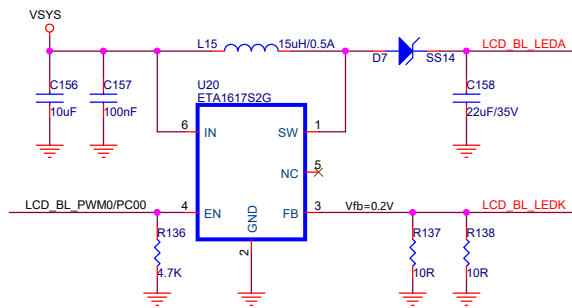
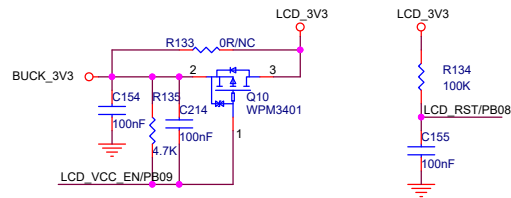
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- >> CAM0_D0P
- >> CAM0_D0N
- >> CAM0_D1P
- >> CAM0_D1N

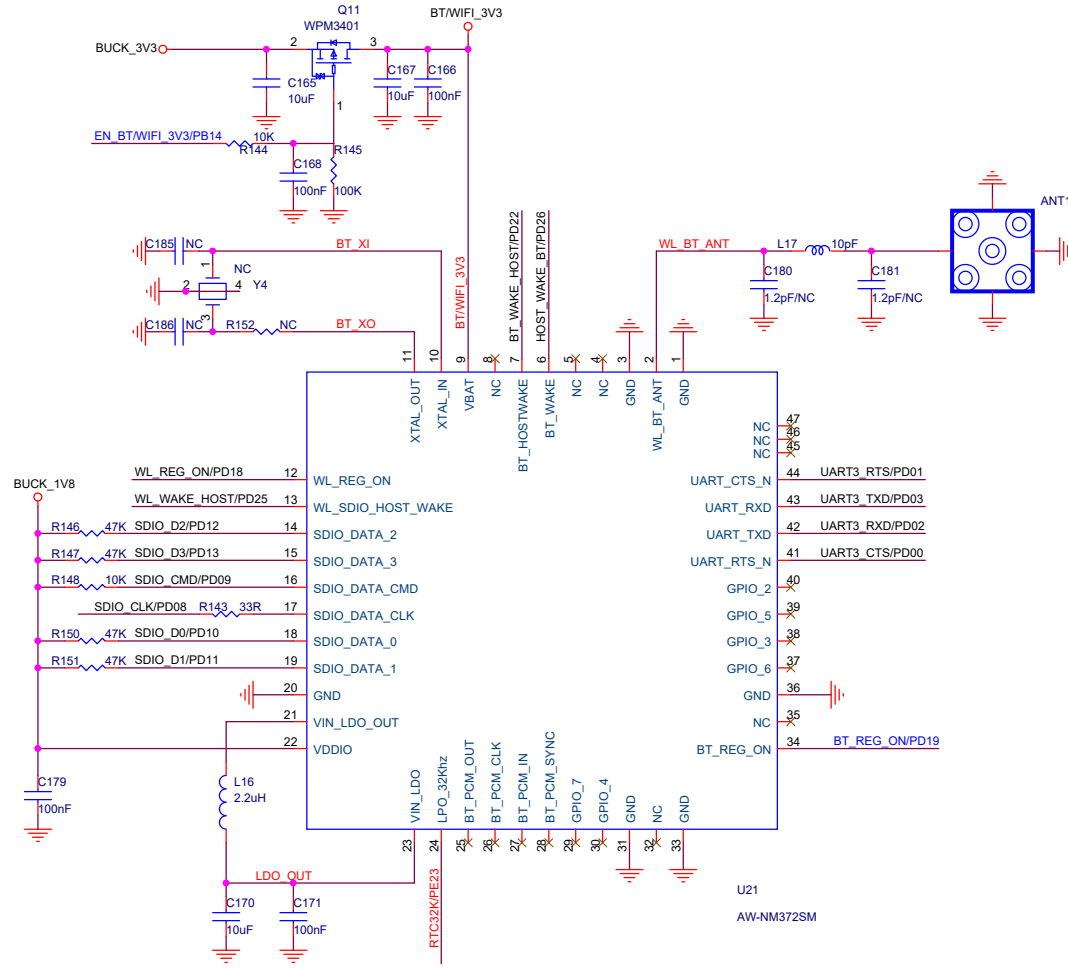
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- >> CAM1_D2P
- >> CAM1_D2N
- >> CAM1_D3P
- >> CAM1_D3N



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- 3 >>> LCD_MIPI_CLKN 2
- 3 >>> LCD_MIPI_D0P 2
- 3 >>> LCD_MIPI_D0N 2
- 3 >>> LCD_MIPI_D1P 2
- 3 >>> LCD_MIPI_D1N 2
- 2 >>> TP_I2C4_SCK/PC25 2
- 2 >>> TP_I2C4_SDA/PC26 2
- 2 >>> TP_RST/PB24 2
- 2 >>> TP_INT/PB25 2
- 2 >>> LCD_VCC_EN/PB09 2
- 2 >>> LCD_BL_PWM0/PC00 2
- 2 >>> LCD_RST/PB08 2
- 2 >>> LCD_TE/PB27 2



- 2 EN_BT/WIFI_3V3/PB14
- 2 SDIO_CLK/PD08
- 2 SDIO_CMD/PD09
- 2 SDIO_D0/PD10
- 2 SDIO_D1/PD11
- 2 SDIO_D2/PD12
- 2 SDIO_D3/PD13
- 2 WL_WAKE_HOST/PD25
- 2 WL_REG_ON/PD18
- 2 UART3_TXD/PD03
- 2 UART3_RXD/PD02
- 2
- 2 UART3_RTS/PD01
- 2 BT_REG_ON/PD19
- 2 HOST_WAKE_BT/PD26
- 2 BT_WAKE_HOST/PD22
- 2
- 2 RTC32K/PE23
- 2 UART3_CTS/PD00



Date	Revision	Change
2021 12 12	Rev2.0	1. 初版
2022 04 06	Rev2.1	1. Update power on sequence. VCC_1V8 > VCC_3V3 & VCC_CORE> VCC_1V2 2. SP3485E_uart3 is change to uart9 3. TP_I2C is change to I2C4 4. Modify CON3 interface and change UART1_TXD/PC24 and UART1_RXD/PC23 exchange
2023 03 10	Rev2.1.1	1. Add U24 power supply for DVP_camera 2. J12 is modified to DMIC 3. Add EFUSE power supply 4. Update LED power input, compatible with 3.3V 5 Update control circuit design for USB_ID 6. Add R205 R206 for USB_VBUS power