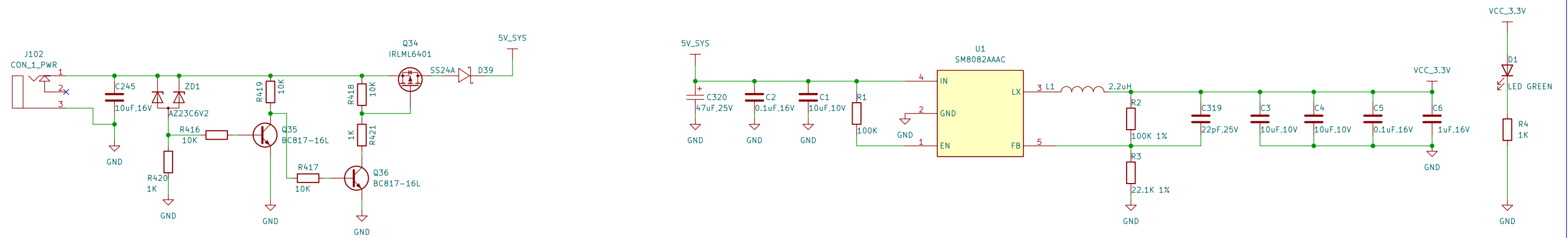
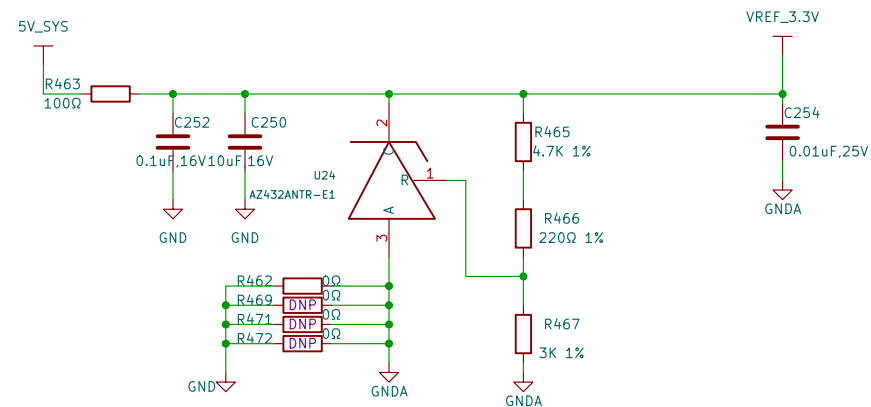
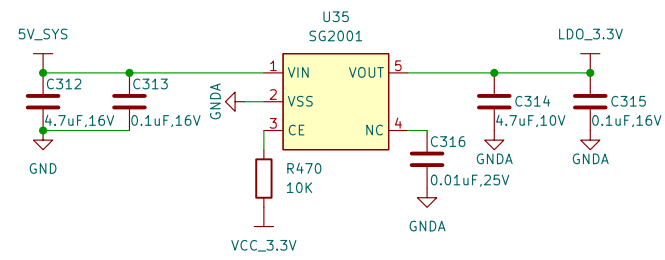




DCDC



LDO 3.3V



02_POWER



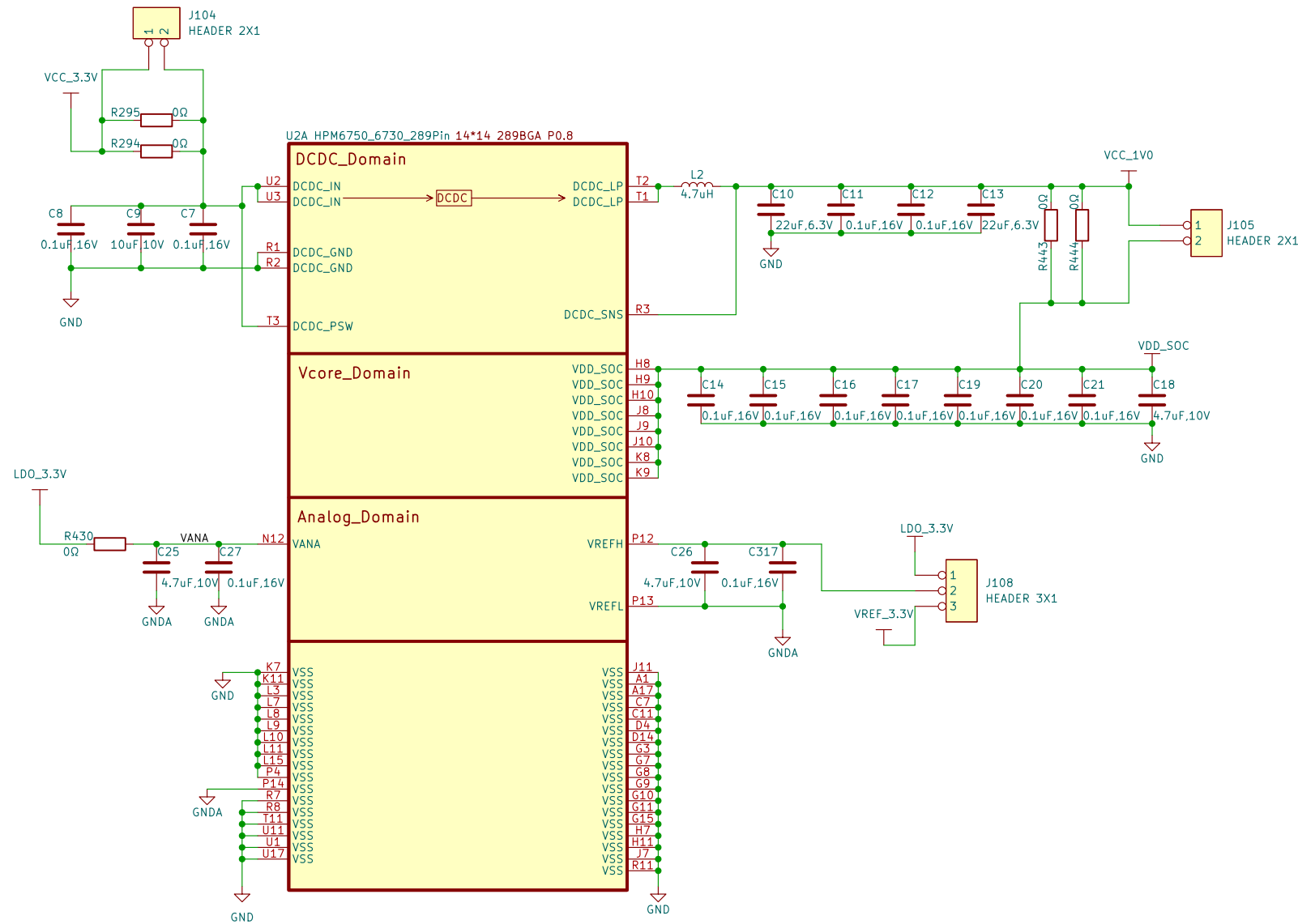
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File: POWER.kicad_sch

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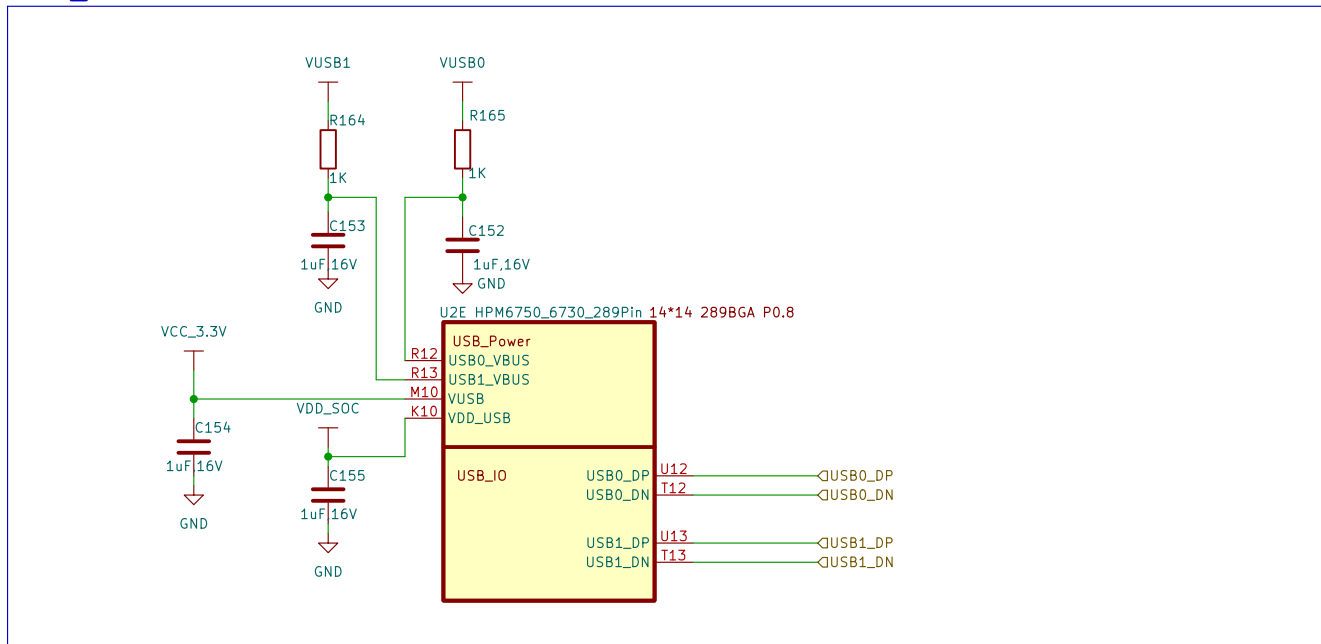
Size: A3 Date:
KiCad E.D.A. kicad (6.0.4)

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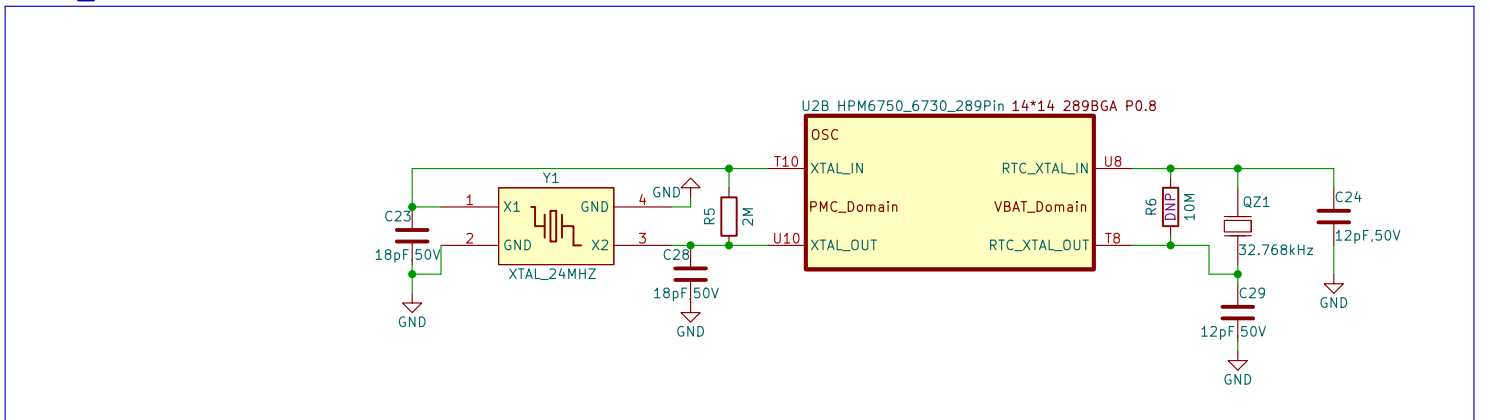
PWR_Domain




USB_Domain

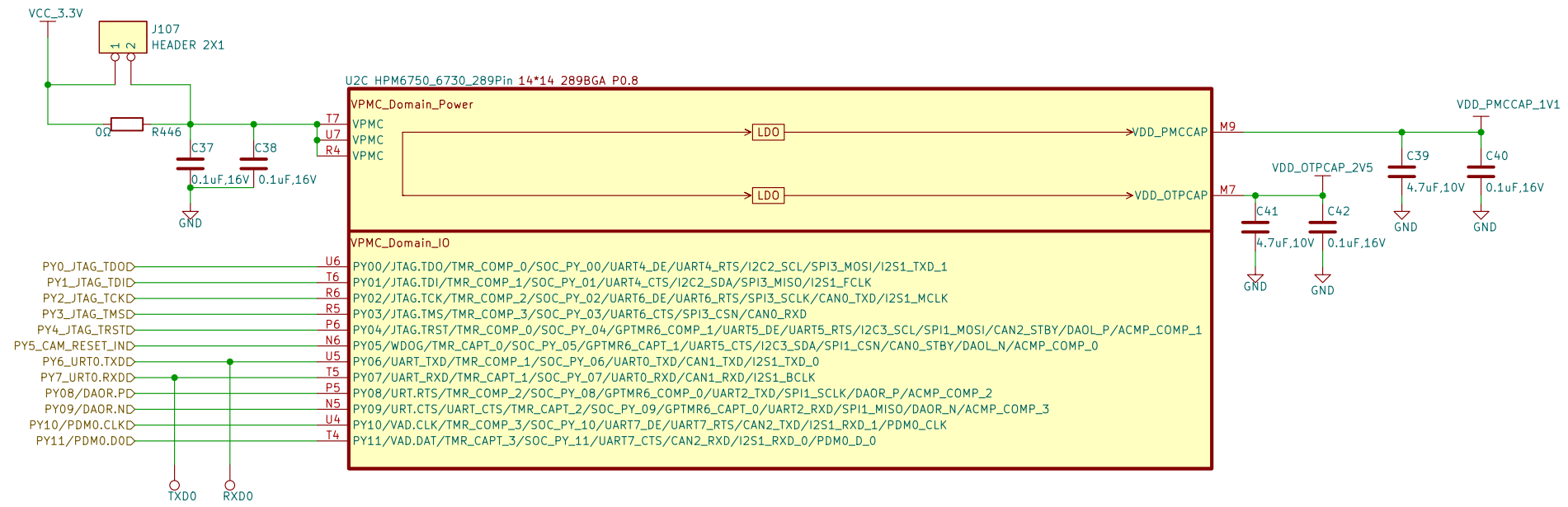


OSC_Domain

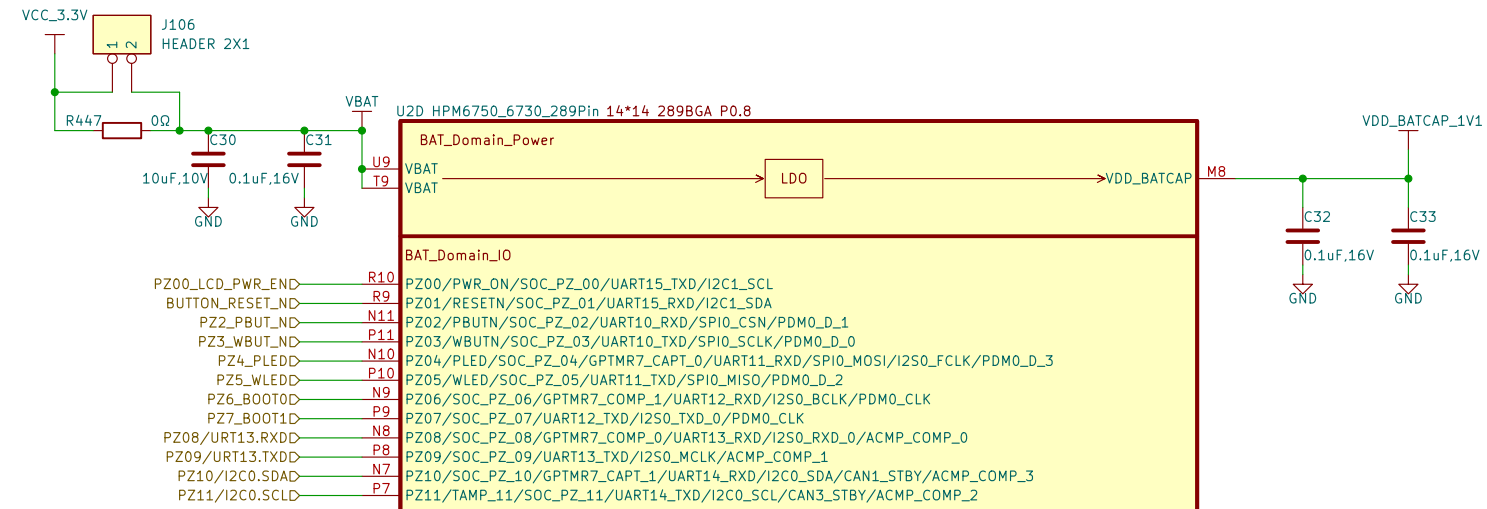


03_HPM6750_04 

BAT_Domain



VPMC_Domain



04_HPM6750-05



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Size: A3

Date:

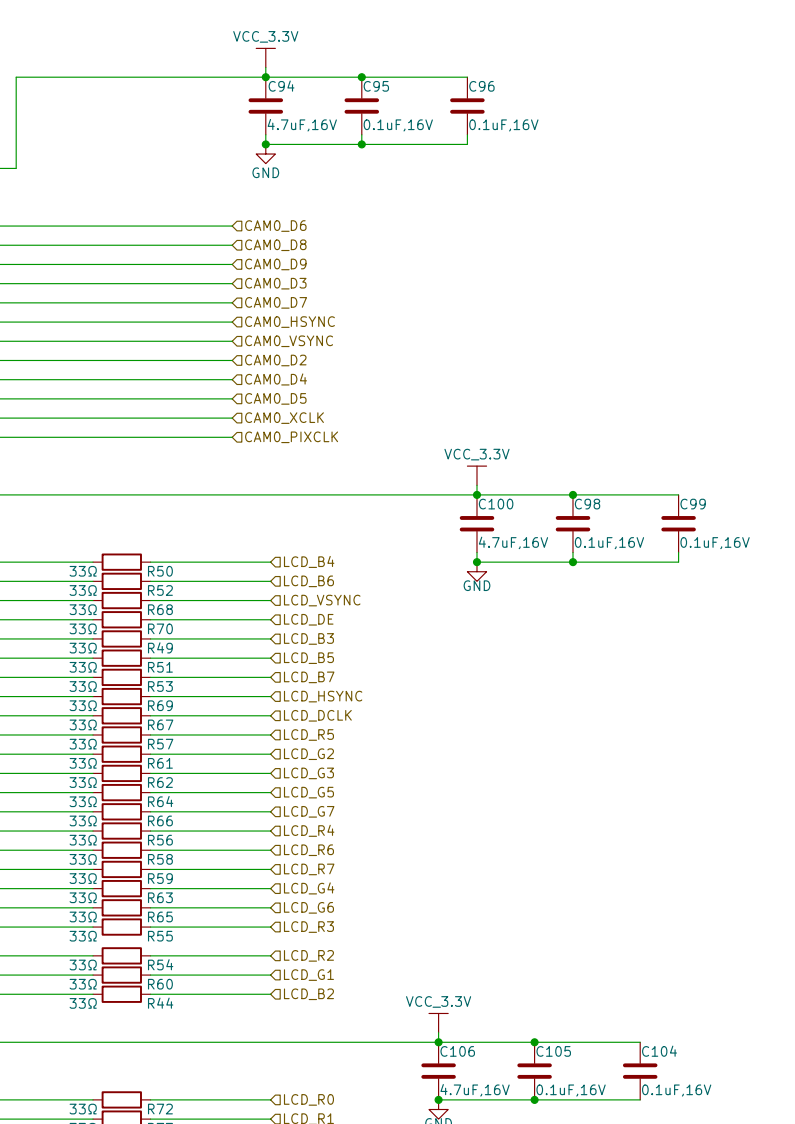
Rev:

KiCad E.D.A. kicad (6.0.4)

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14*14 289BGA P0.8 HPM6750-6730-289Pin U2H

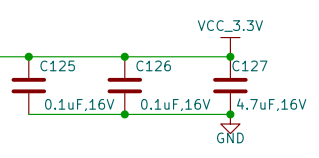
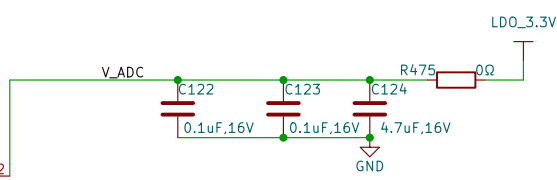
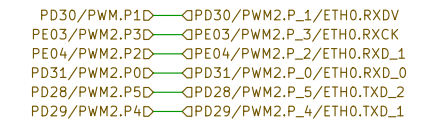
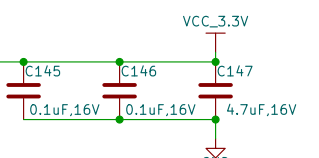
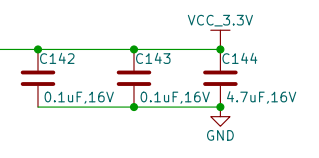
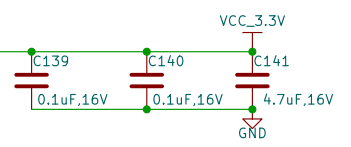
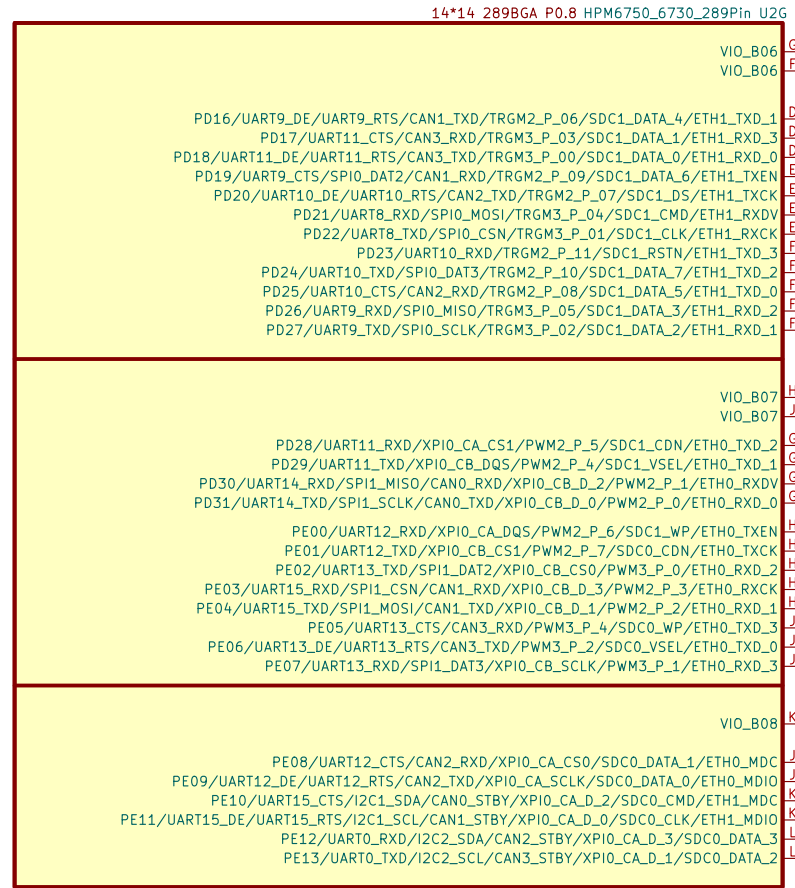
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VIO_B00	L6				
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PA01/UART1_RXD/SPI0_SCLK/CAN3_RXD/I2S1_TXD_1/CAM0_D_8	P2				QCAM0_D8
PA02/UART1_TXD/SPI0_MISO/CAN3_TXD/I2S1_TXD_0/CAM0_D_9	P1				QCAM0_D9
PA03/GPTMR1_COMP_1/UART3_RXD/I2S1_TXD_3/I2S2_BCLK/CAM0_D_3	N4				QCAM0_D3
PA04/GPTMR1_COMP_0/UART4_TXD/SPI0_DAT2/I2S1_MCLK/I2S2_MCLK/CAM0_D_7	N3				QCAM0_D7
PA05/GPTMR0_COMP_1/UART6_RXD/I2C0_SDA/SPI0_CSN/I2S1_BCLK/DAOR_P/CAM0_HSYNC	N2				QCAM0_HSYNC
PA06/GPTMR0_COMP_0/UART6_TXD/I2C0_SCL/SPI0_MOSI/I2S1_FCLK/DAOR_P/CAM0_VSYNC	N1				QCAM0_VSYNC
PA07/GPTMR1_COMP_1/UART5_RXD/I2S1_MCLK/CAM0_D_2/SOC_REF1	M5				QCAM0_D2
PA08/GPTMR1_COMP_0/UART5_TXD/I2S1_RXD_3/I2S2_FCLK/CAM0_D_4	M4				QCAM0_D4
PA09/UART4_RXD/I2S1_RXD_2/I2S2_RXD_0/CAM0_D_5/SOC_REF0	M3				QCAM0_D5
PA10/GPTMR0_COMP_1/UART7_RXD/I2C1_SDA/CAN3_STBY/I2S1_RXD_1/DAOR_N/CAM0_XCLK	M2				QCAM0_XCLK
PA11/GPTMR0_COMP_0/UART7_TXD/I2C1_SCL/CAN2_STBY/I2S1_RXD_0/DAOR_N/CAM0_PIXCLK	M1				QCAM0_PIXCLK
VIO_B01	K6				
VIO_B01	J6				
PA12/GPTMR1_CAPT_1/UART9_CTS/I2C3_SDA/SPI1_DAT3/I2S1_RXD_3/DISO_B_4/CAM0_PIXCLK	L5				QLCD_B4
PA13/GPTMR1_CAPT_0/UART9_RTS/I2C3_SCL/SPI1_DAT2/I2S1_RXD_2/DISO_B_6/CAM0_HSYNC	L4	33Ω	R50		QLCD_B6
PA14/GPTMR0_CAPT_1/UART8_CTS/I2C2_SDA/CAN1_STBY/I2S2_TXD_3/I2S1_TXD_3/DISO_VSYNC	L2	33Ω	R52		QLCD_VSYNC
PA15/GPTMR0_CAPT_0/UART8_DE/UART8_RTS/I2C2_SCL/CAN0_STBY/I2S2_TXD_2/I2S1_TXD_2/DISO_EN	L1	33Ω	R68		QLCD_DE
PA16/GPTMR1_CAPT_1/UART10_CTS/SPI1_MOSI/CAN0_RXD/I2S1_RXD_1/DISO_B_3/CAM0_XCLK	K5	33Ω	R70		QLCD_B3
PA17/GPTMR1_CAPT_0/UART10_DE/UART10_RTS/CAN0_TXD/I2S1_RXD_0/DISO_B_5/CAM0_VSYNC	K4	33Ω	R49		QLCD_B5
PA18/UART10_TXD/SPI1_CSN/I2S1_BCLK/DISO_B_7/CAM0_D_4	K3	33Ω	R51		QLCD_B7
PA19/GPTMR0_CAPT_1/UART11_CTS/CAN1_RXD/I2S2_TXD_1/I2S1_TXD_1/PWM1_P_7/DISO_HSYNC	K2	33Ω	R53		QLCD_HSYNC
PA20/GPTMR0_CAPT_0/UART11_DE/UART11_RTS/CAN1_TXD/I2S2_TXD_0/I2S1_TXD_0/PWM1_P_6/DISO_CLK	K1	33Ω	R69		QLCD_DCLK
PA21/UART11_RXD/SPI1_SCLK/I2S3_BCLK/DAOR_P/PWM0_P_7/DISO_R_5	J5	33Ω	R67		QLCD_R5
PA22/UART11_TXD/I2S1_FCLK/DAOR_P/PWM0_P_6/DISO_G_2/CAM0_D_2	J4	33Ω	R57		QLCD_G2
PA23/UART10_RXD/SPI1_MISO/I2S2_MCLK/PWM0_P_1/DISO_G_3/CAM0_D_3	J3	33Ω	R61		QLCD_G3
PA24/UART8_RXD/CAN2_RXD/I2S2_FCLK/PWM1_P_5/DISO_G_5/CAM0_D_6	J2	33Ω	R62		QLCD_G5
PA25/UART8_TXD/CAN2_TXD/I2S2_BCLK/PWM1_P_4/DISO_G_7/CAM0_D_7	J1	33Ω	R64		QLCD_G7
PA26/UART12_RXD/SPI2_CSN/I2S3_TXD_1/DAOR_N/PWM0_P_5/DISO_R_4	H5	33Ω	R66		QLCD_R4
PA27/UART12_TXD/SPI2_MOSI/I2S3_TXD_0/DAOR_N/PWM0_P_4/DISO_R_6	H4	33Ω	R56		QLCD_R6
PA28/UART13_TXD/I2S3_BCLK/PWM0_P_0/DISO_R_7/CAM0_D_5	H3	33Ω	R58		QLCD_R7
PA29/UART9_RXD/CAN3_RXD/I2S2_RXD_1/I2S3_RXD_1/PWM1_P_3/DISO_G_4/CAM0_D_9	H2	33Ω	R59		QLCD_G4
PA30/UART9_TXD/CAN3_TXD/I2S2_RXD_0/I2S3_RXD_0/PWM1_P_1/DISO_G_6/CAM0_D_8	H1	33Ω	R63		QLCD_G6
PA31/UART13_CTS/I2C1_SDA/SPI2_MISO/I2S3_TXD_3/PWM0_P_3/DISO_R_3	G5	33Ω	R65		QLCD_R3
PB00/UART13_DE/UART13_RTS/I2C1_SCL/SPI2_SCLK/I2S3_TXD_2/PWM0_P_2/DISO_R_2	G4		R44		QLCD_R2
PB01/UART14_RXD/I2S2_RXD_3/I2S3_RXD_3/PWM1_P_2/DISO_G_1/SYSCTL_CLK_OBS_1	G2	33Ω	R54		QLCD_G1
PB02/UART14_TXD/I2S2_RXD_2/I2S3_RXD_2/PWM1_P_0/DISO_B_2/SYSCTL_CLK_OBS_0	G1	33Ω	R60		QLCD_G1
VIO_B02	H6				
VIO_B02	G6				
PB03/UART14_CTS/I2C2_SDA/I2S3_MCLK/TRGM0_P_11/DISO_R_0/CAM1_D_2	F5				QLCD_R0
PB04/UART14_DE/UART14_RTS/I2C2_SCL/I2S3_RXD_3/I2S2_FCLK/TRGM0_P_10/DISO_R_1/CAM1_D_4	F4	33Ω	R72		QLCD_R1
PB05/UART13_RXD/I2S3_RXD_2/I2S2_RXD_0/TRGM0_P_08/DISO_B_0/CAM1_D_6	F3	33Ω	R73		QLCD_B0
PB06/UART15_RXD/I2S3_RXD_1/TRGM1_P_05/DISO_G_0/CAM1_PIXCLK/SYSCTL_CLK_OBS_2	F2	33Ω	R74		QLCD_G0
PB07/UART15_TXD/I2S3_RXD_0/TRGM1_P_02/DISO_B_1/CAM1_HSYNC/SYSCTL_CLK_OBS_3	F1	33Ω	R79		QLCD_B1
PB08/UART2_CTS/CAN2_RXD/I2S3_TXD_3/I2S2_BCLK/TRGM0_P_09/CAM1_D_3	E4	33Ω	R75		QPBO8/CAP_INT_N
PB09/UART2_DE/UART2_RTS/CAN2_TXD/I2S3_MCLK/I2S2_MCLK/TRGM0_P_07/CAM1_D_5	E3				QPBO9/CAP_RESET_N
PB10/UART12_CTS/I2C0_SDA/CAN3_STBY/I2S3_BCLK/TRGM1_P_04/CAM1_XCLK	E2				QPBO10/TFT_PWM
PB11/UART12_DE/UART12_RTS/I2C0_SCL/CAN2_STBY/I2S3_FCLK/TRGM1_P_01/CAM1_VSYNC	E1				QPBO11
PB12/UART3_DE/UART3_RTS/CAN3_TXD/I2S3_TXD_2/I2S2_TXD_0/TRGM0_P_06/CAM1_D_7	D3				QPBO12
PB13/UART15_CTS/I2C3_SDA/CAN1_STBY/I2S3_TXD_1/TRGM1_P_03/CAM1_D_8	D2				QPBO13
PB14/UART15_DE/UART15_RTS/I2C3_SCL/CAN0_STBY/I2S3_TXD_0/TRGM1_P_00/CAM1_D_9	D1				QPBO14/CAN0_STBY



06_HPM6750-102



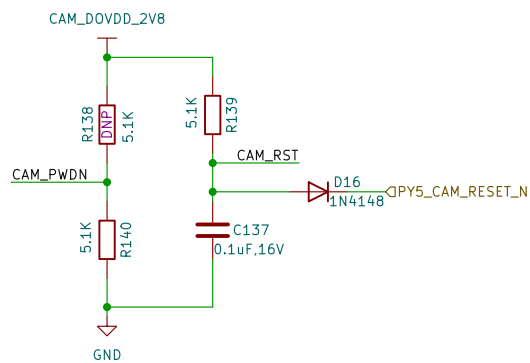
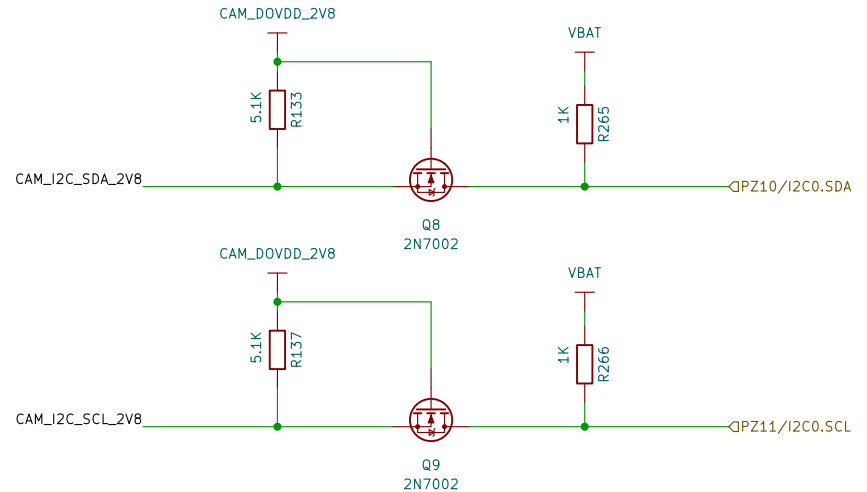
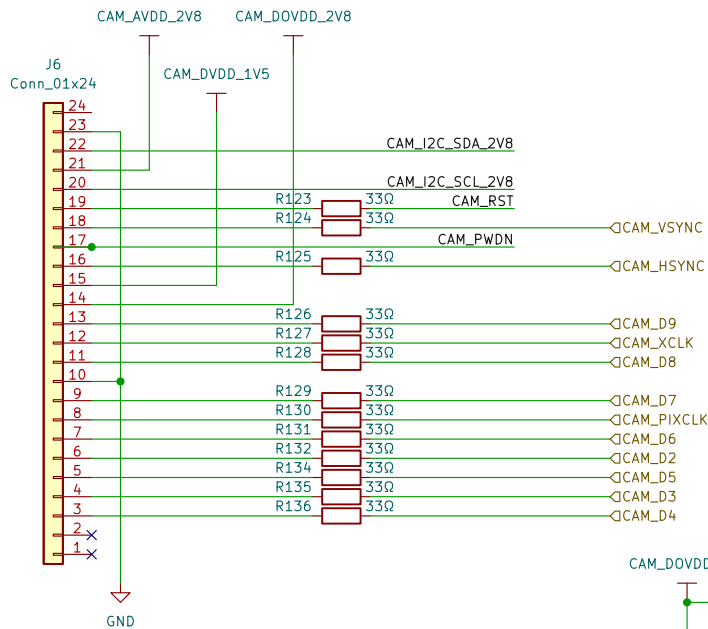
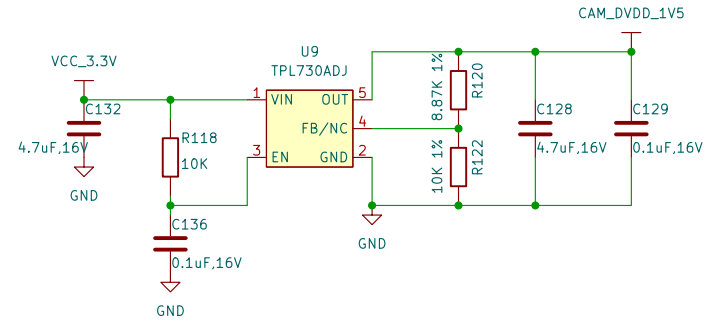
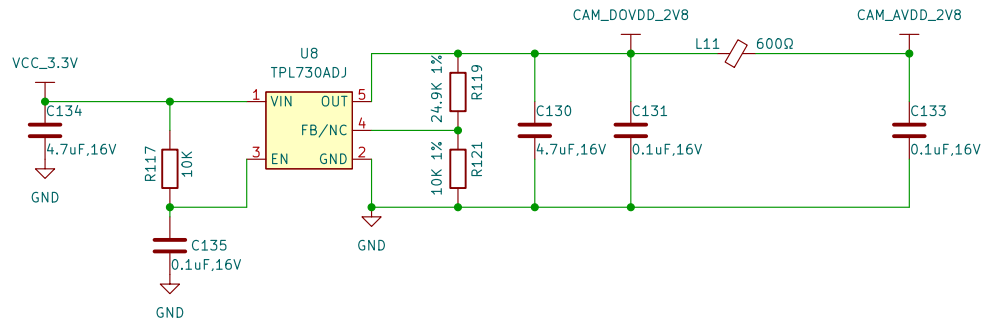
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07_HPM6750-103



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09_CAM



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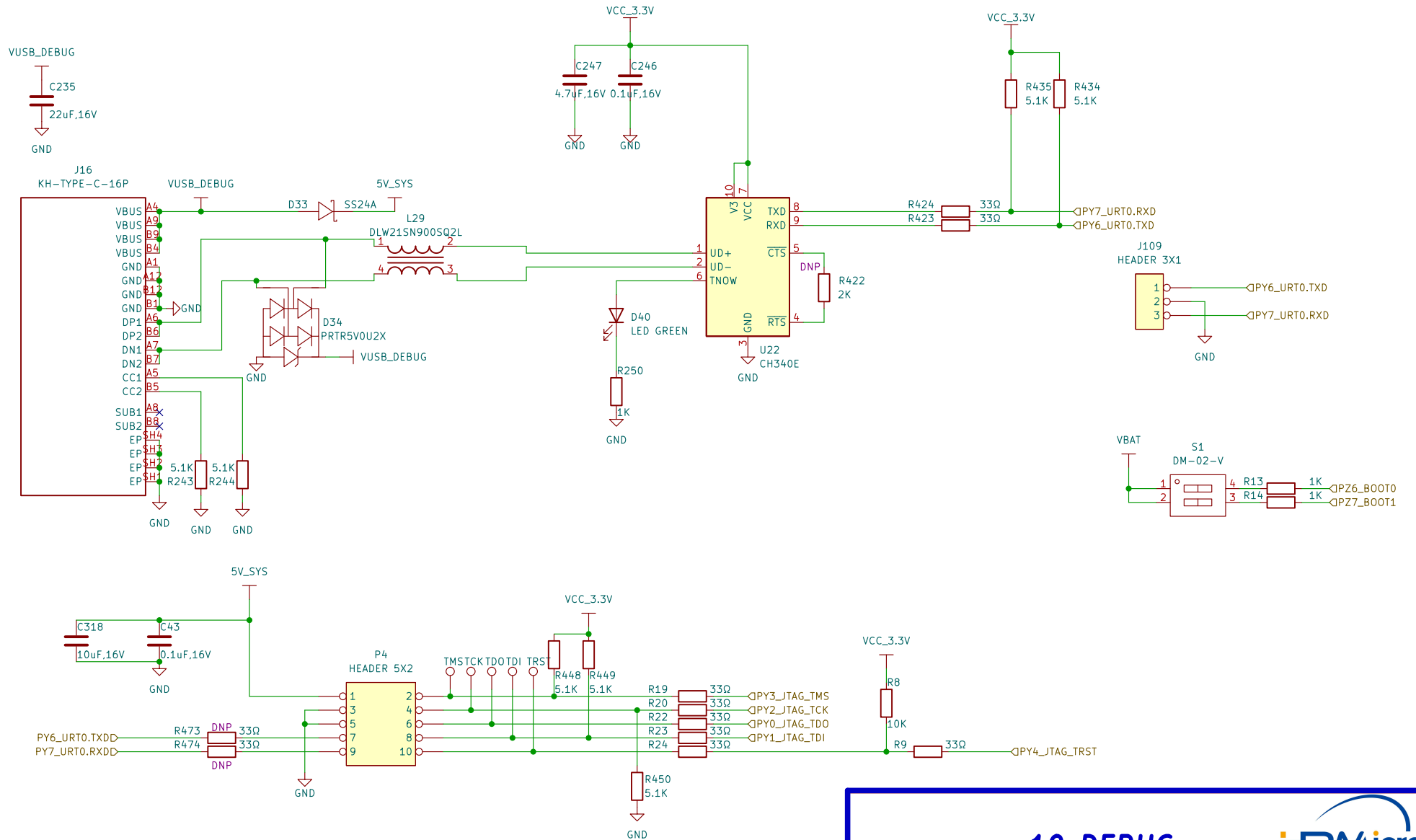
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Date:

Rev:

Id: 10/20



10_DEBUG

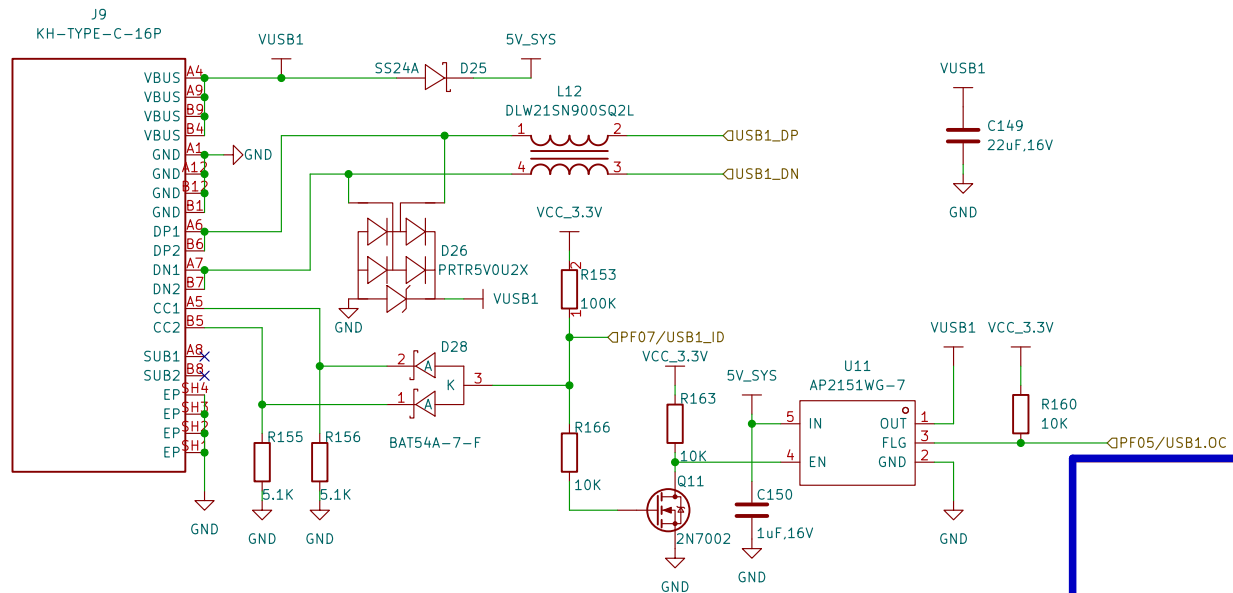
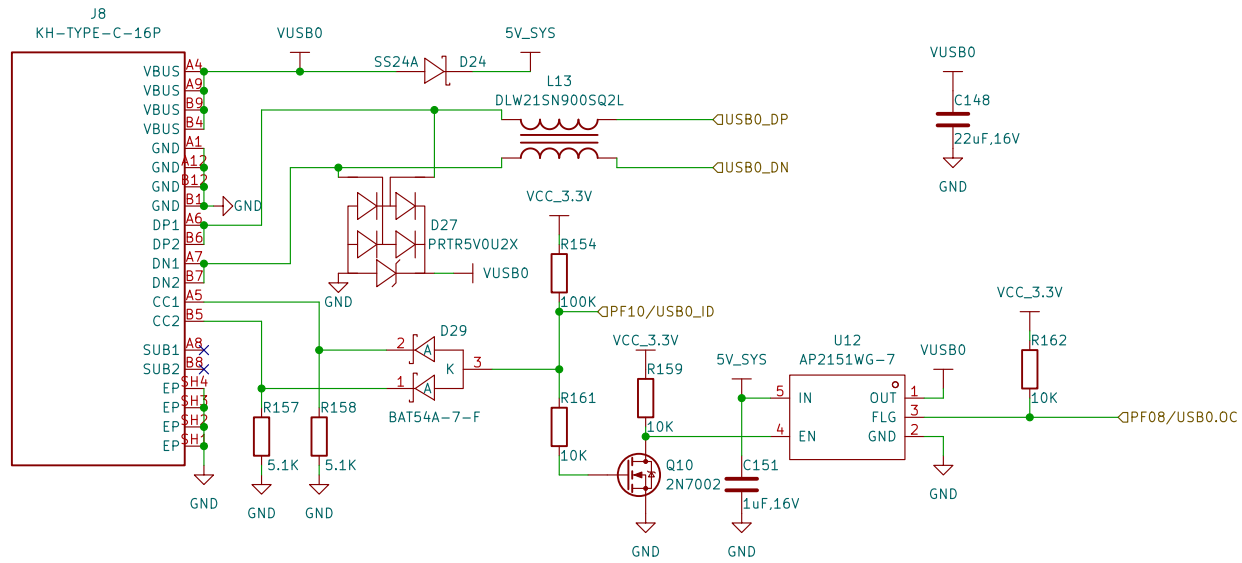


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Rev:
Id: 11/20



11_USB



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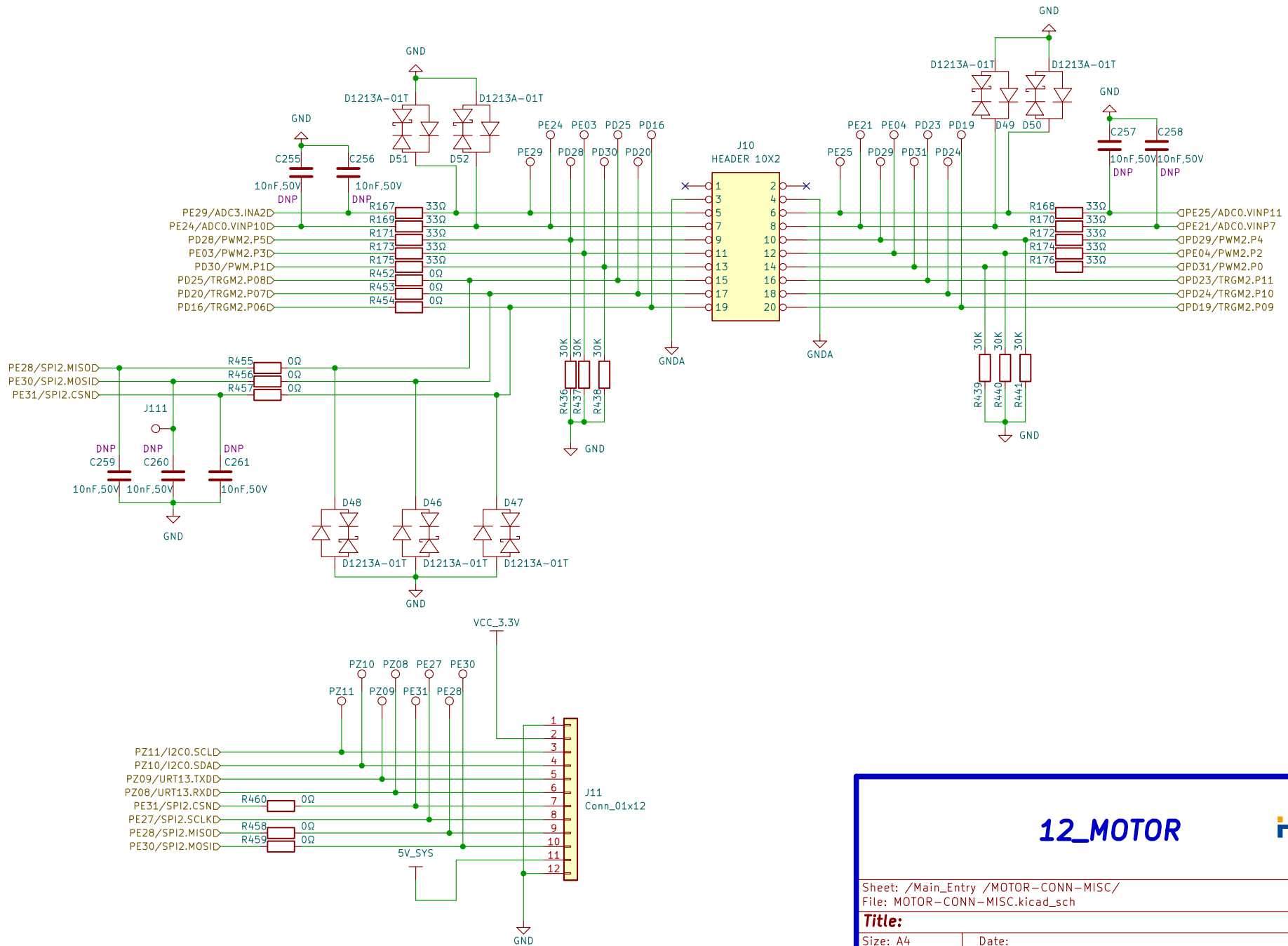
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Date:

Rev:

Id: 12/20



12_MOTOR



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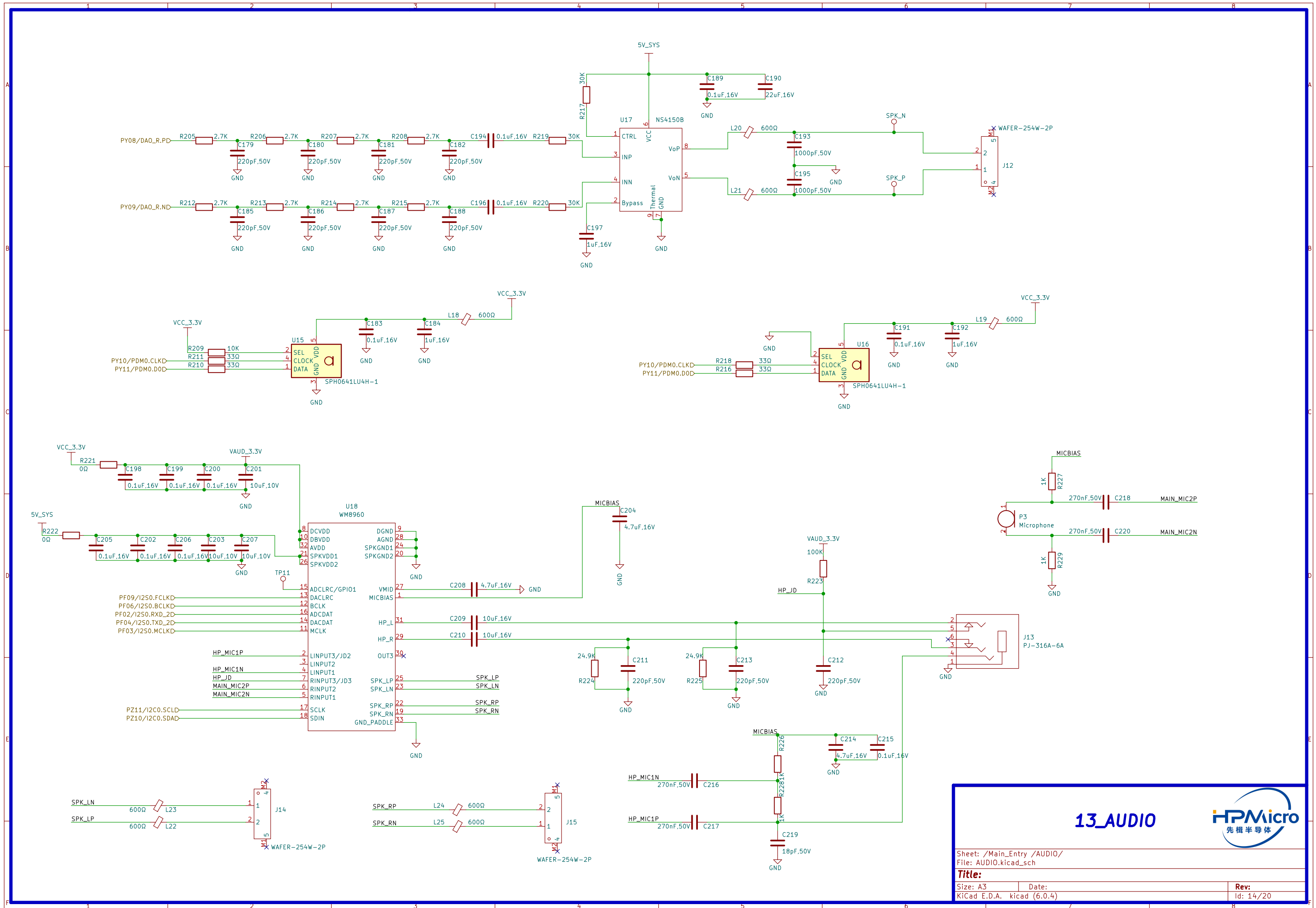
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Date:

Rev:

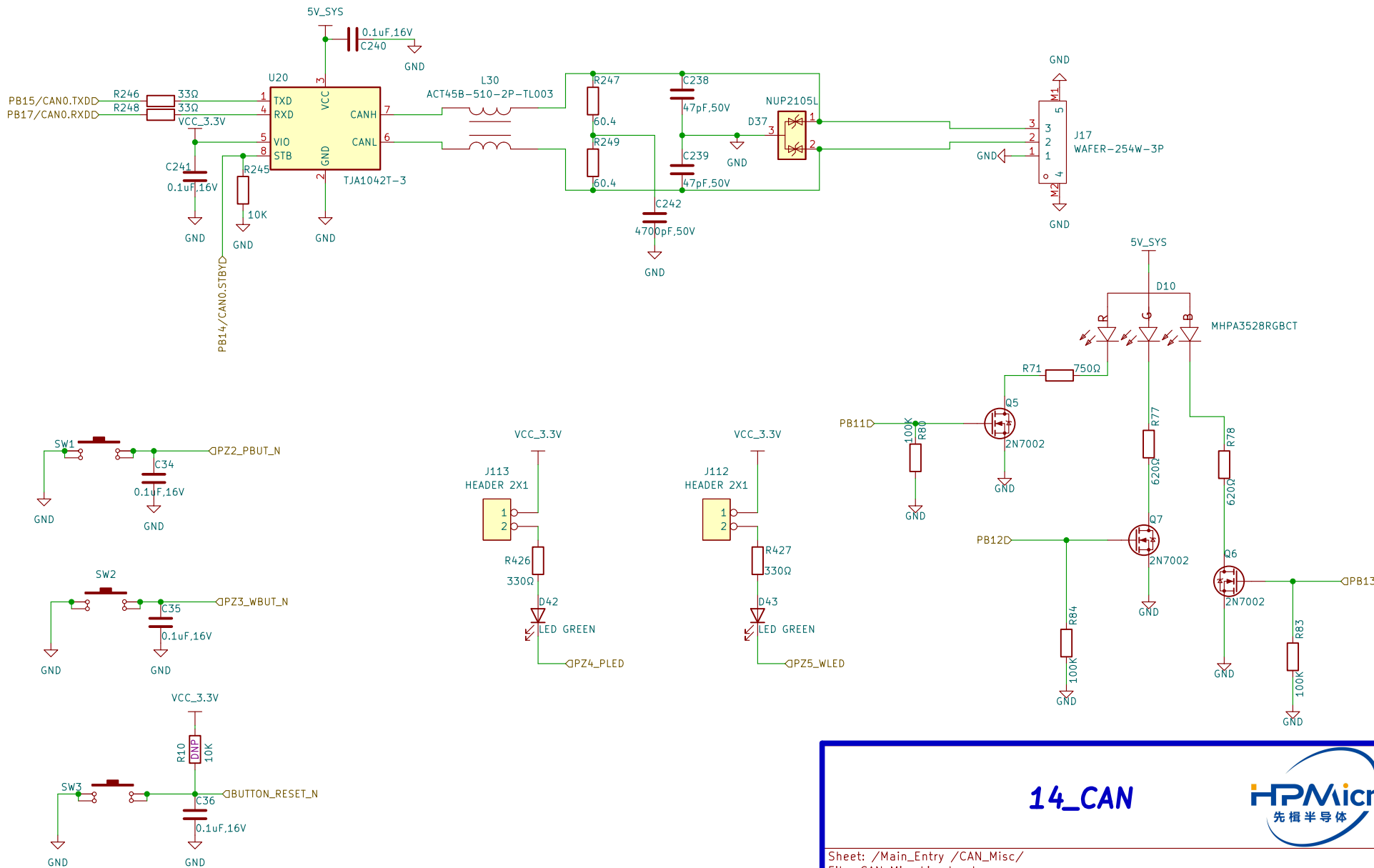
Id: 13/20



13_AUDIO

HPMicro
先轸半导体

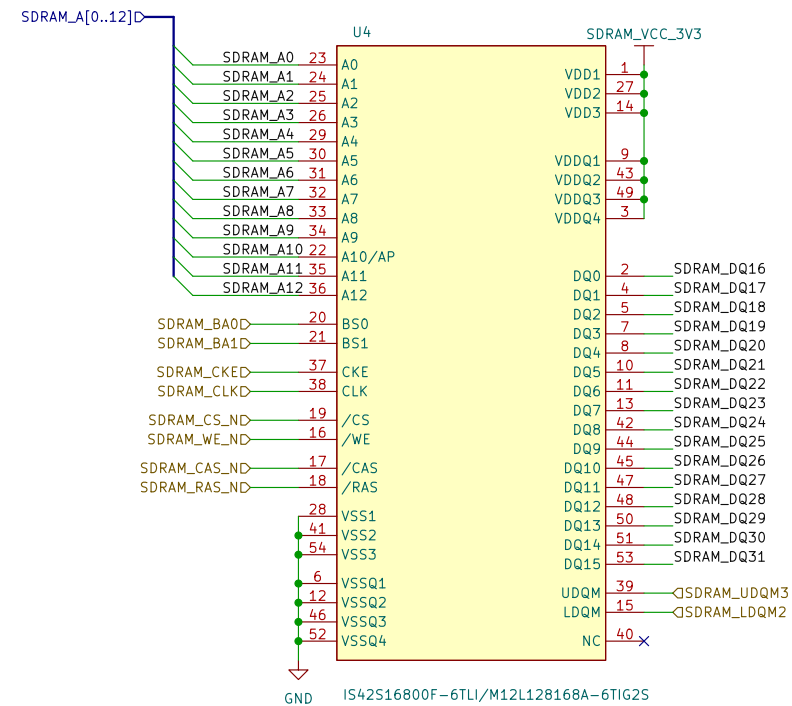
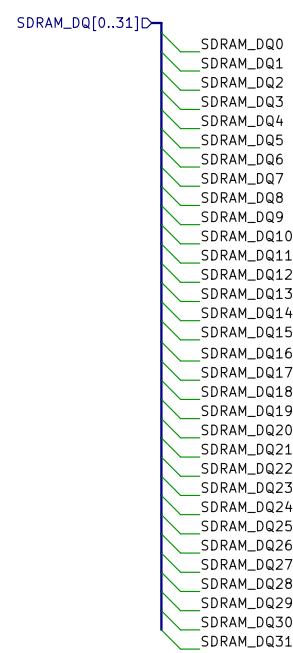
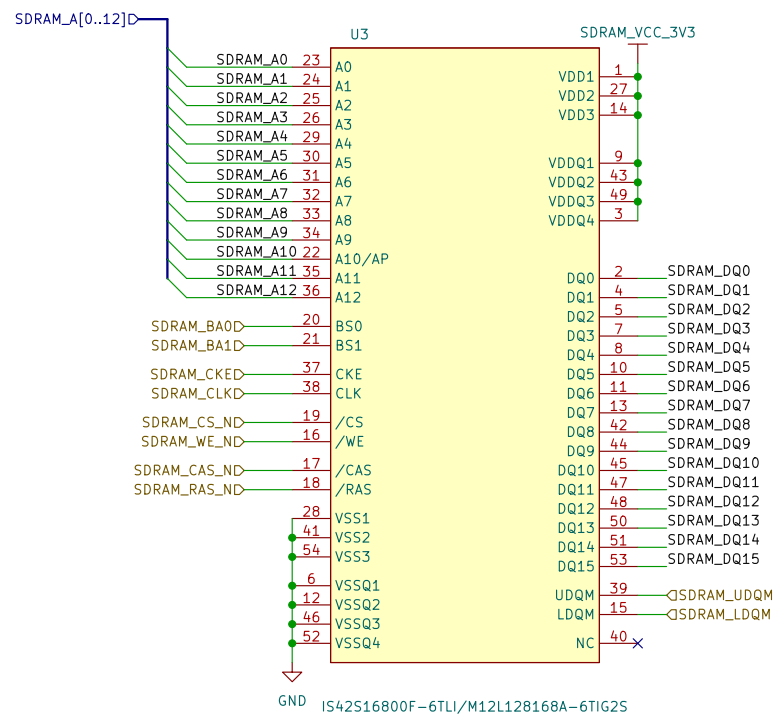
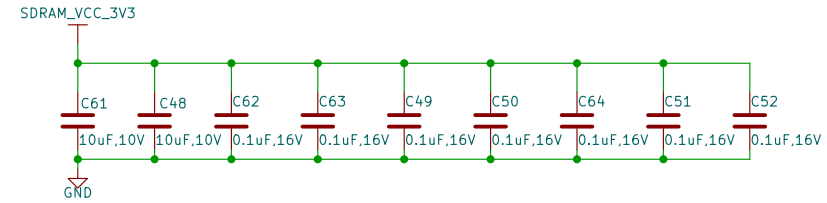
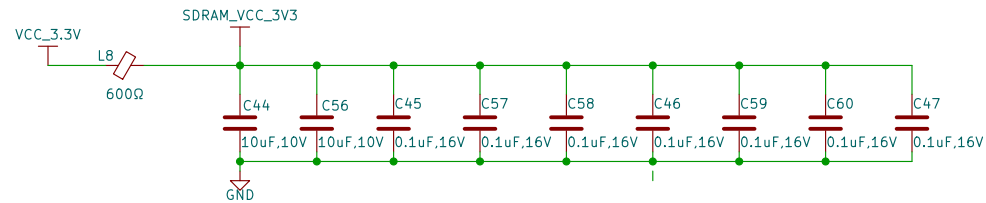
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14_CAN



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KiCad E.D.A. kicad (6.0.4)	Rev: 15/20



15_SDRAM

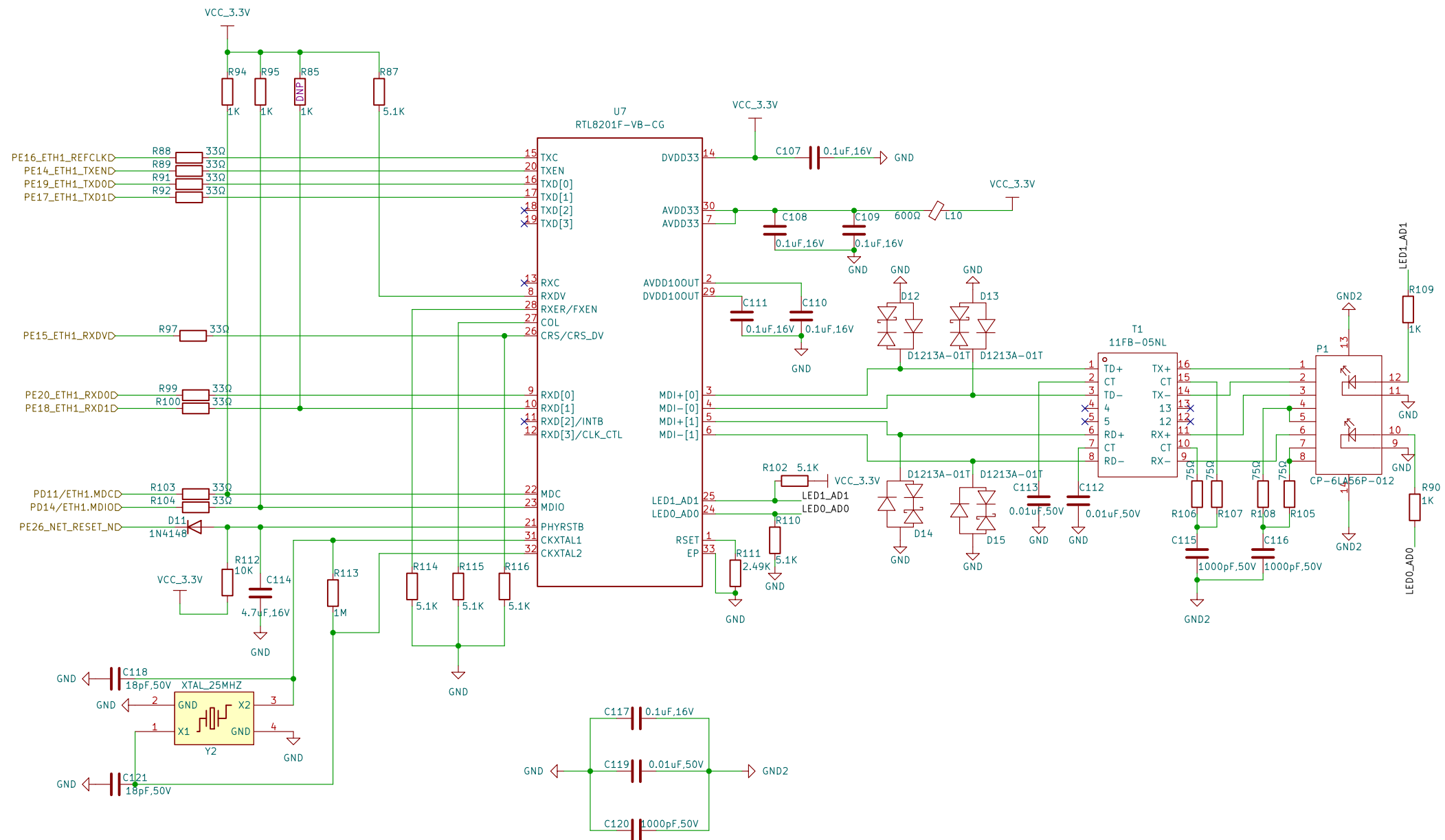


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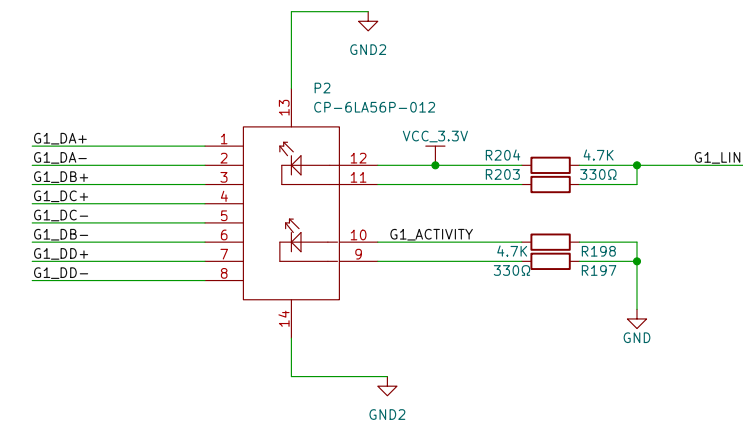
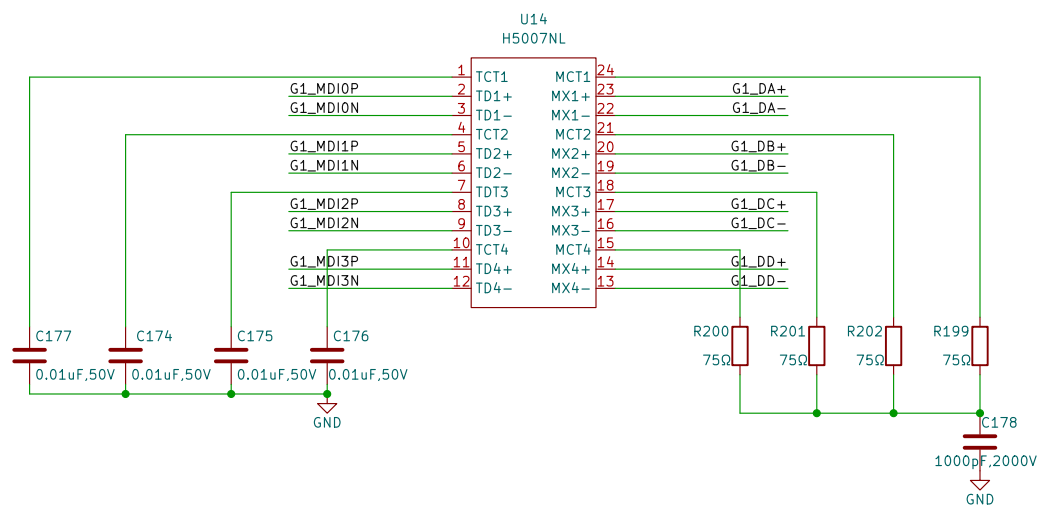
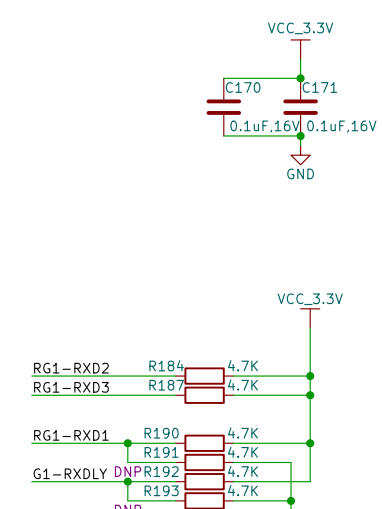
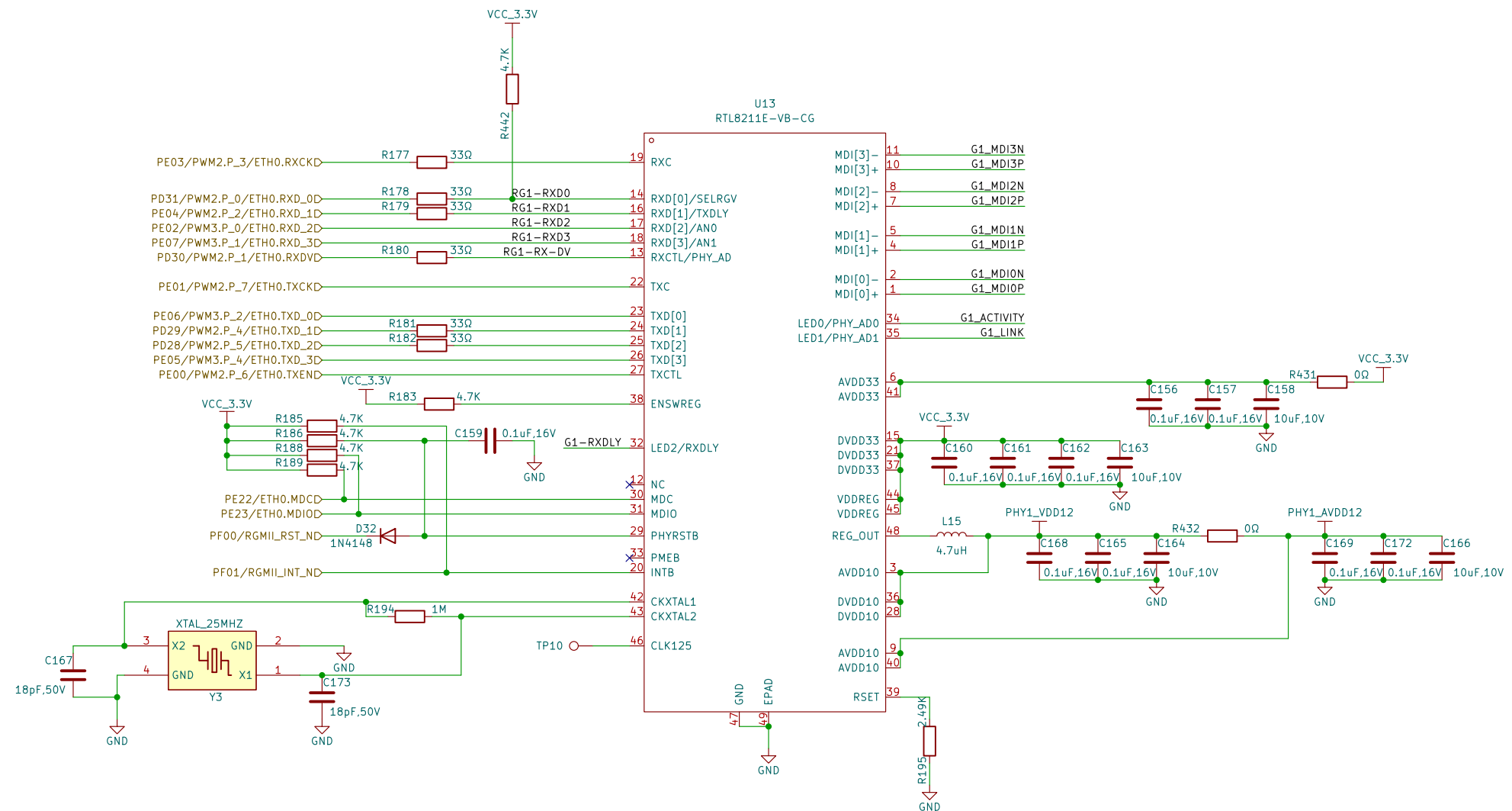
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KiCad E.D.A. kicad (6.0.4)

Rev:
Id: 16/20



16_ETH_RMII





17_ETH_RGMII



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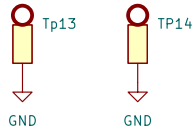
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Rev:

KiCad E.D.A. kicad (6.0.4)

Id: 18/20



ASSEM



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Rev:
Id: 20/20