

6

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1

Update(2012.06.26)
1;ç,üðÀ51PIN Óe838 V1.5 Ĩãĭ¬
2;ç0ð%04PIN Connector >>MCUB´£¬(2-Wire)
3;ç51PIN_μŭ45PIN¶ĭ¬0âĬ°EXT_PWR_DET>>¶Ĭμç¹0»ú(IPD838 SI1161+TVP5150 Signal Board v1.2)
4.R103 =0R,R65=0R (delete opt)
5.MCU 0ð%0,´Ĭ»μçĀ·
6.R97=10K(delete opt)
7.R82=opt
8.add C42

REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:

DRAWN:
Xie Bo

DATED:
2012/07/19

CHECKED:

DATED:

QUALITY CONTROL:
<QC By>

DATED:
<QC Date>

APPROVED:
Liu Fan

DATED:
2012/07/19

COMPANY:
 iView Limited

TITLE:
IPD635 main board v1.1

CODE: <Code>	SIZE: C	DRAWING NO: DWG-ES0128-R01	REV: 1.1
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SCALE:
<Scale>

SHEET:
ðf 7

A

B

C

D

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D

C

B

A

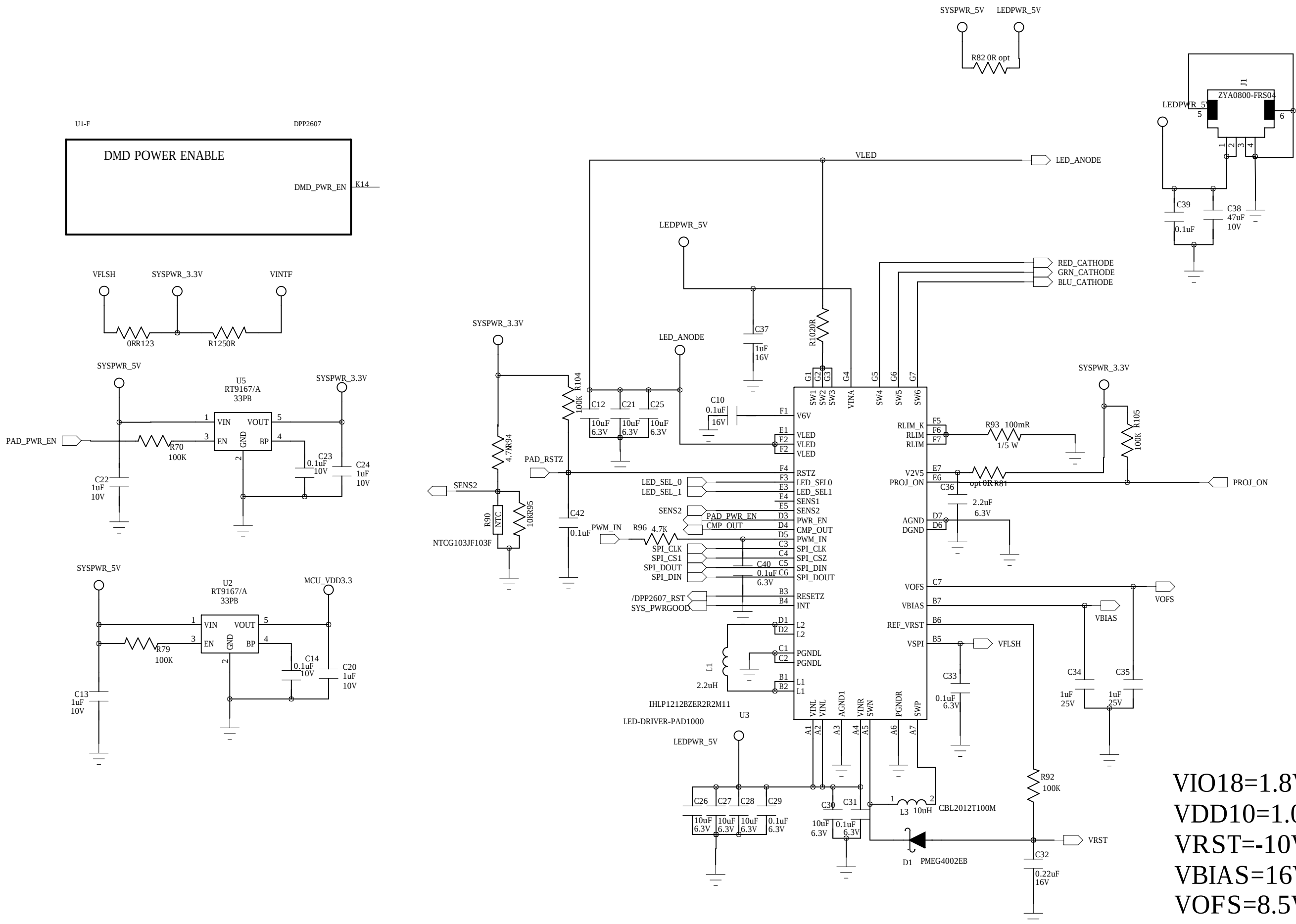
D

C

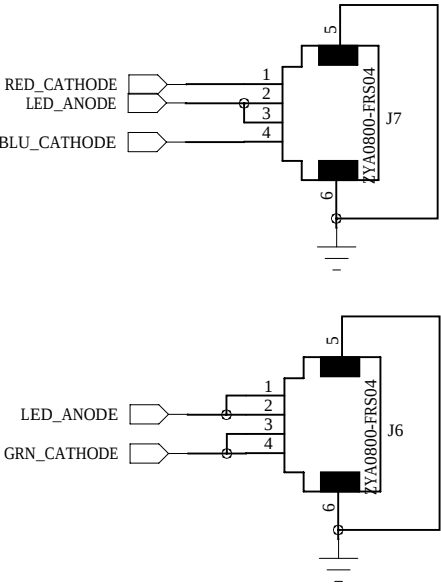
B

A

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

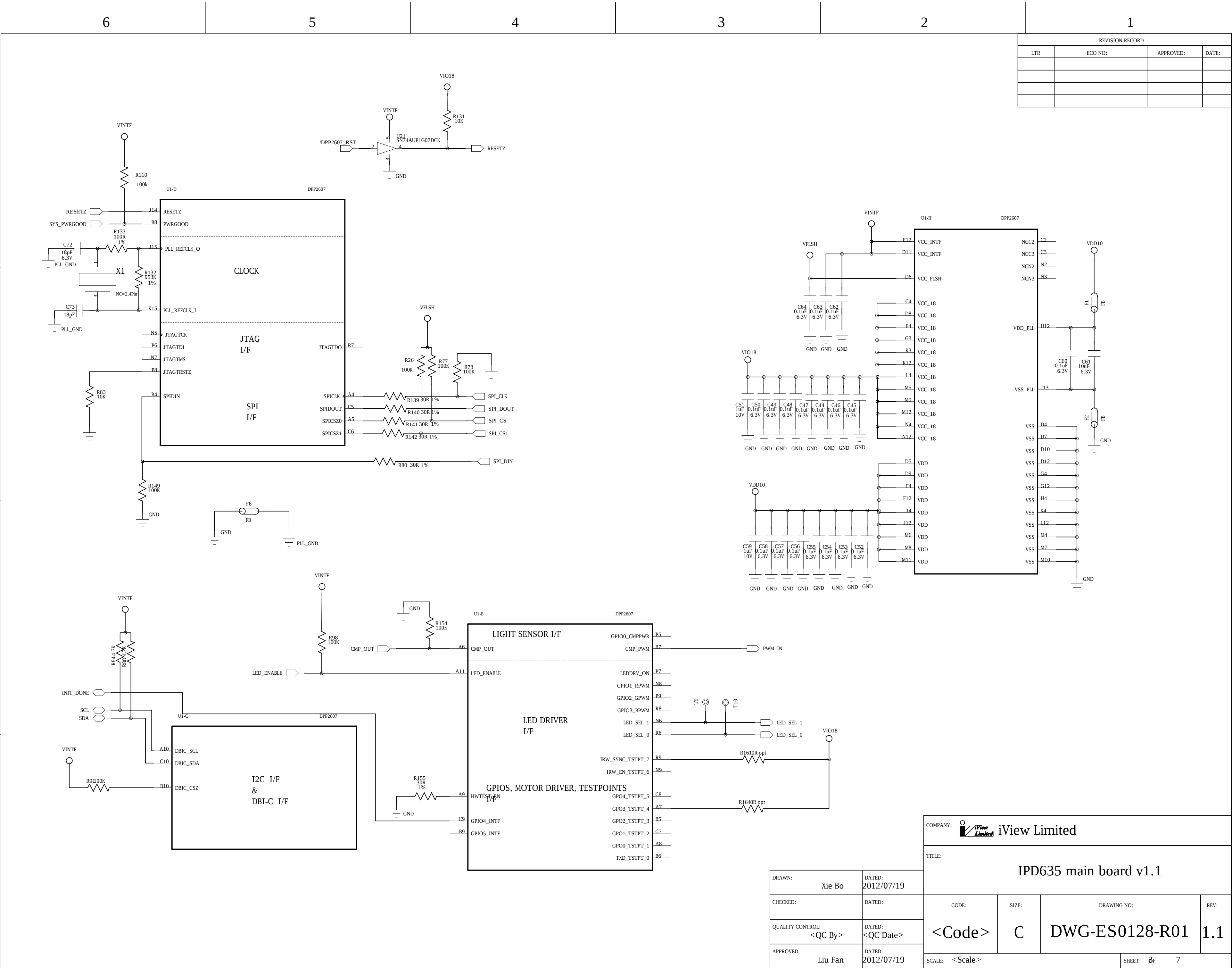


VIO18=1.8V
VDD10=1.0V
VRST=-10V
VBIAS=16V
VOFS=8.5V
VFLSH=3.3V
VINTF=3.3V



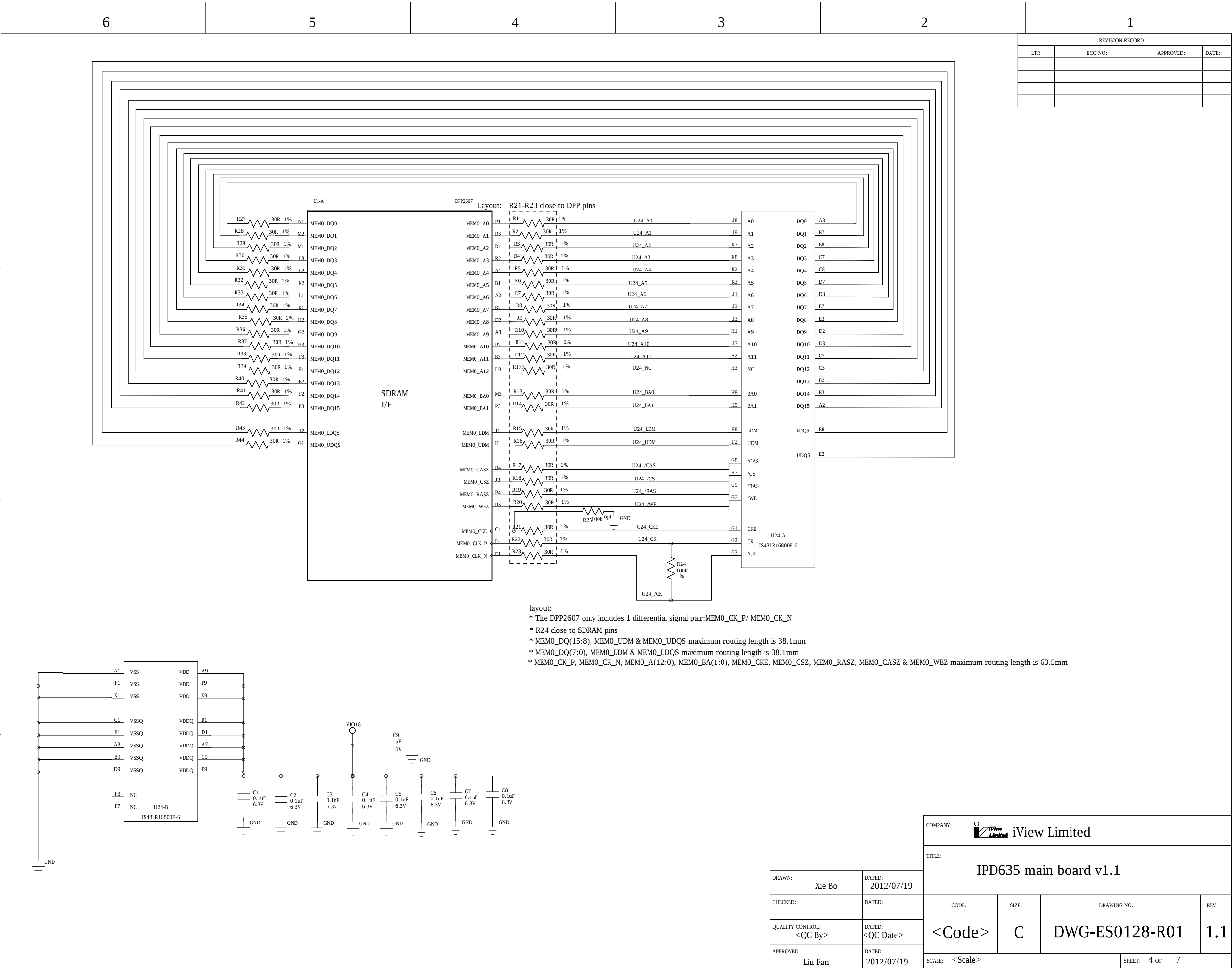
COMPANY: iView Limited			
TITLE: IPD635 main board v1.1			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	C	DWG-ES0128-R01	1.1
SCALE: <Scale>			SHEET: 2 of 7

DRAWN:	Xie Bo	DATED:	2012/07/19
CHECKED:		DATED:	
QUALITY CONTROL:		DATED:	
APPROVED:	Liu Fan	DATED:	2012/07/19



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY:  iView Limited			
TITLE: IPD635 main board v1.1			
DRAWN: Xie Bo	DATED: 2012/07/19	CODE: <Code>	SIZE: C
CHECKED:	DATED:	DRAWING NO: DWG-ES0128-R01	REV: 1.1
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	
APPROVED: Liu Fan	DATED: 2012/07/19	SHEET: 3 of 7	



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

- layout:
- * The DPP2607 only includes 1 differential signal pair:MEMO_CK_P/ MEMO_CK_N
 - * R24 close to SDRAM pins
 - * MEMO_DQ(15:8), MEMO_UDM & MEMO_UDQS maximum routing length is 38.1mm
 - * MEMO_DQ(7:0), MEMO_LDM & MEMO_LDQS maximum routing length is 38.1mm
 - * MEMO_CK_P, MEMO_CK_N, MEMO_A(12:0), MEMO_BA(1:0), MEMO_CKE, MEMO_CSZ, MEMO_RASZ, MEMO_CASZ & MEMO_WEZ maximum routing length is 63.5mm

COMPANY: iView Limited			
TITLE: IPD635 main board v1.1			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	C	DWG-ES0128-R01	1.1
SCALE: <Scale>			SHEET: 4 OF 7

DRAWN:	DATED:
Xie Bo	2012/07/19
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
<QC By>	<QC Date>
APPROVED:	DATED:
Liu Fan	2012/07/19

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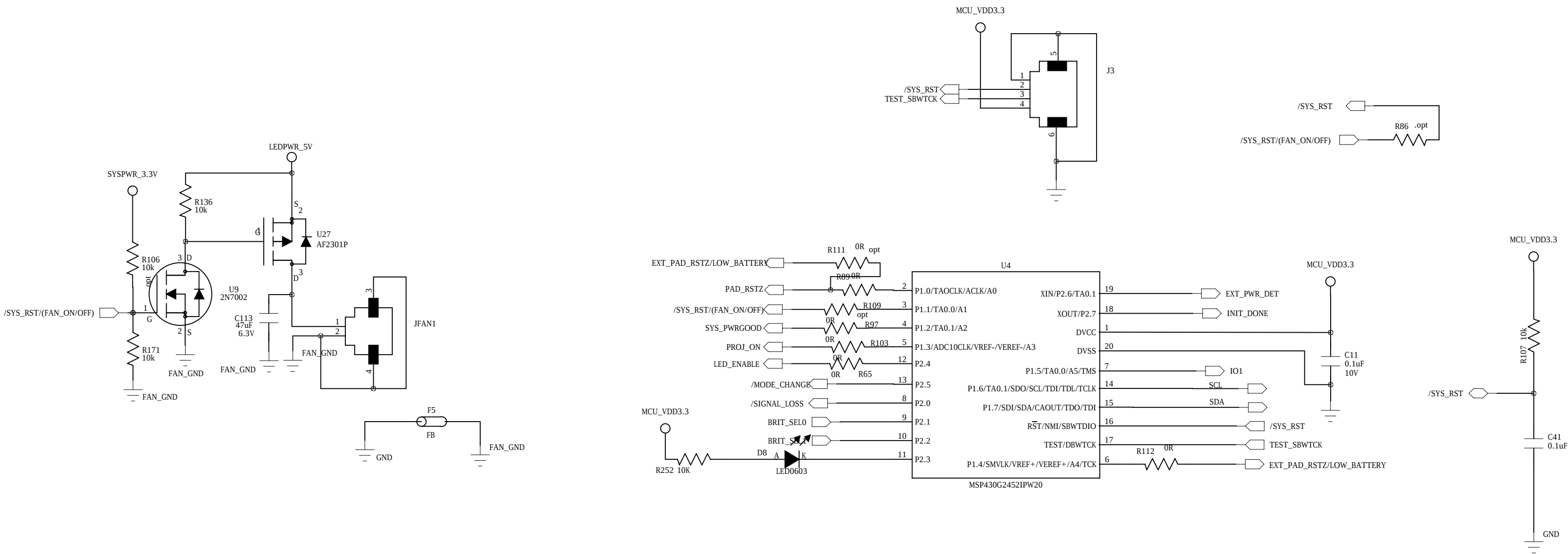
3

2

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

MSP430 DEBUG



COMPANY:  iView Limited			
TITLE: IPD635 main board v1.1			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	C	DWG-ES0128-R01	1.1
SCALE: <Scale>			SHEET: 5 of 7

DRAWN:	DATED:
Xie Bo	2012/07/19
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
<QC By>	<QC Date>
APPROVED:	DATED:
Liu Fan	2012/07/19

6

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C

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A

D

C

B

A

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

