

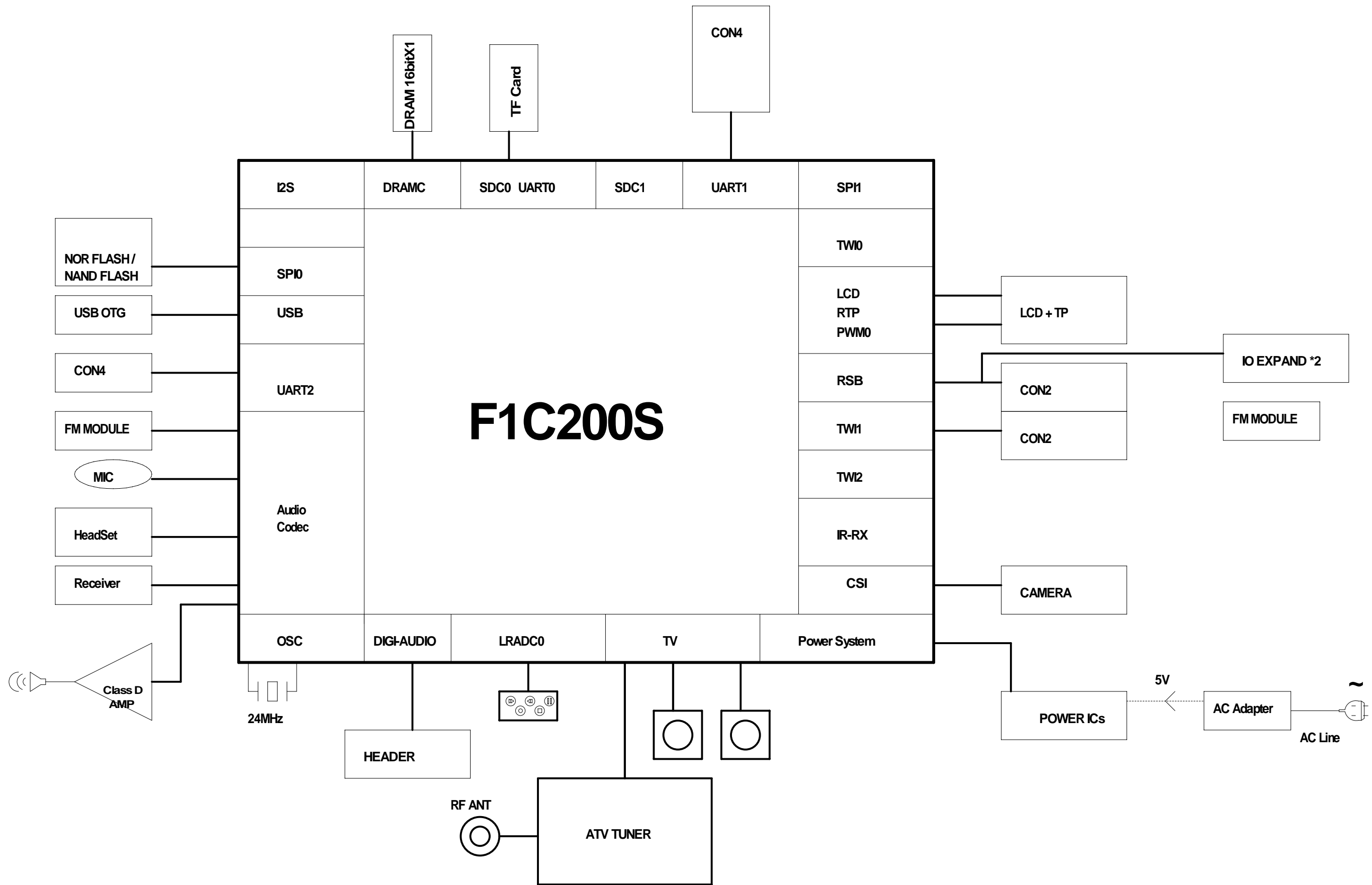
**Indexf<sup>0</sup>**

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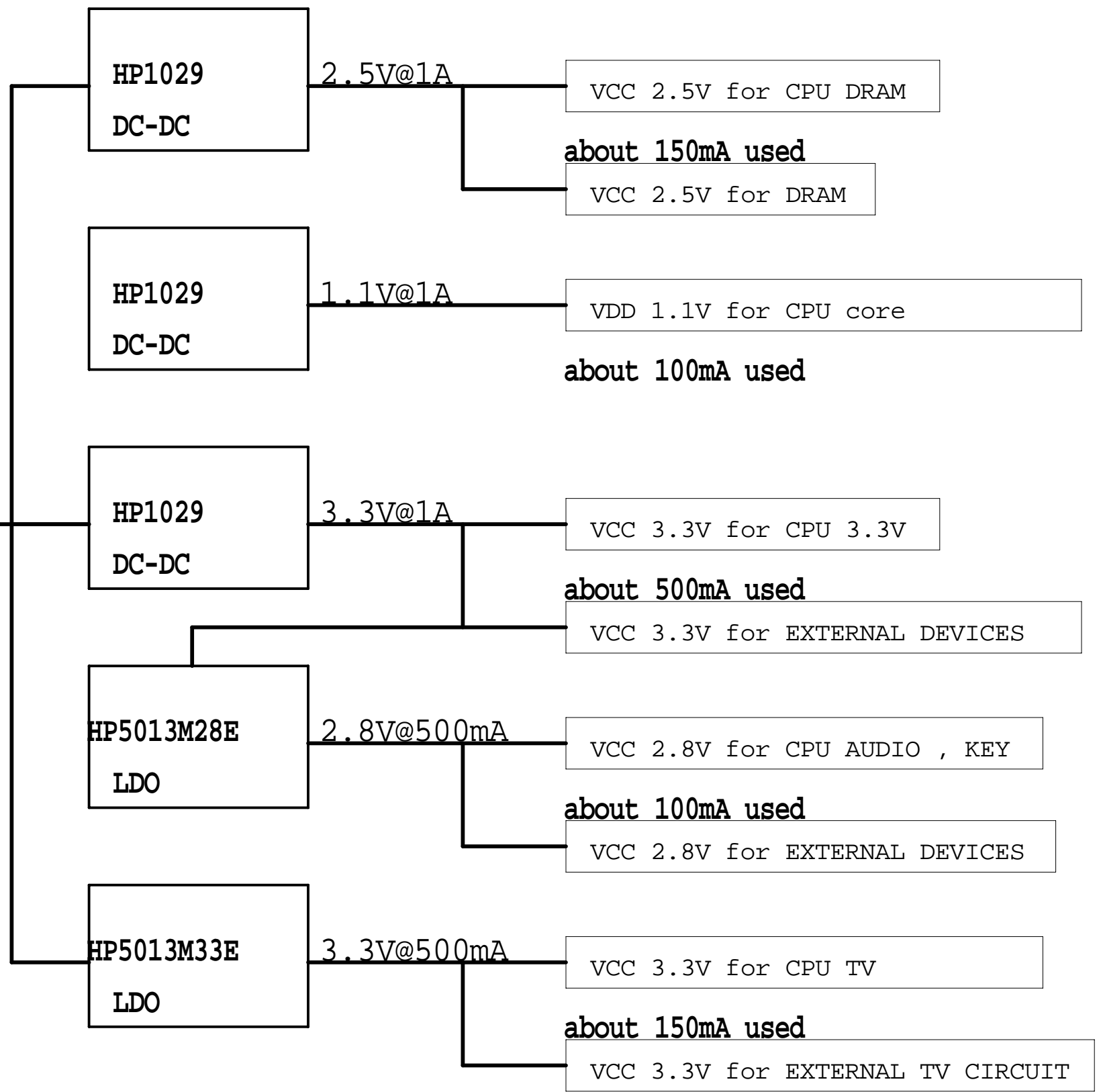
REVISION	DESCRIPTION	Date	Drawn	Checked	Approved
Ver0.1	AW_F1C200s_V10	2018-2-27	LY	YXB	

- P01: VERSION HISTORY
- P02: BLOCK DIAGRAM
- P03: POWER TREE
- P04: GPIO ASSIGNMENT
- P05: CPU DDR1
- P06: SYS OSC BESIDE CPU
- P07: POWER 1 USB
- P08: POWER 2
- P09: LCD
- P10: eMMC
- P11: AUDIO
- P12: TV
- P13: KEY IO-EXPAND
- P14: TWI RSB UART
- P15: CAMERA DIGI-AUDIO

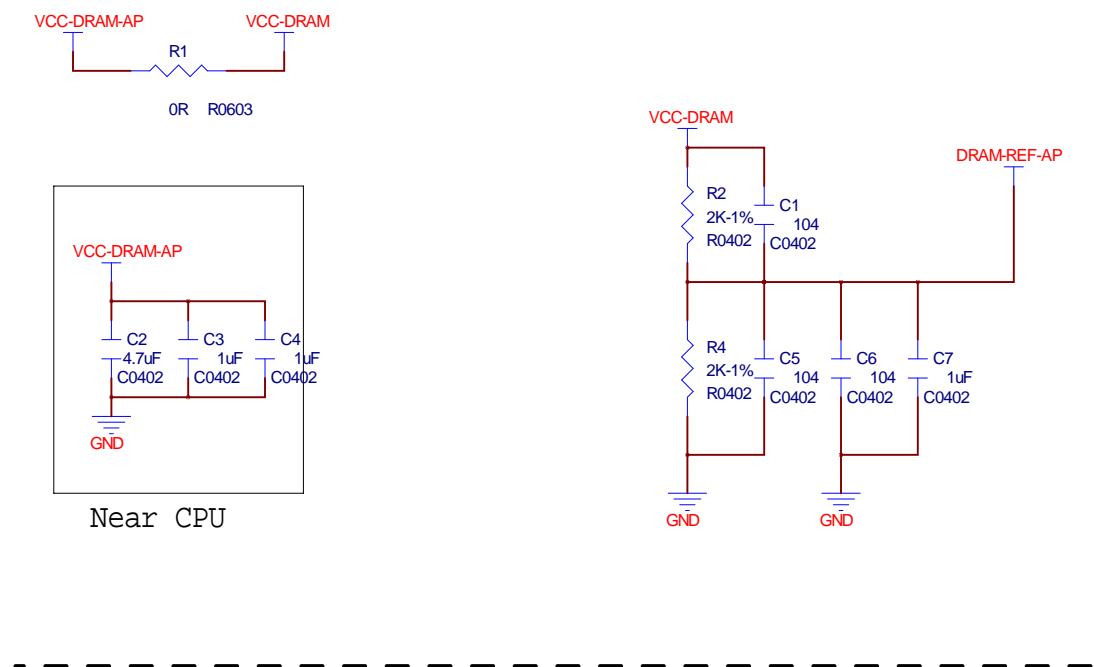
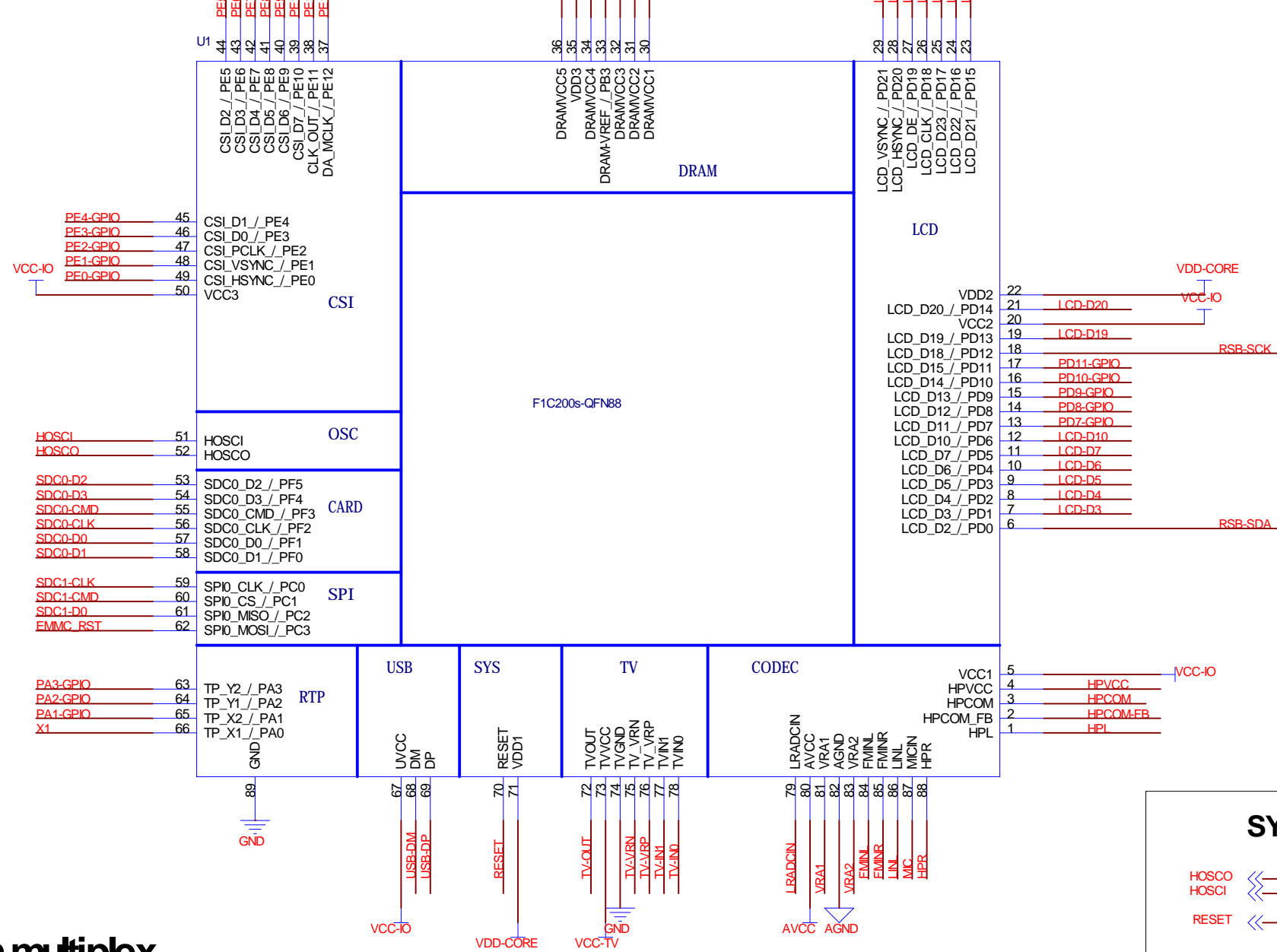
<b>AllWinner Technology Co.,Ltd</b>			
Design Name			
<b>AW_F1C200s_V10</b>			
Size	Page Name		Rev
<b>B</b>	<b>VERSION HISTORY</b>		<b>?</b>
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# Supply Circuit

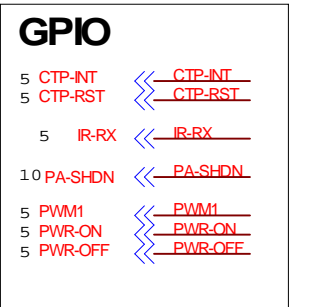
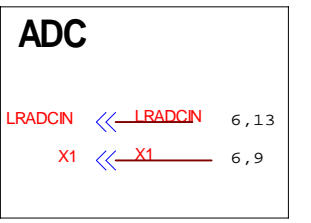
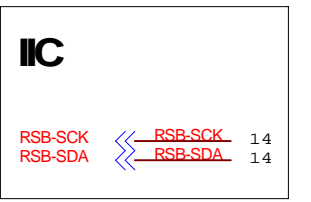
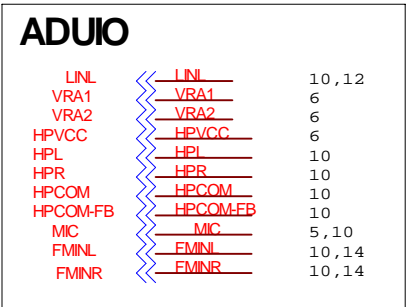
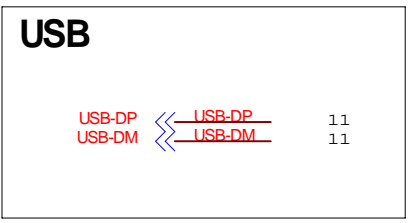
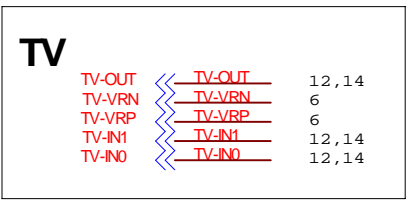
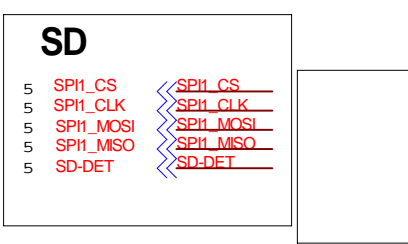
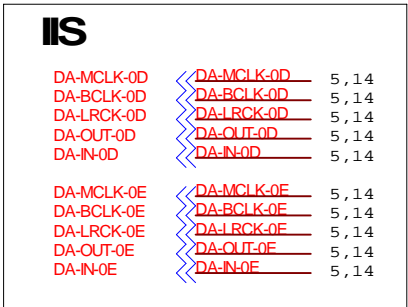
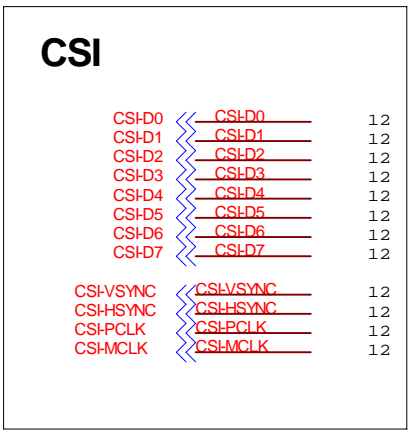
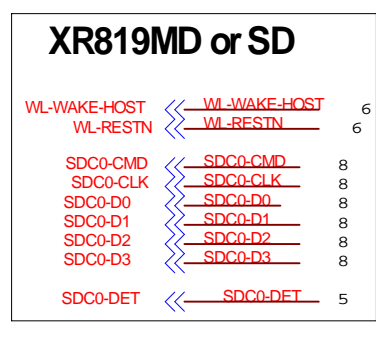
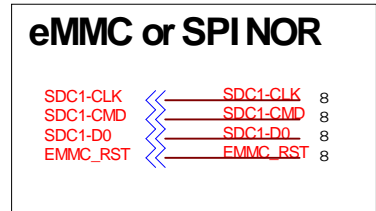
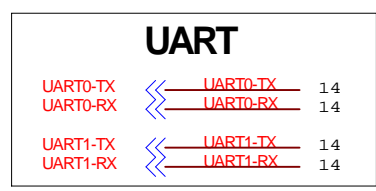
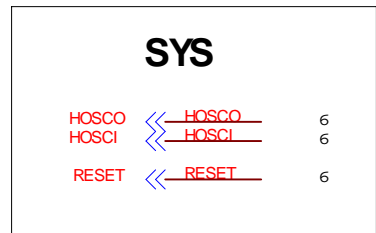
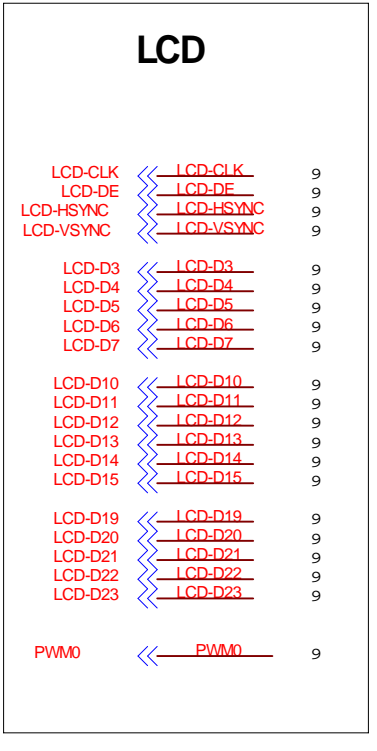
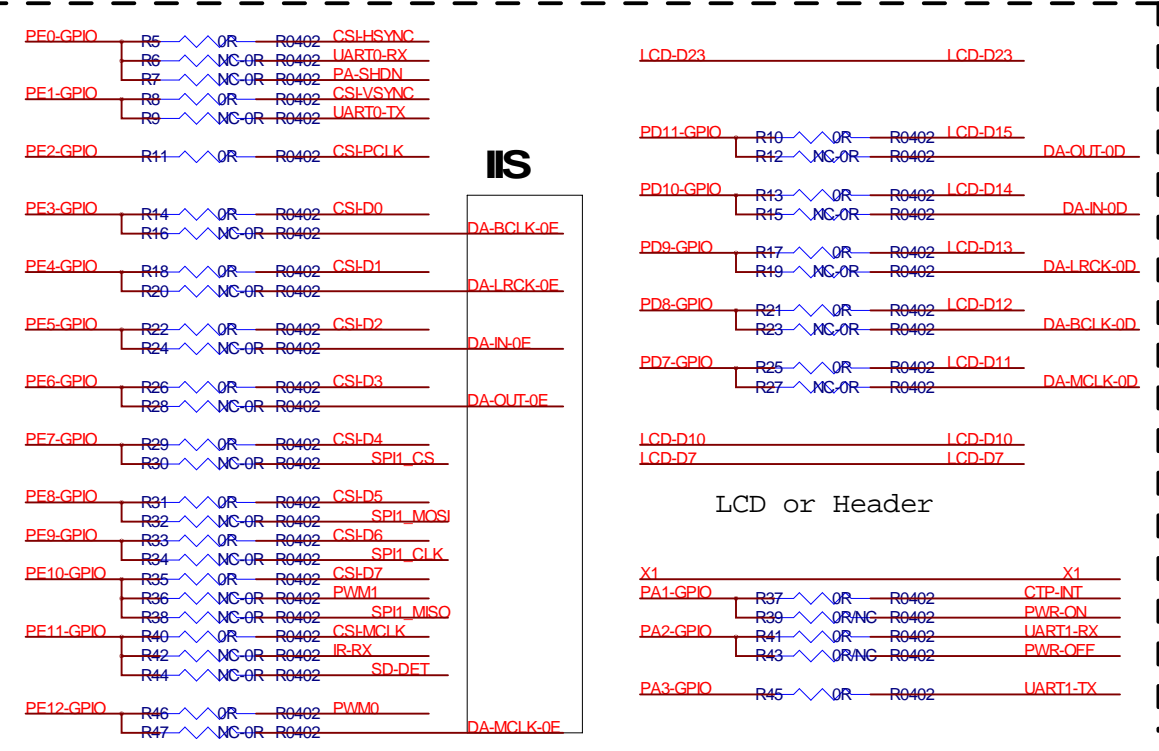


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<b>AW_F1C200s_V10</b>		
Size	Page Name	Rev
<b>B</b>	<b>GPIO ASSIGNMENT</b>	<b>?</b>
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Near CPU

### Pin multiplex

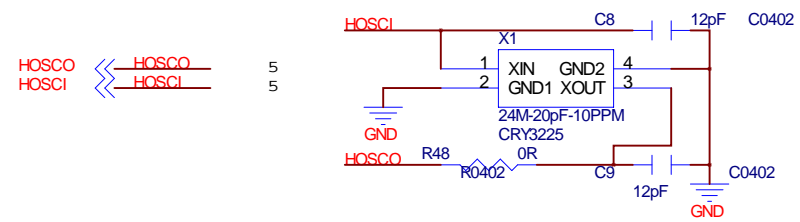


**AllWinner Technology Co.,Ltd**

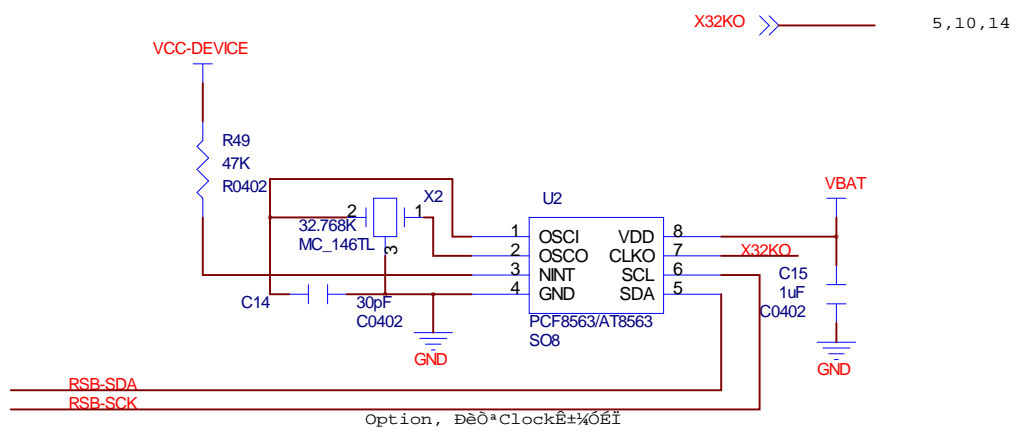
Design Name: **AW\_F1C200s\_v10**

Size: **B** Page Name: **CPU DDR1** Rev: **?**

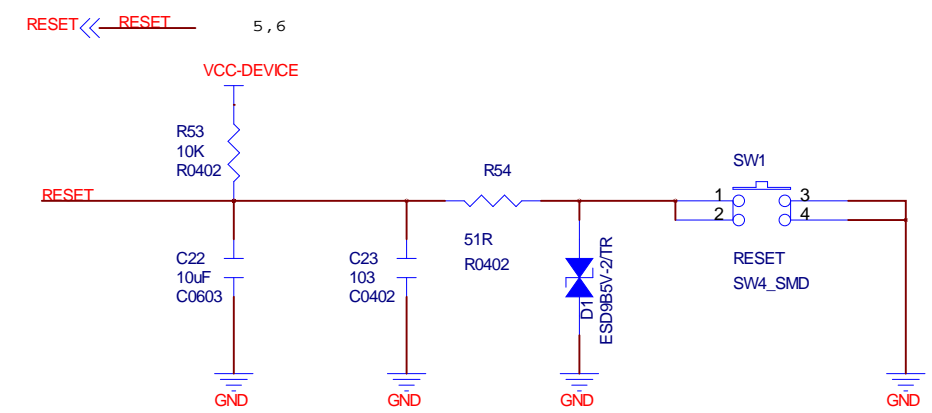
Date: **Friday, April 6, 2018** Sheet: **5** of **16**



### RTC

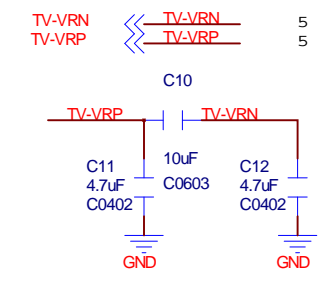


### RESET



Low maintain 22ms to (3.3V \* 0.2)

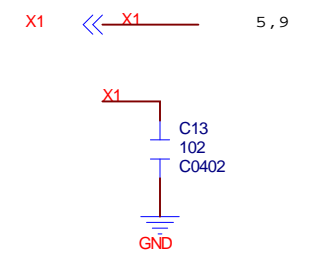
### TV VRP/N



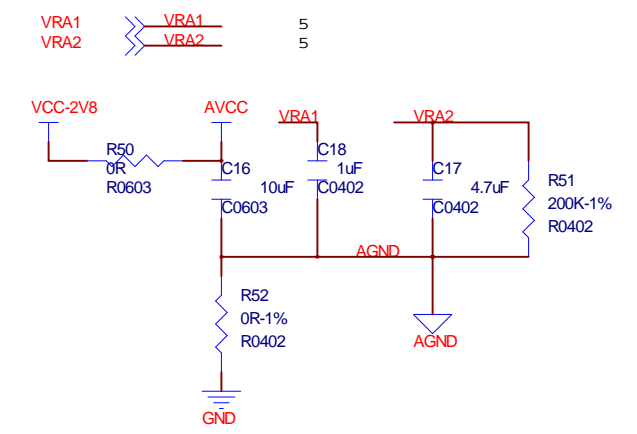
### TV POWER



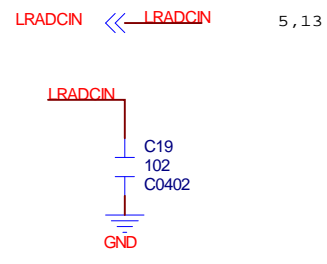
### TOUCH



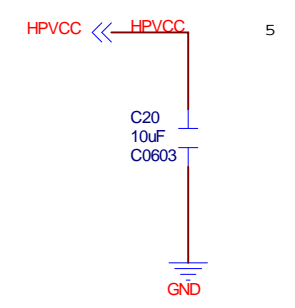
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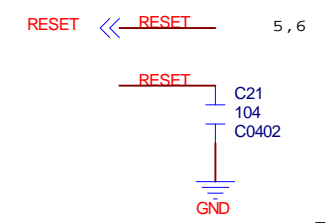
### ADCIN



### AUDIO HPVCC

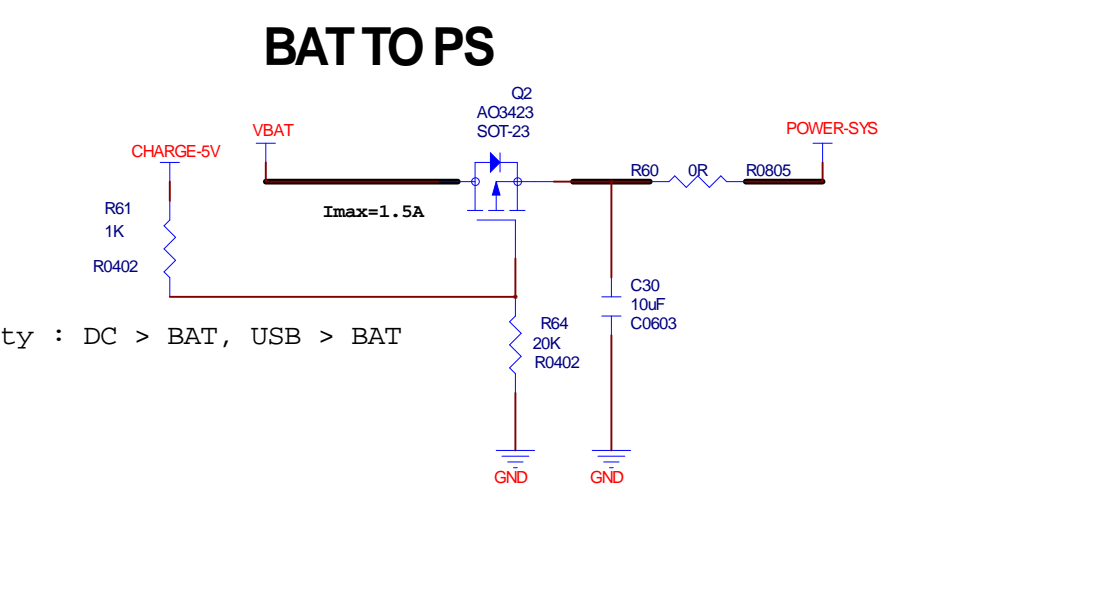
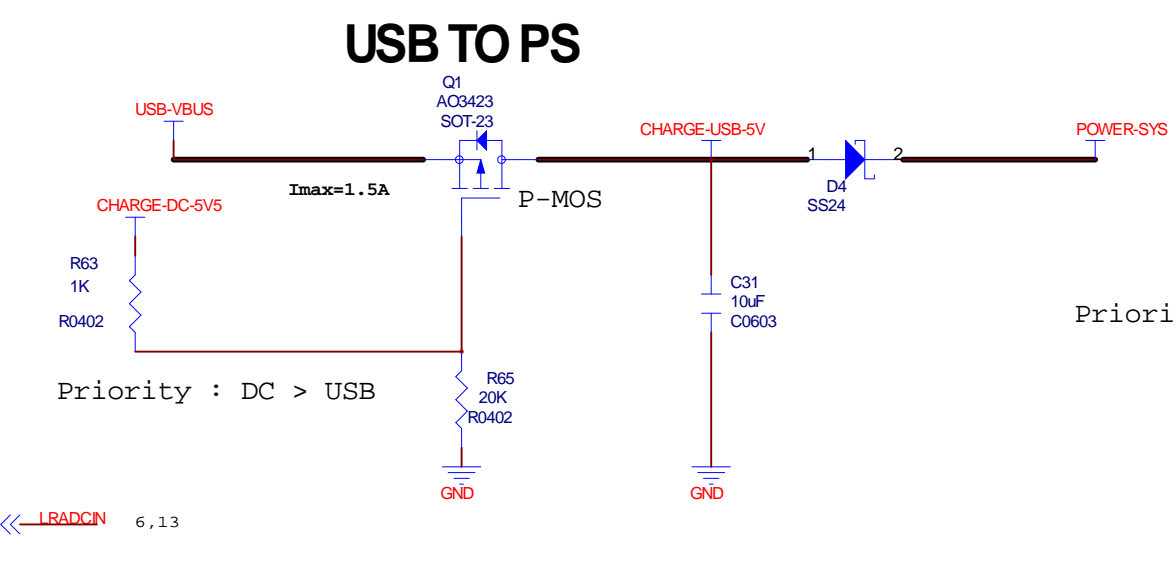
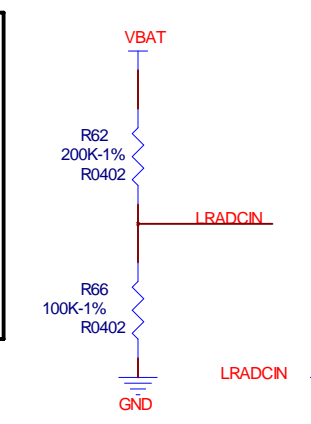
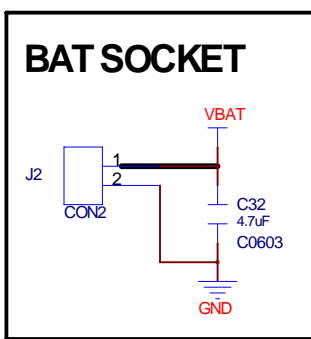
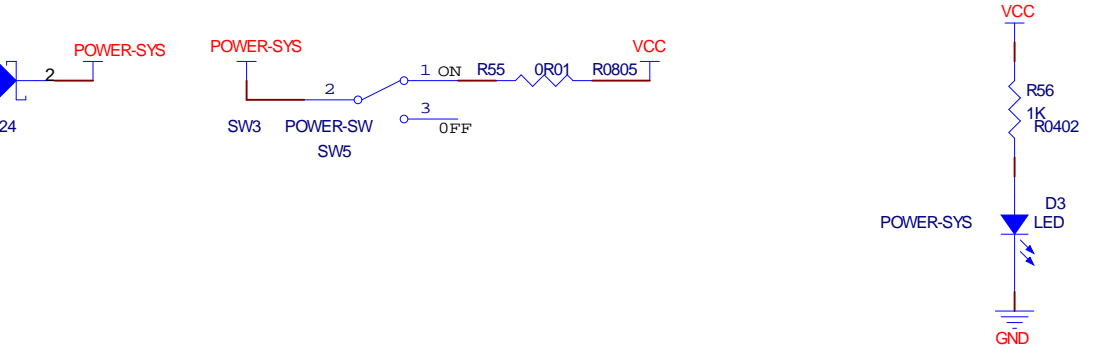
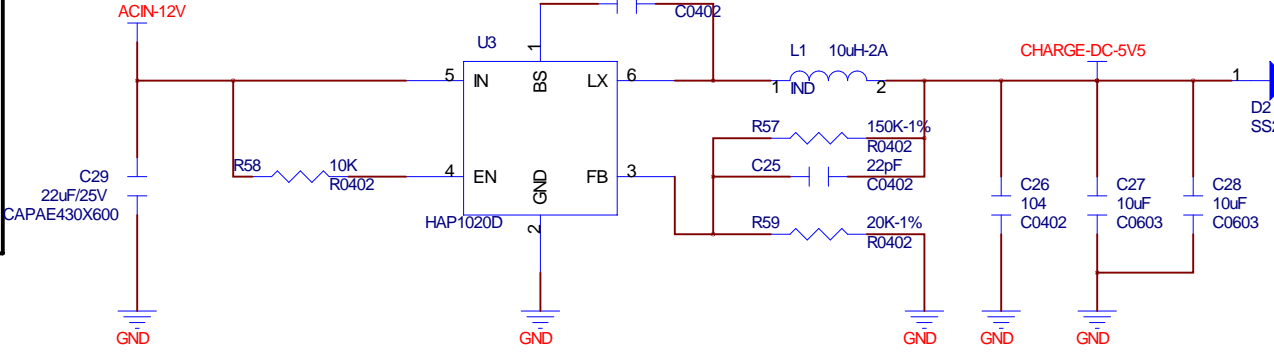
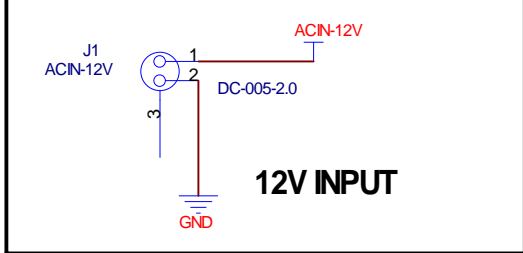


### RESET KEY

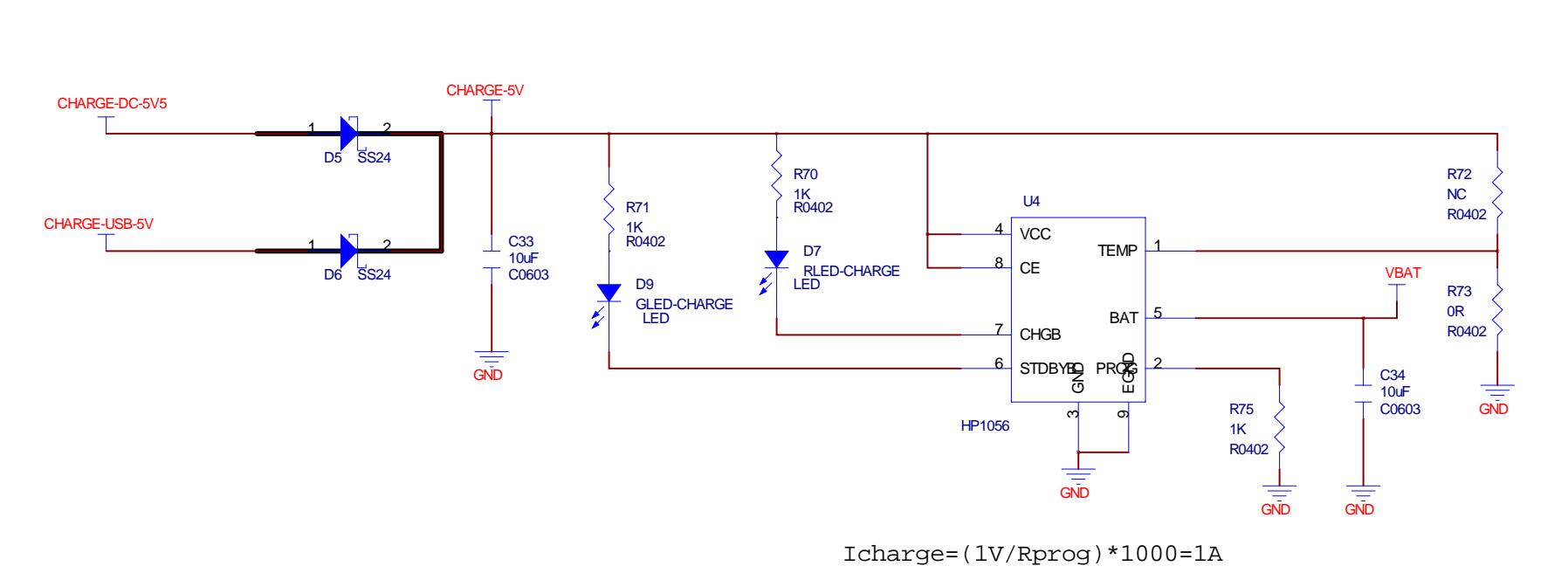


All placed closed to CPU

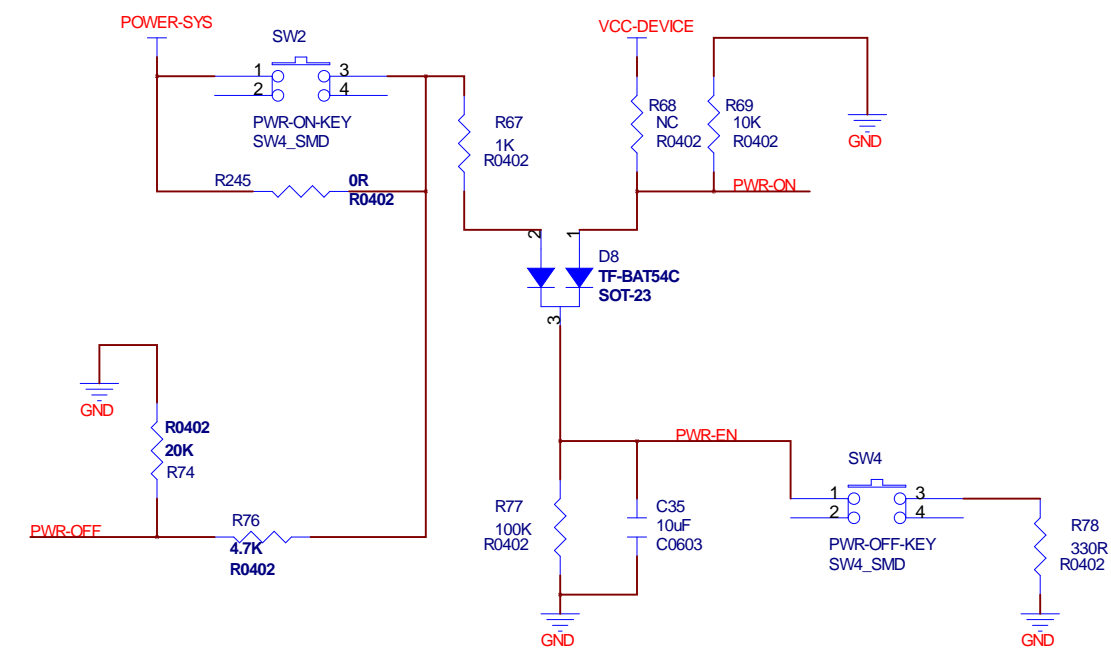
AllWinner Technology Co.,Ltd		
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## CHARGE CONTROL



## POWER-ON KEY



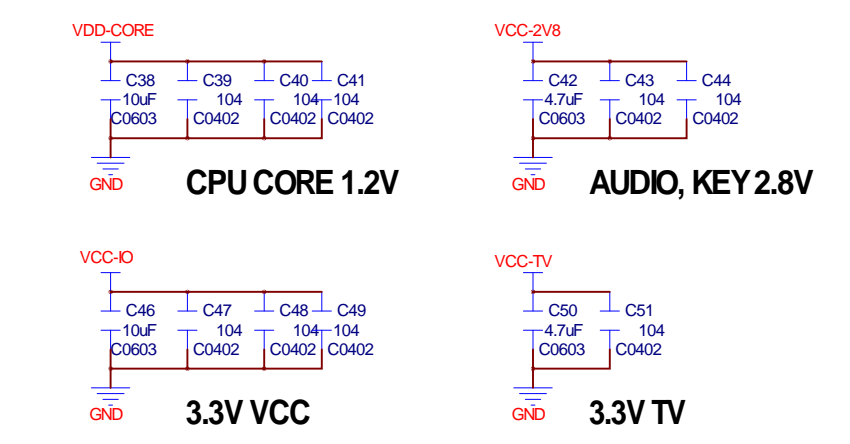
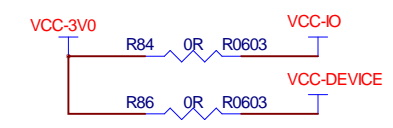
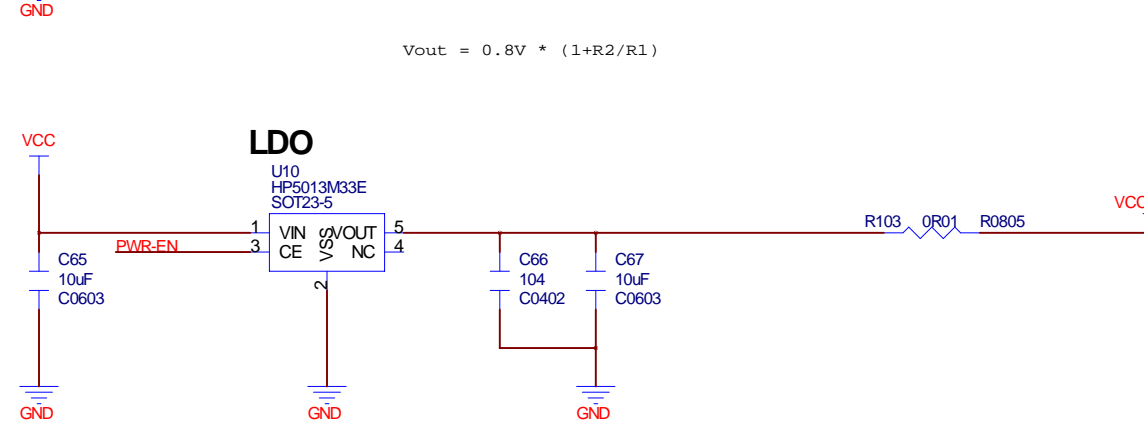
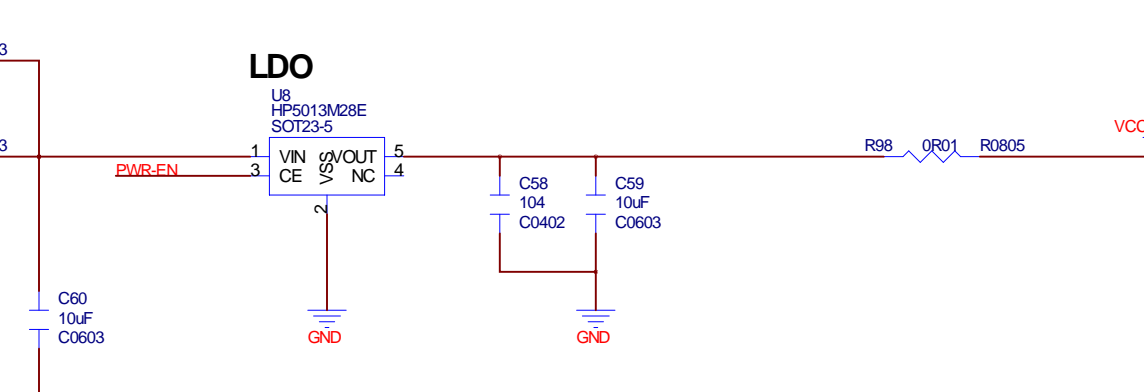
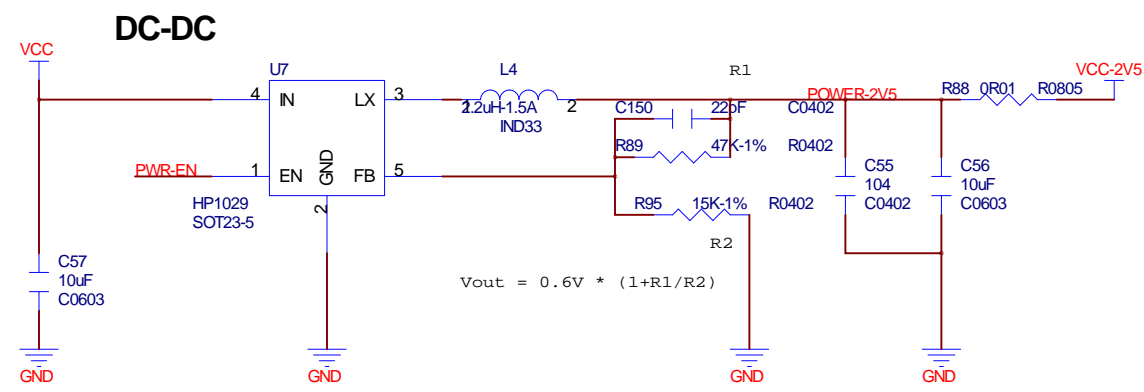
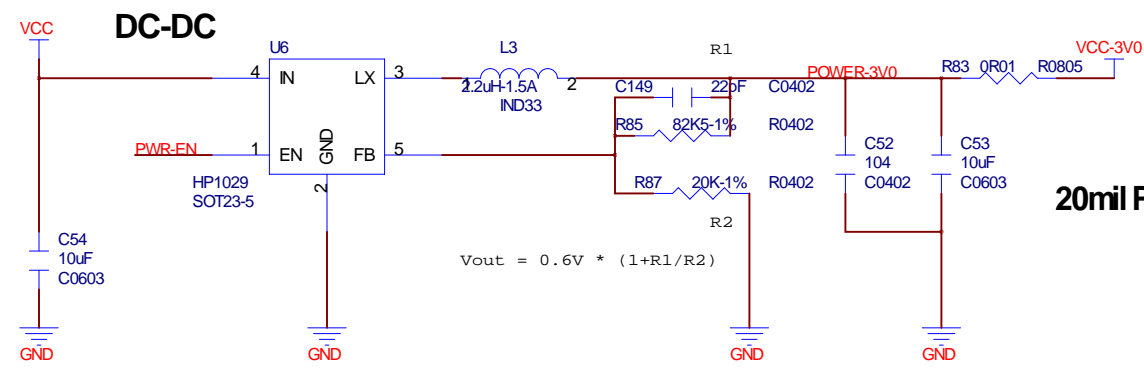
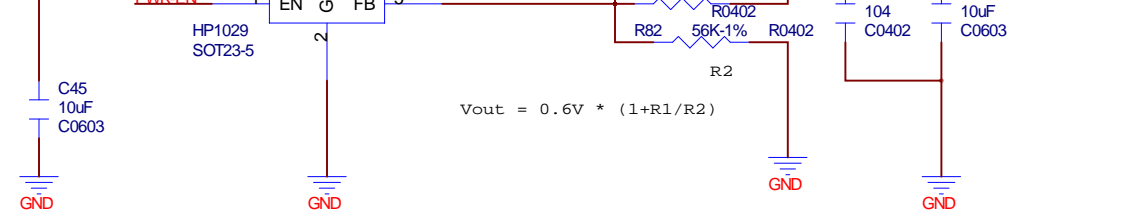
power up : SW KEY on , power down : PWR-EN keep high for 1000ms

Layout Guide : POWER-SW's ON , OFF silkscreen text needed

- Layout: power line 60mil
- Layout: power line 40mil
- Layout: power line 30mil

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Size <b>B</b>	Page Name <b>POWER 1</b>
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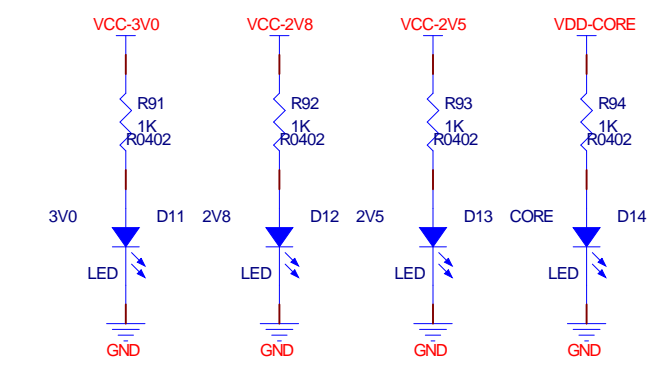
5 PWR-EN ← PWR-EN



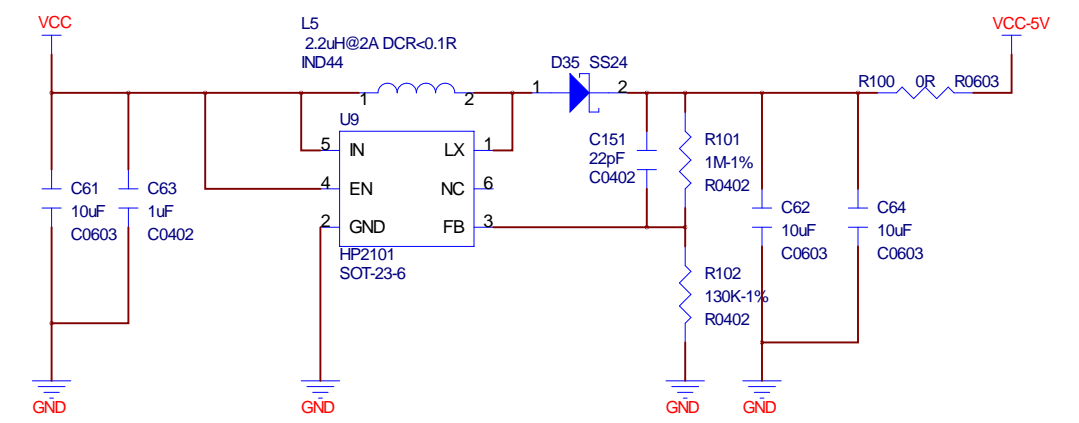
20mil PCB line width

Ensure one Power pin with a cap  
place caps closed to CPU

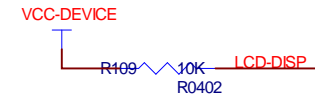
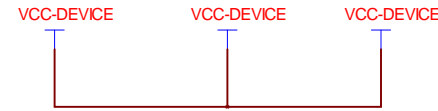
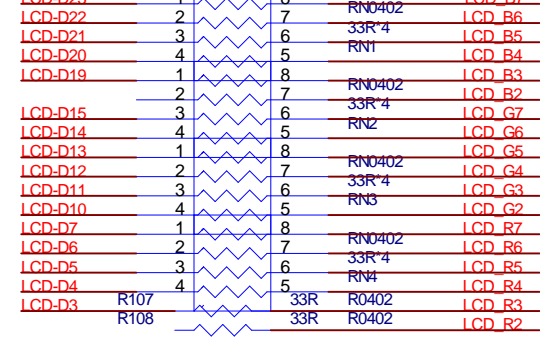
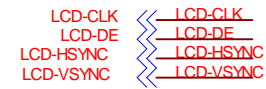
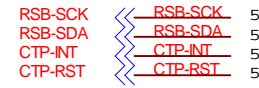
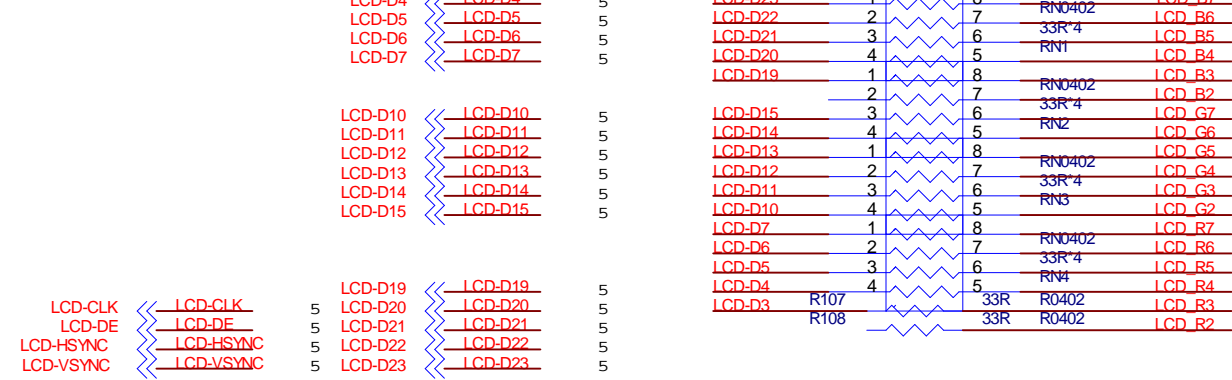
### POWER LED



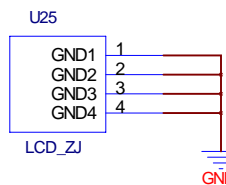
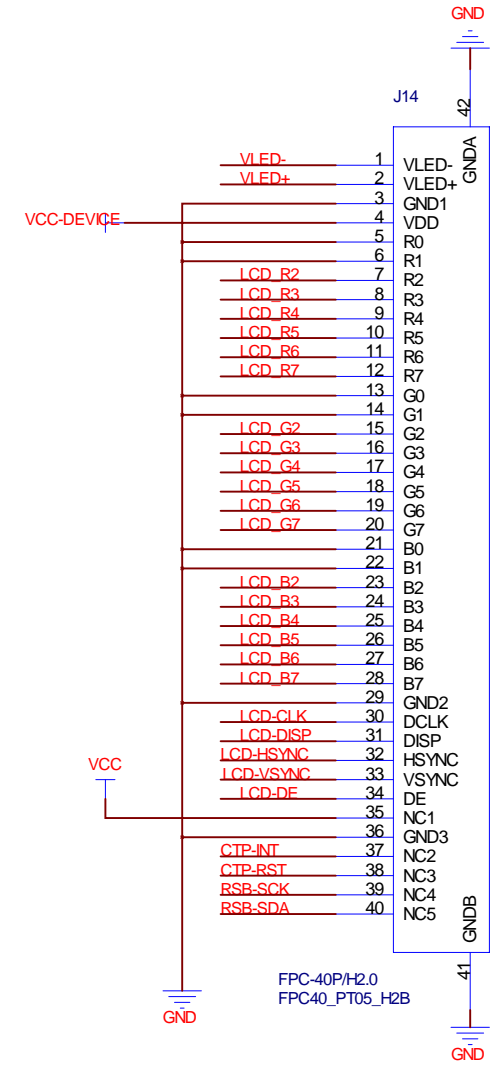
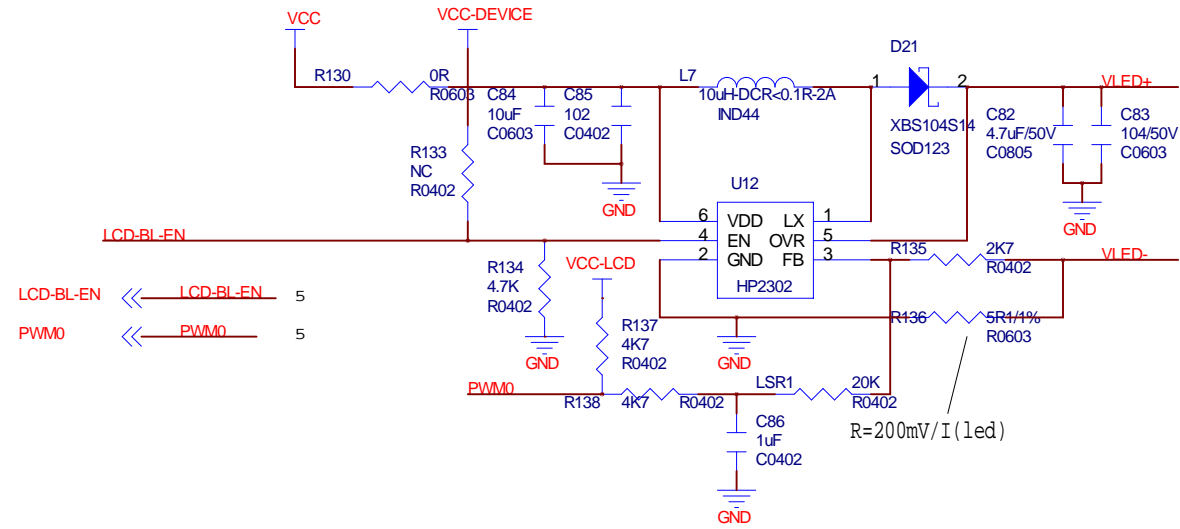
place at a same domain



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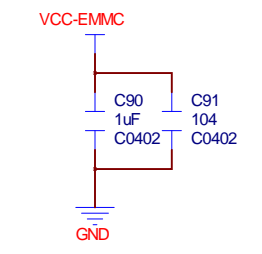
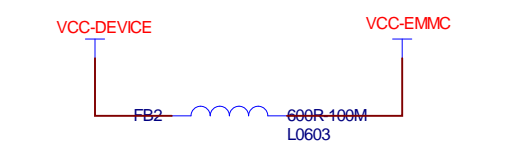
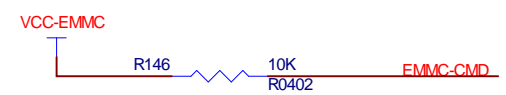


**BACK LIGHT**

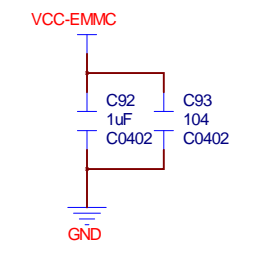


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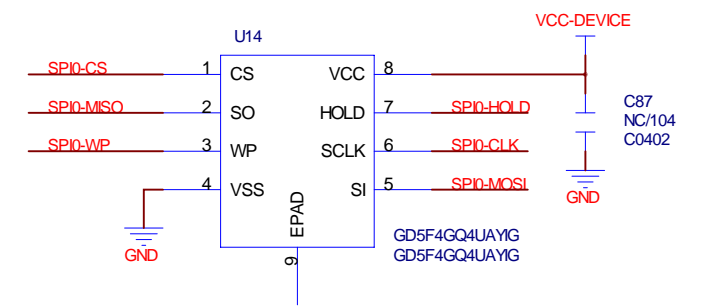
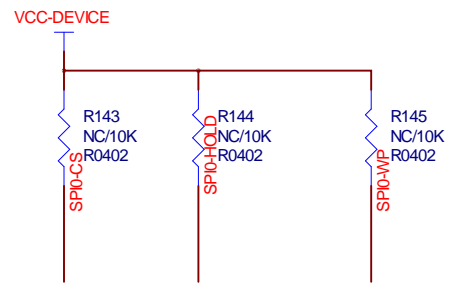
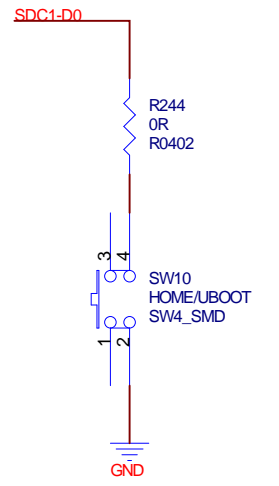
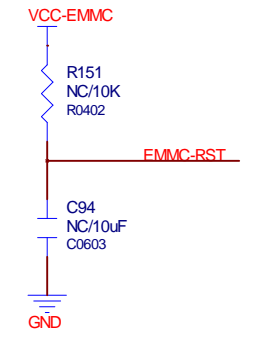
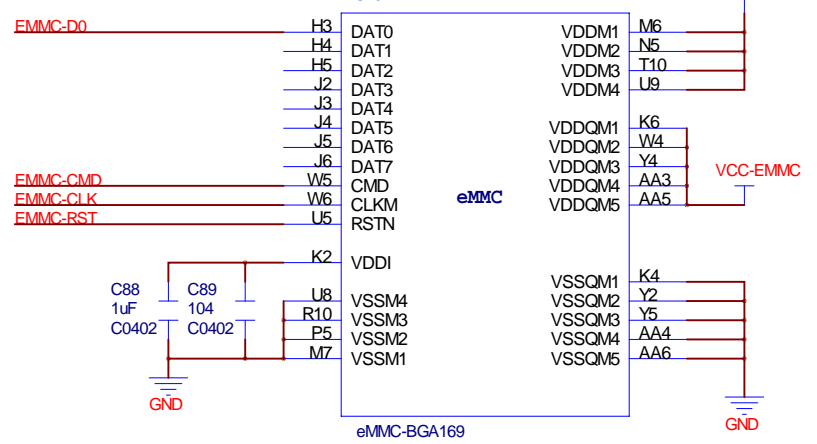
- 5 SDC1-CLK < R139 R0402 0R EMMC-CLK
- 5 SDC1-CMD < R140 R0402 0R EMMC-CMD
- 5 SDC1-D0 < R141 R0402 0R EMMC-D0
- 5 EMMC\_RST < R142 R0402 -NG/0R EMMC-RST



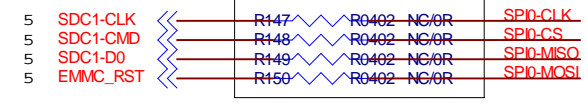
1/2µ eMMC VDDM1-VDDM4



1/2µ eMMC VDDQM1-VDDQM5

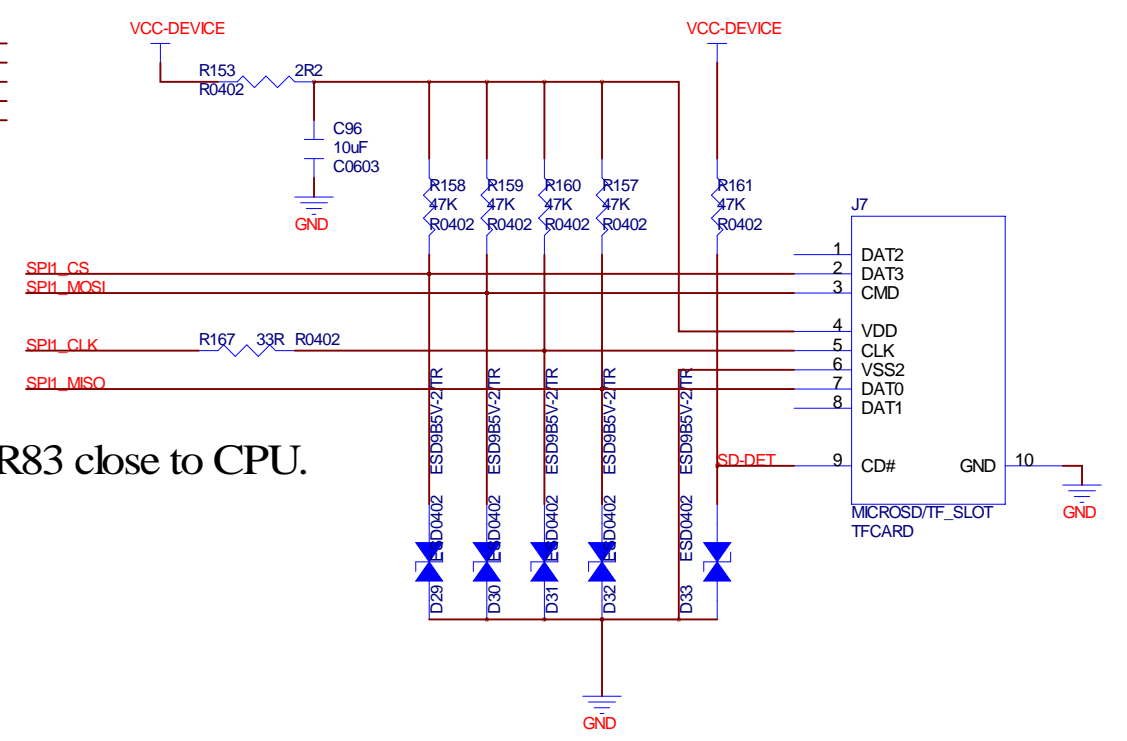


### Default NC



## SD CARD(OPTION)

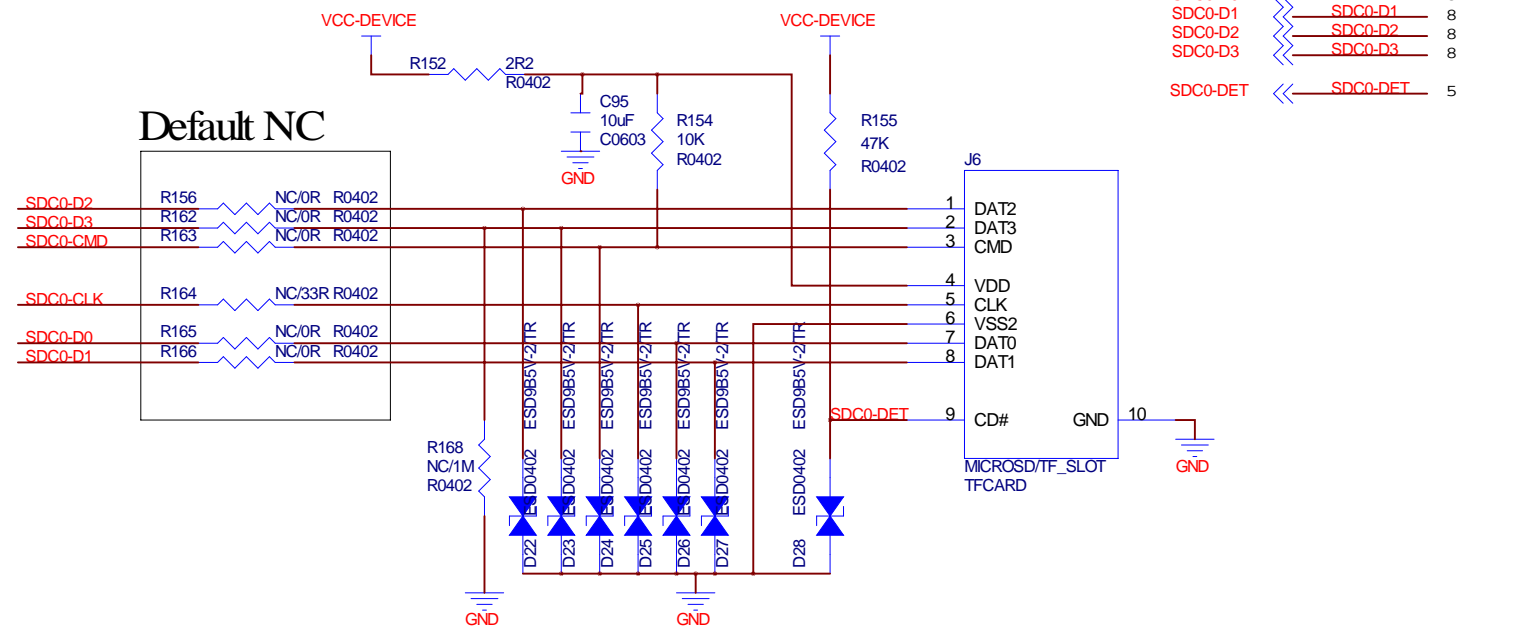
- 5 SPI1\_CS < R153 R0402 2R2 SPI1\_CS
- 5 SPI1\_CLK < R167 R0402 33R SPI1\_CLK
- 5 SPI1\_MOSI < R158 R0402 47K SPI1\_MOSI
- 5 SPI1\_MISO < R159 R0402 47K SPI1\_MISO
- 5 SD-DET < R160 R0402 47K SD-DET



Put R83 close to CPU.

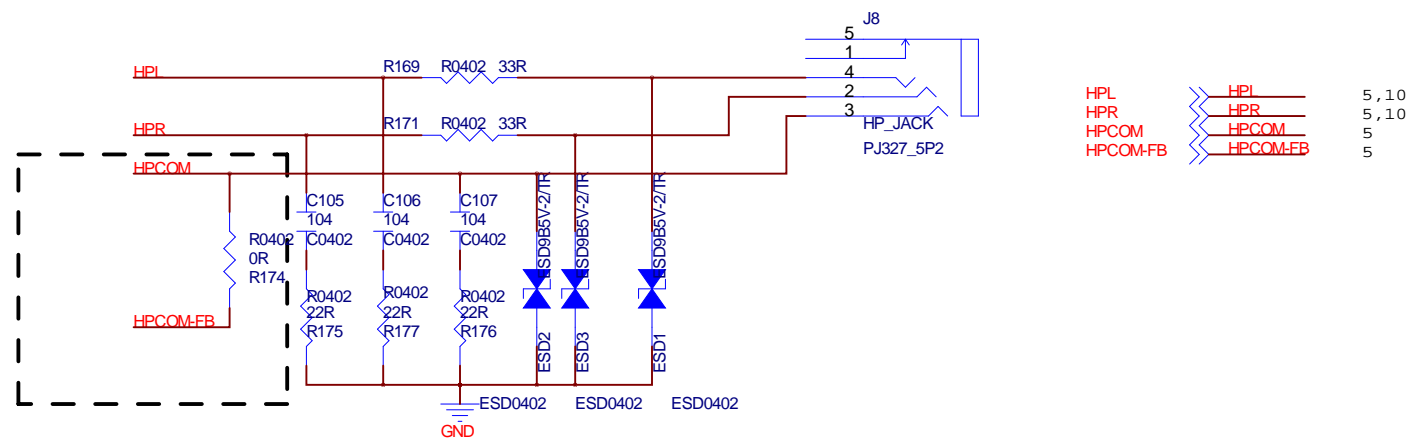
## Wi-Fi or SD

- SDC0-CMD < R163 R0402 SDC0-CMD
- SDC0-CLK < R164 R0402 SDC0-CLK
- SDC0-D0 < R165 R0402 SDC0-D0
- SDC0-D1 < R166 R0402 SDC0-D1
- SDC0-D2 < R162 R0402 SDC0-D2
- SDC0-D3 < R163 R0402 SDC0-D3
- SDC0-DET < R155 R0402 SDC0-DET

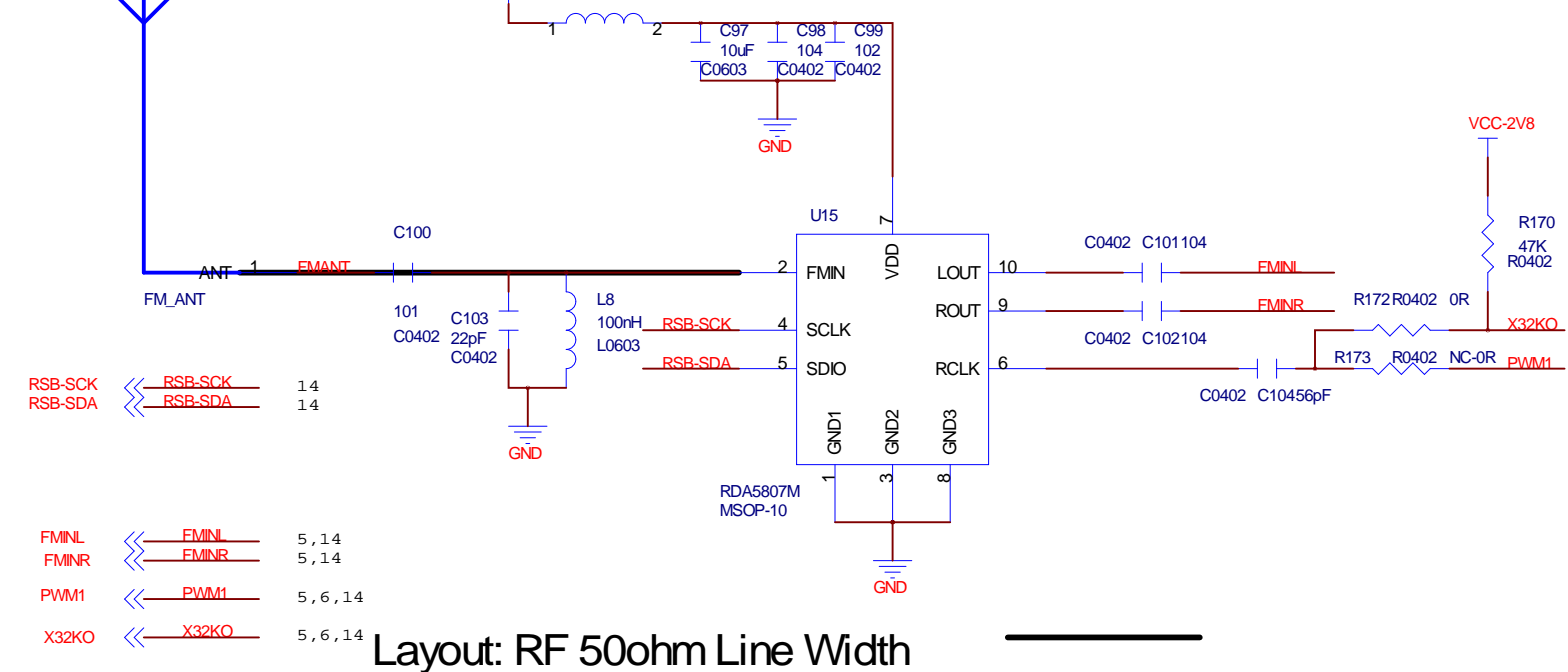


<b>Allwinner Technology Co.,Ltd</b>		
Design Name		
<b>AW_F1C200s_V10</b>		
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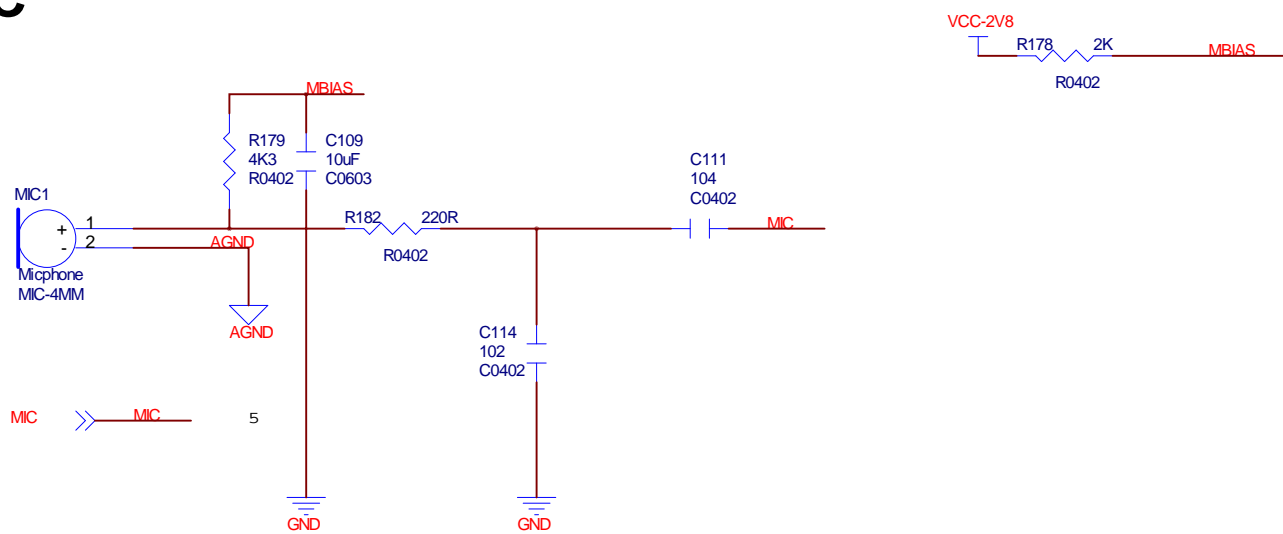
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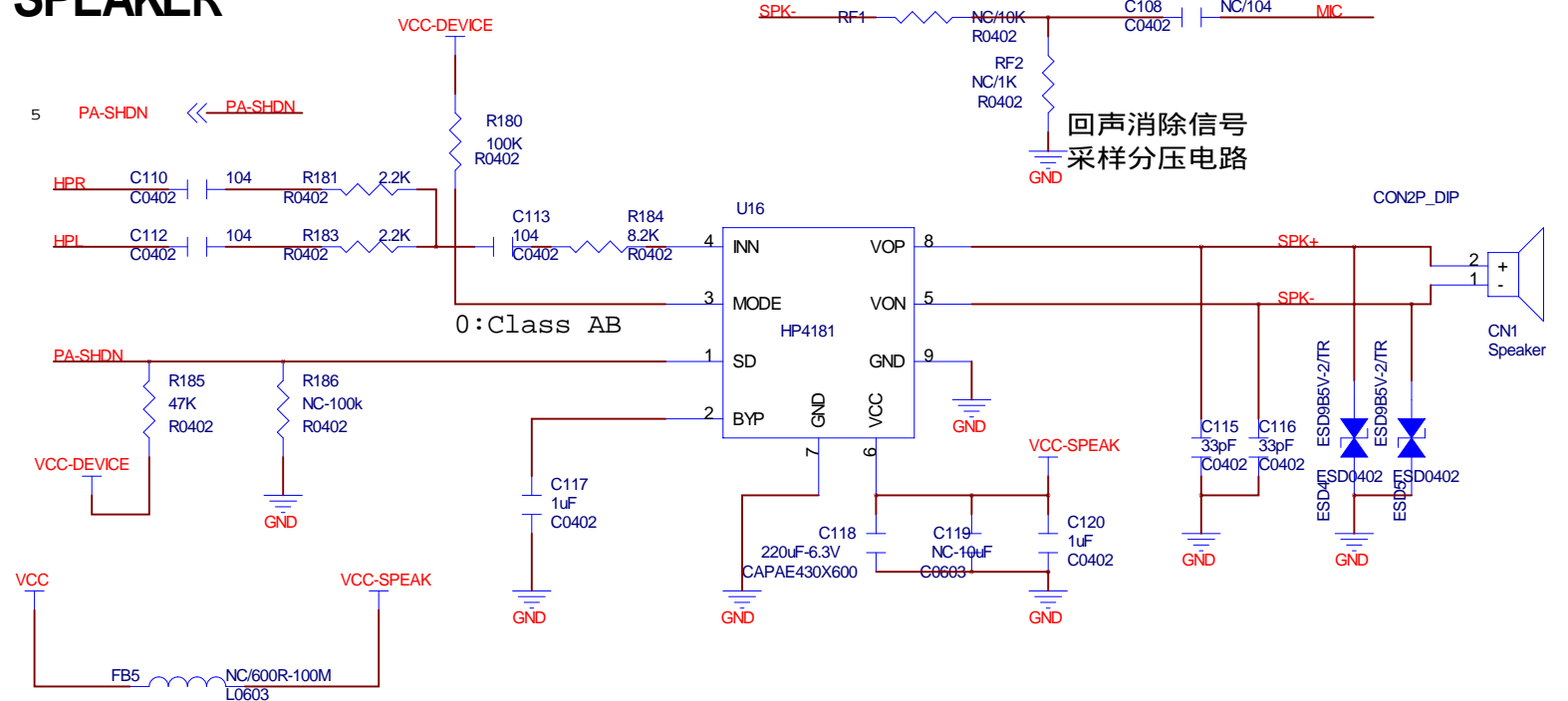
place closed to HP\_JACK



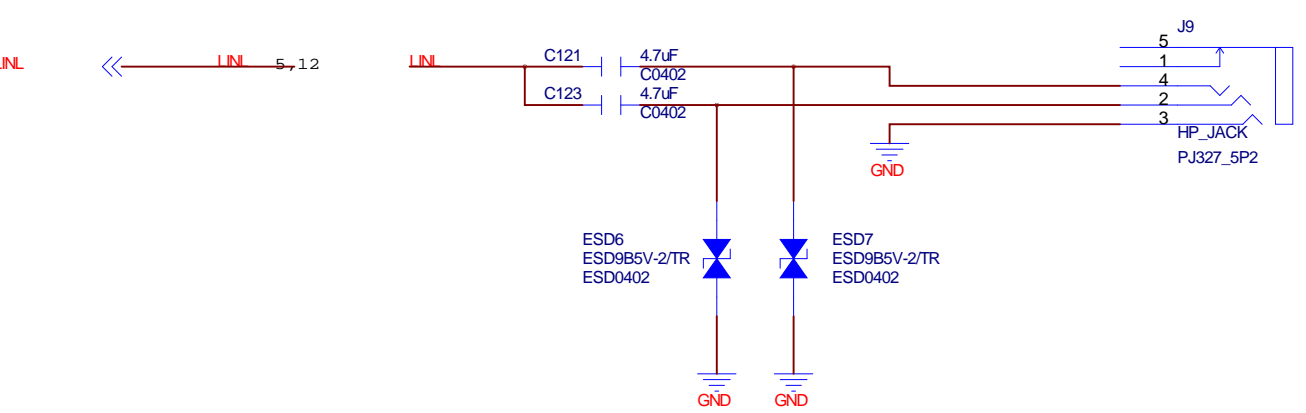
# MIC



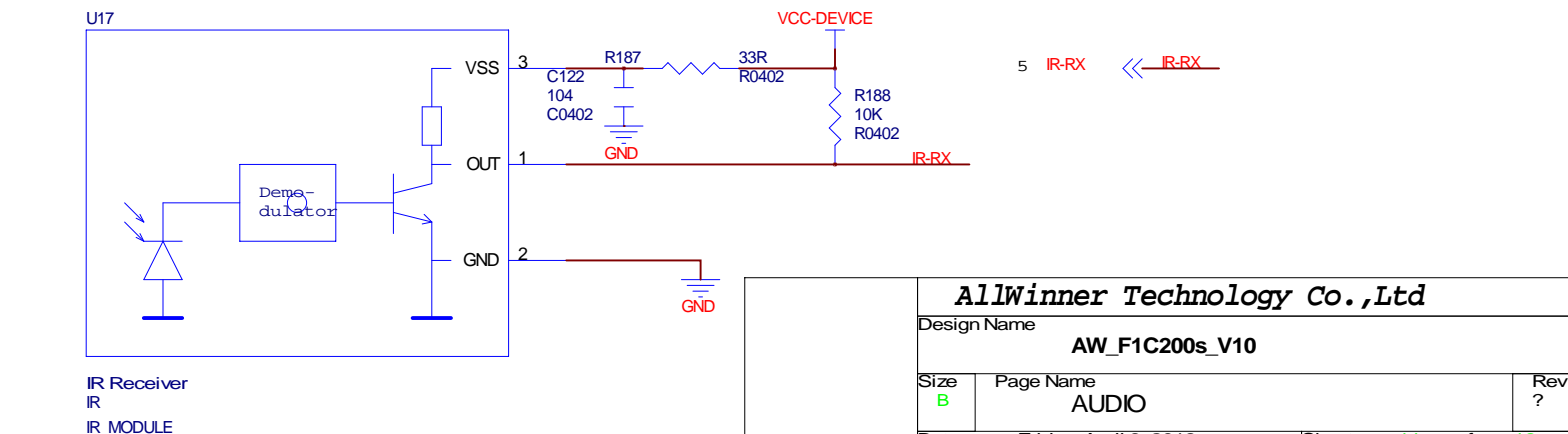
# SPEAKER

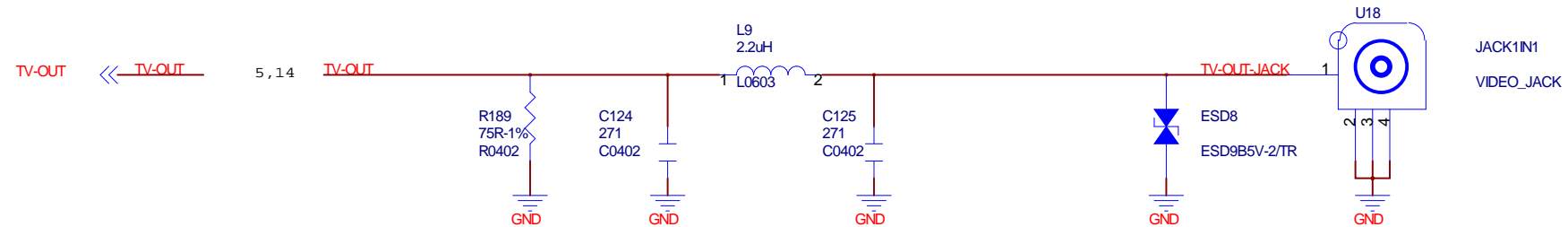


# LINE IN

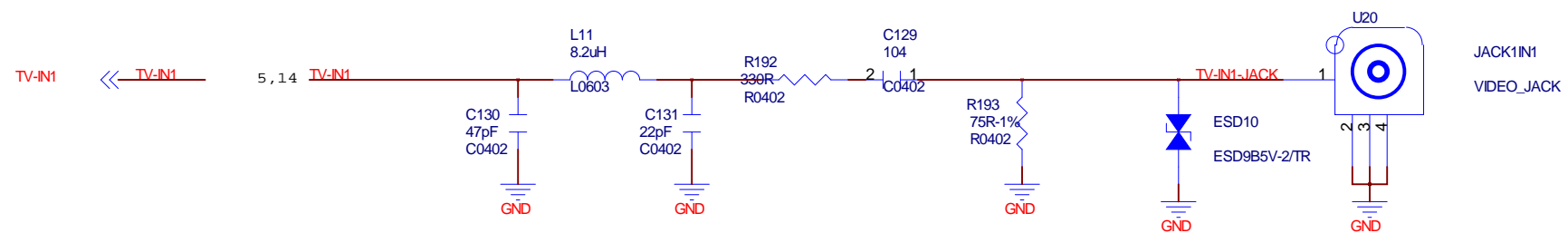
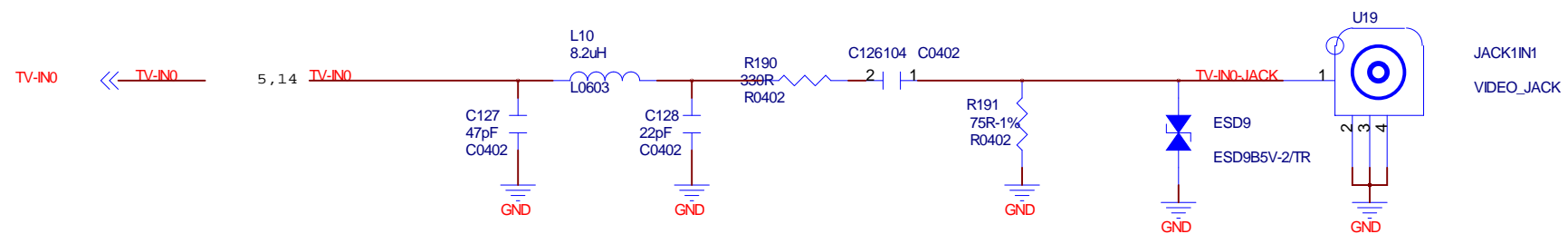


# IR MODULE



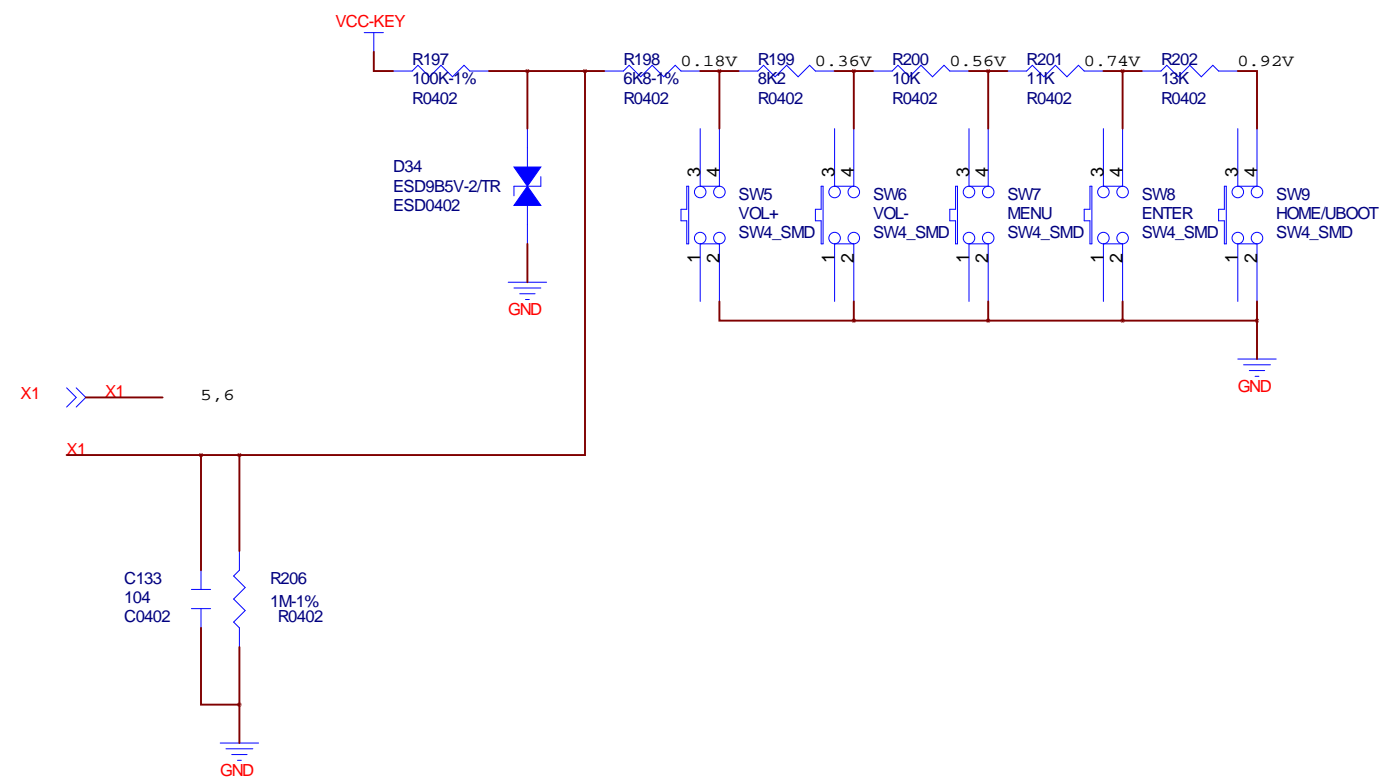


# TV IN

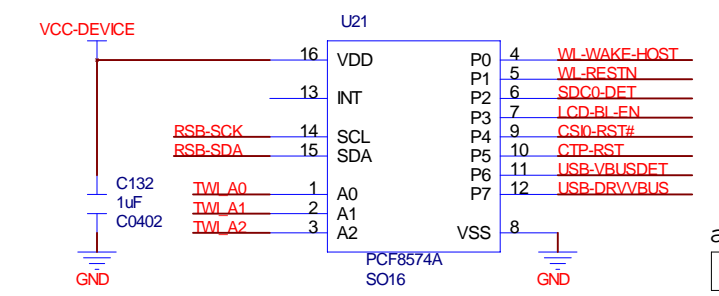
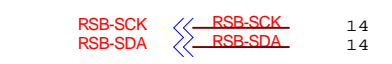


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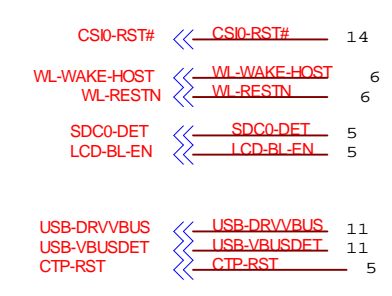
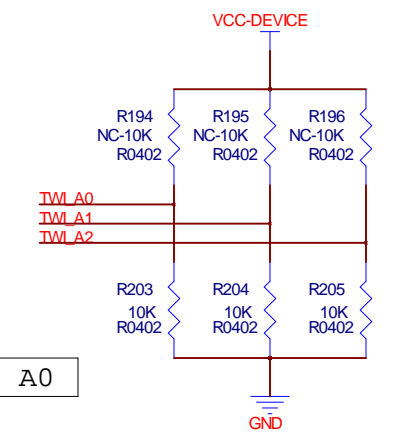
# KEY



# IO EXPAND



address  
0 1 0 0 A2 A1 A0

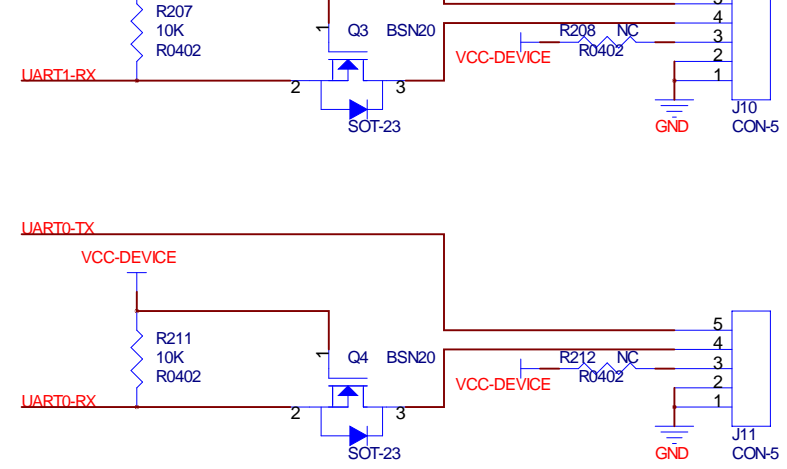


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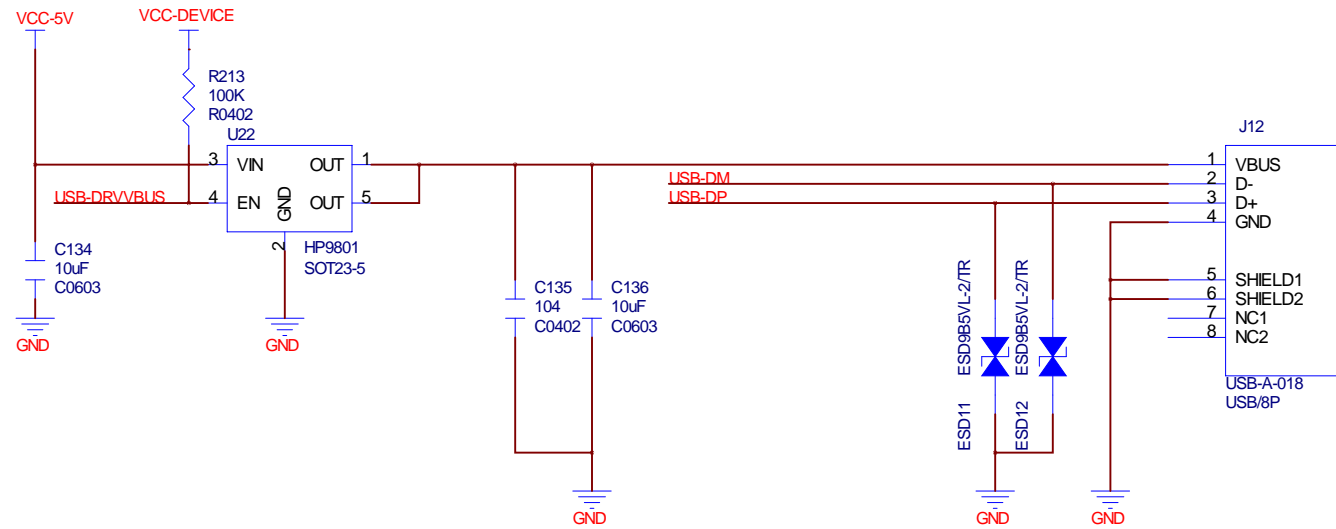
RSB-SCK  
RSB-SDA

RSB-SCK  
RSB-SDA

UART0-TX  
UART0-RX



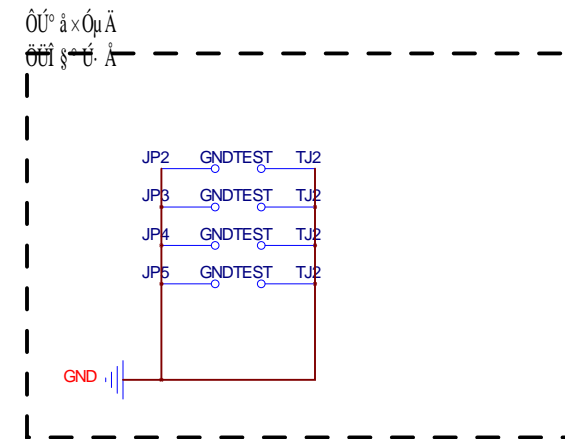
## USB OTG



5 USB-DP  
5 USB-DM

5 USB-DRVVBUS  
5 USB-VBUSDET

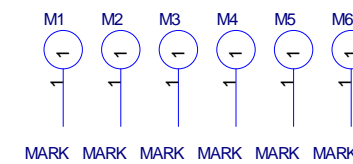
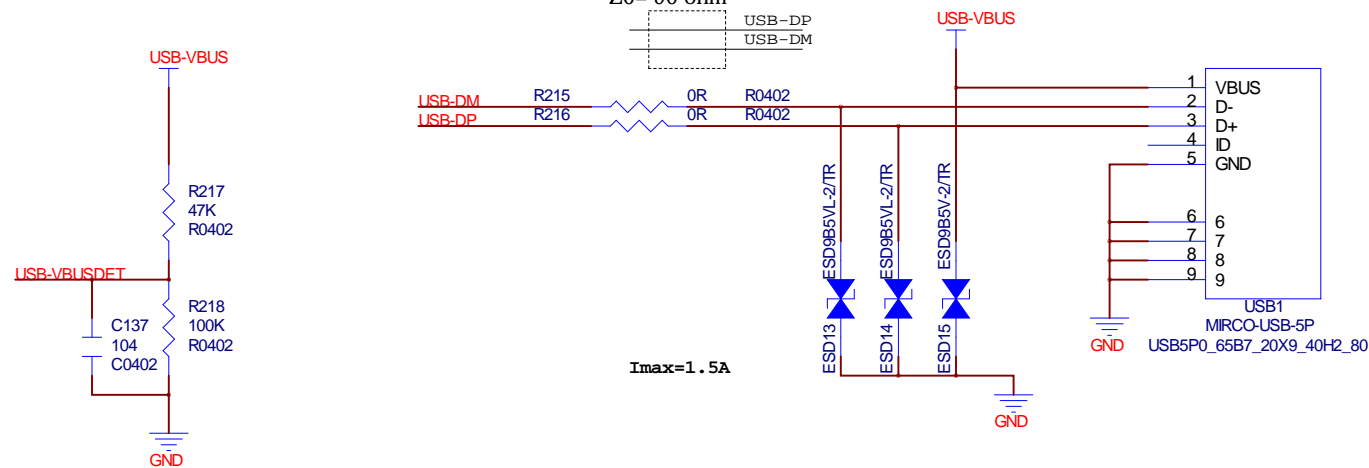
## TEST POINT



Differential pairs  
Z0= 90 ohm

USB-DP  
USB-DM

Imax=1.5A



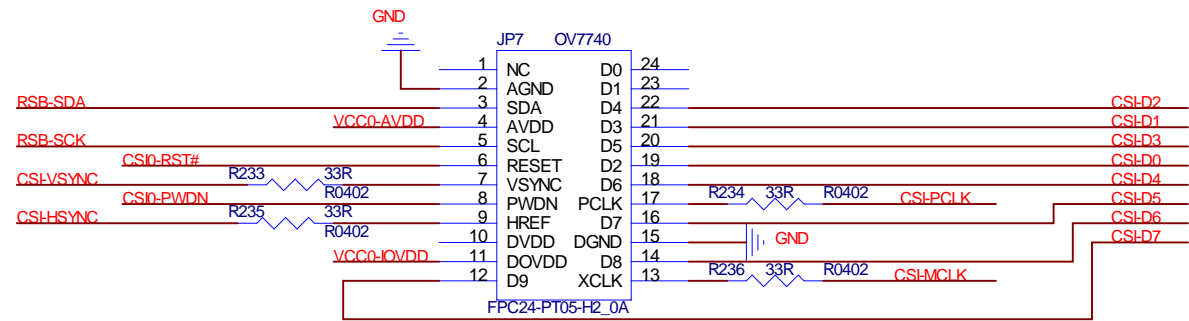
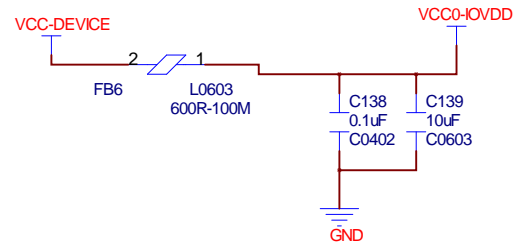
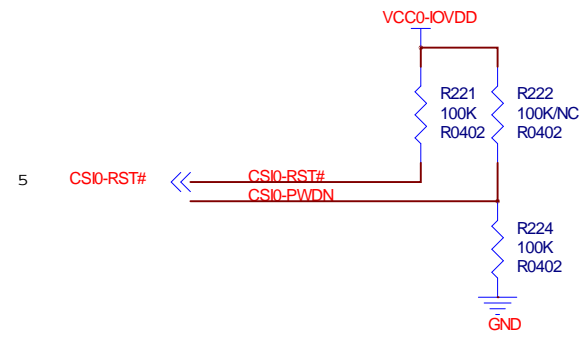
FPAD\_3

FPAD\_2

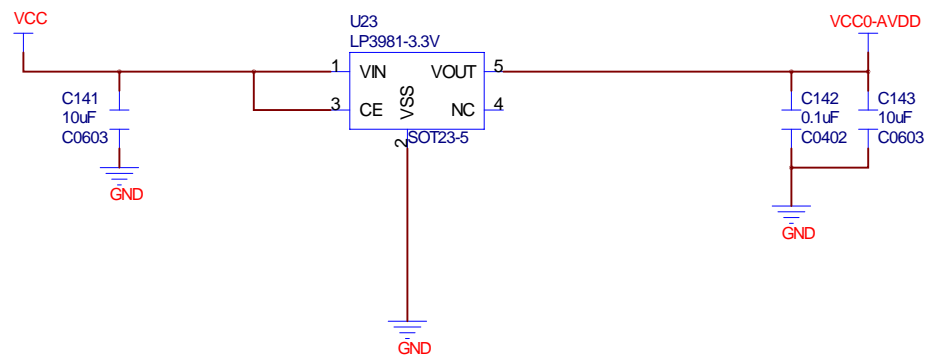
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5,6,13,1 RSB-SCK  
5,6,13,1 RSB-SDA

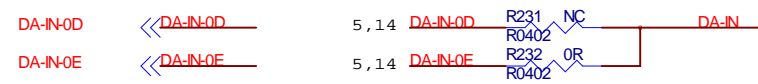
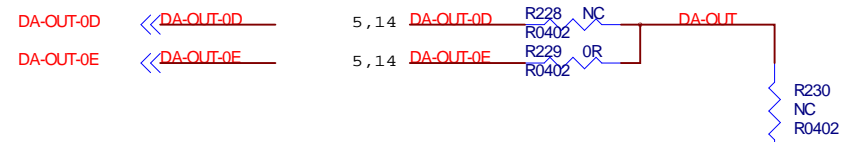
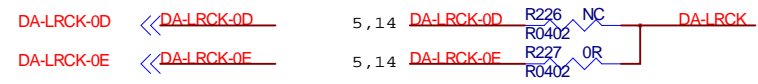
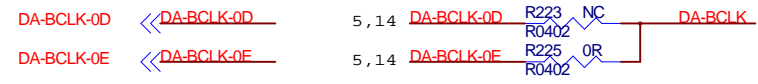
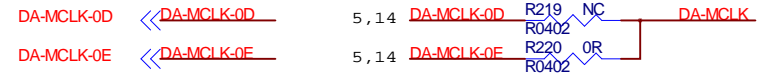
CSHD0 CSLD0 1,2  
CSHD1 CSLD1 1,2  
CSHD2 CSLD2 1,2  
CSHD3 CSLD3 1,2  
CSHD4 CSLD4 1,2  
CSHD5 CSLD5 1,2  
CSHD6 CSLD6 1,2  
CSHD7 CSLD7 1,2  
CSHVSNC CSLVSNC 1,2  
CSHHSNC CSLHSNC 1,2  
CSHPCLK CSLPCLK 1,2  
CSHMCLK CSLMCLK 1,2



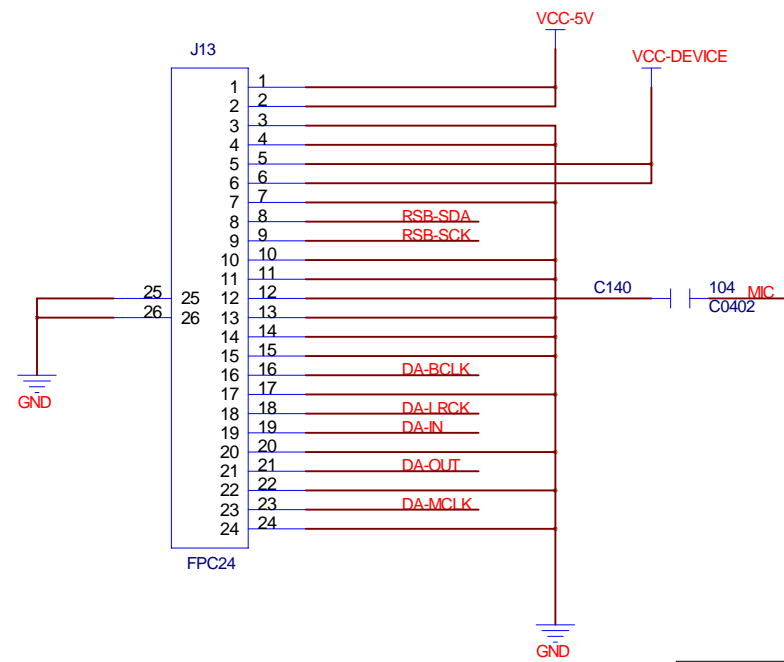
Ä-Ëi GC0308



RSB-SCK RSB-SCK 14  
RSB-SDA RSB-SDA 14

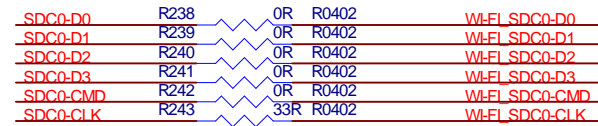
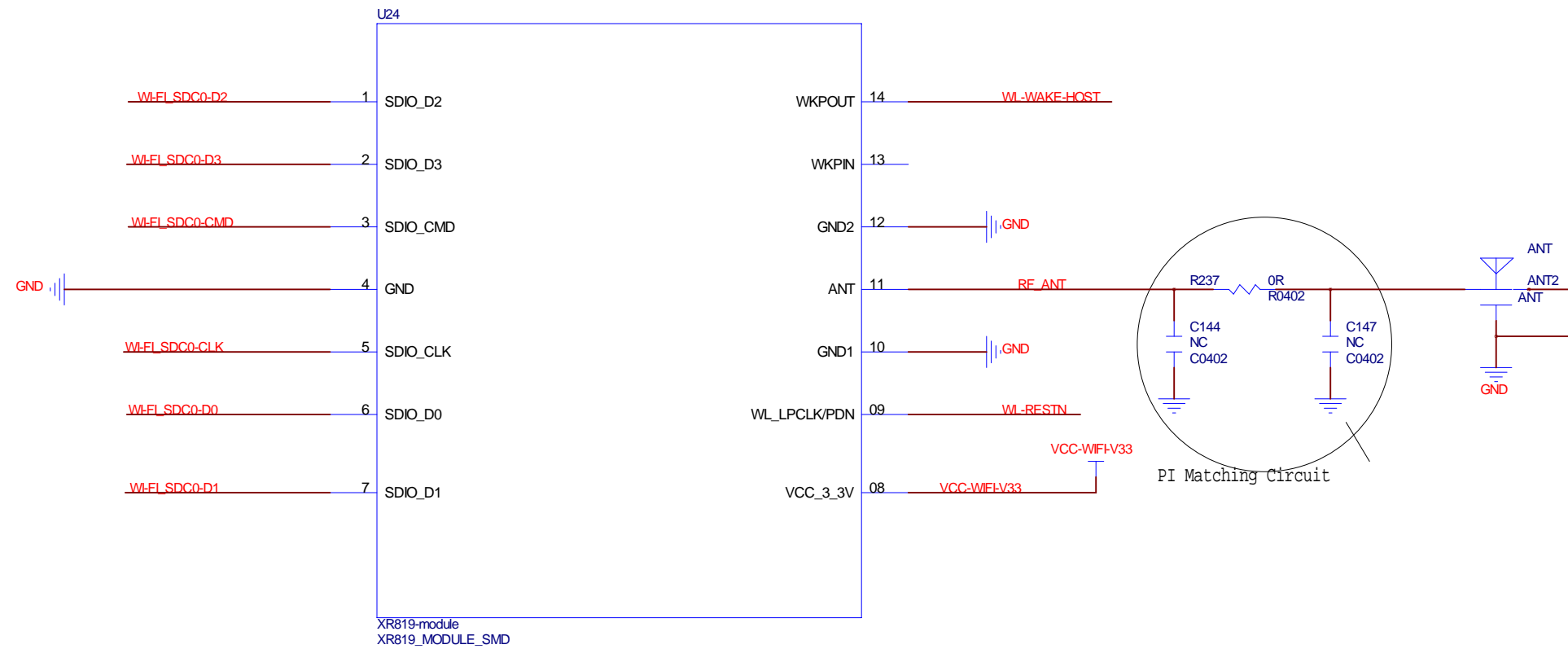
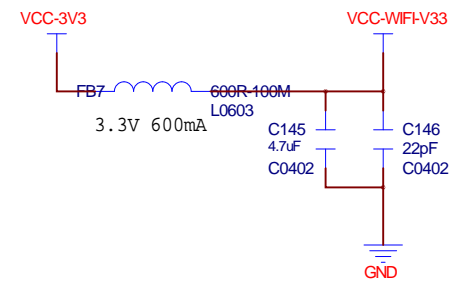


MIC MIC 5



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# XR819 ON BOARD



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