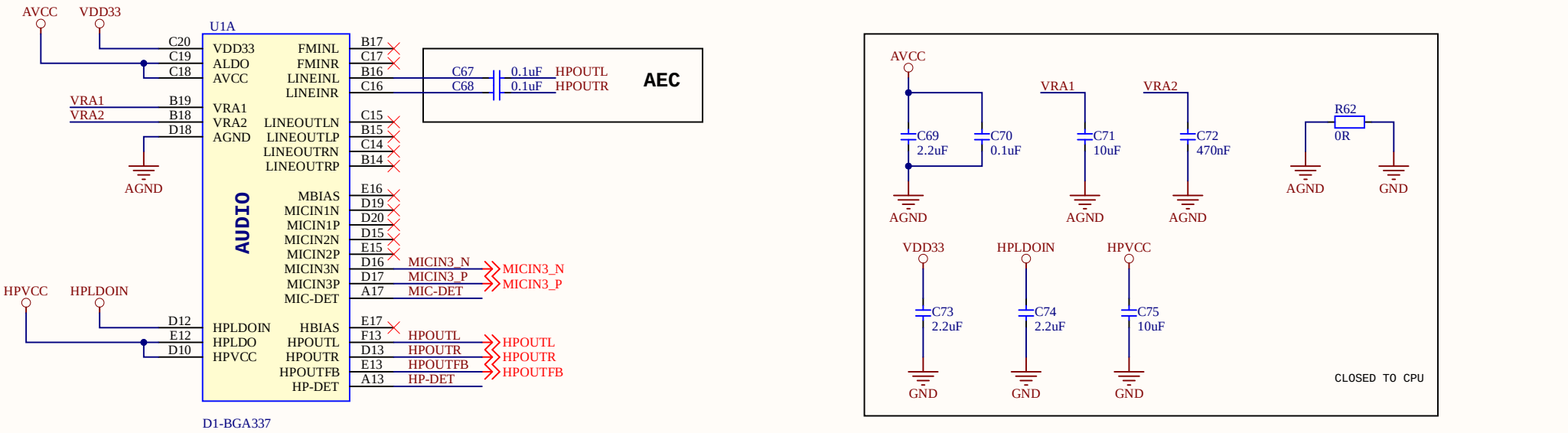





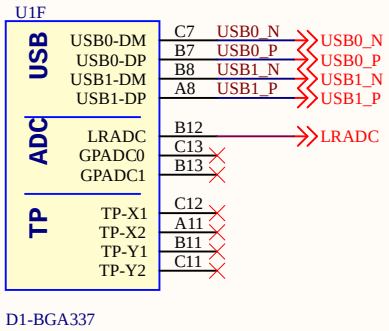



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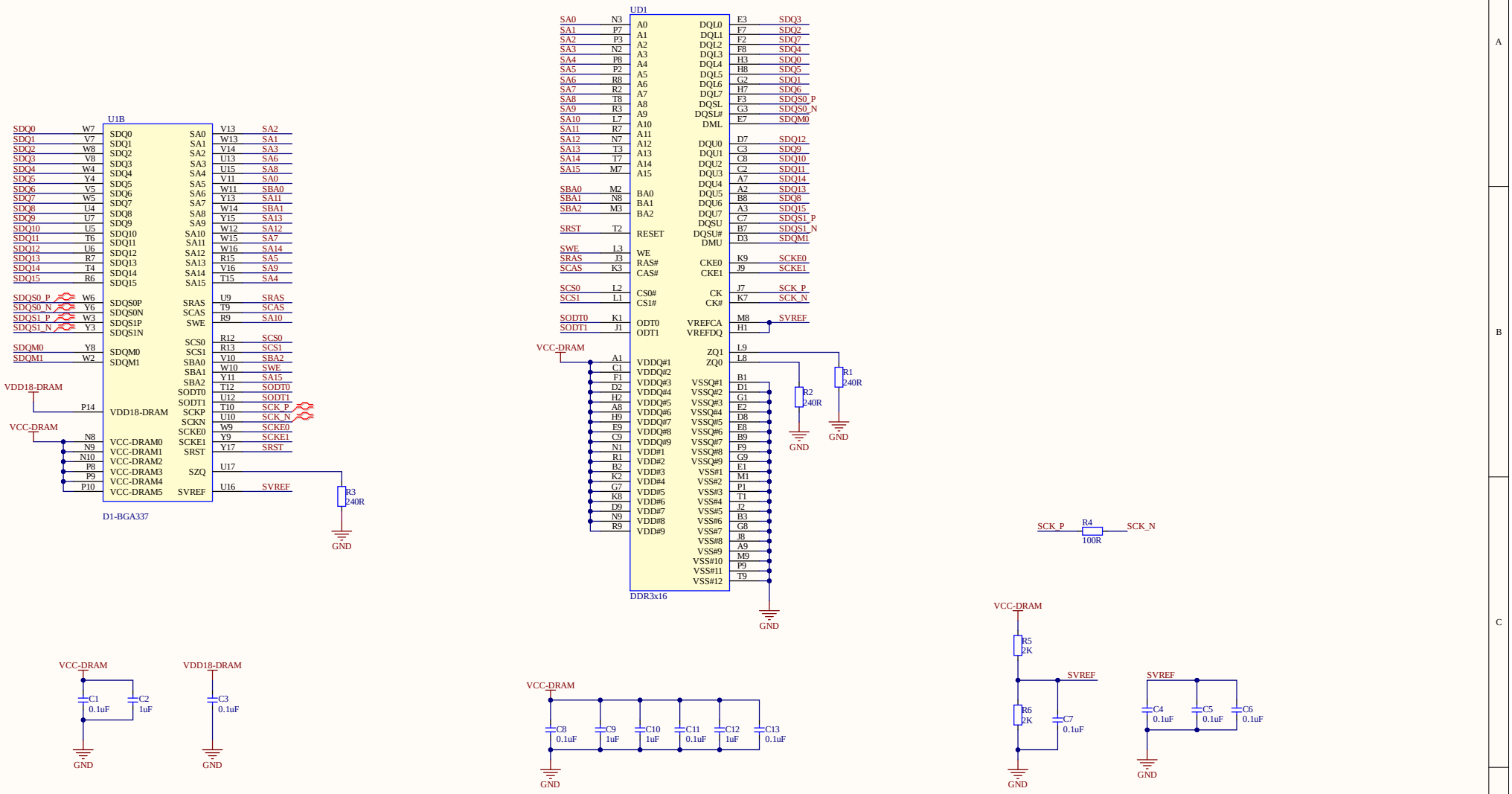


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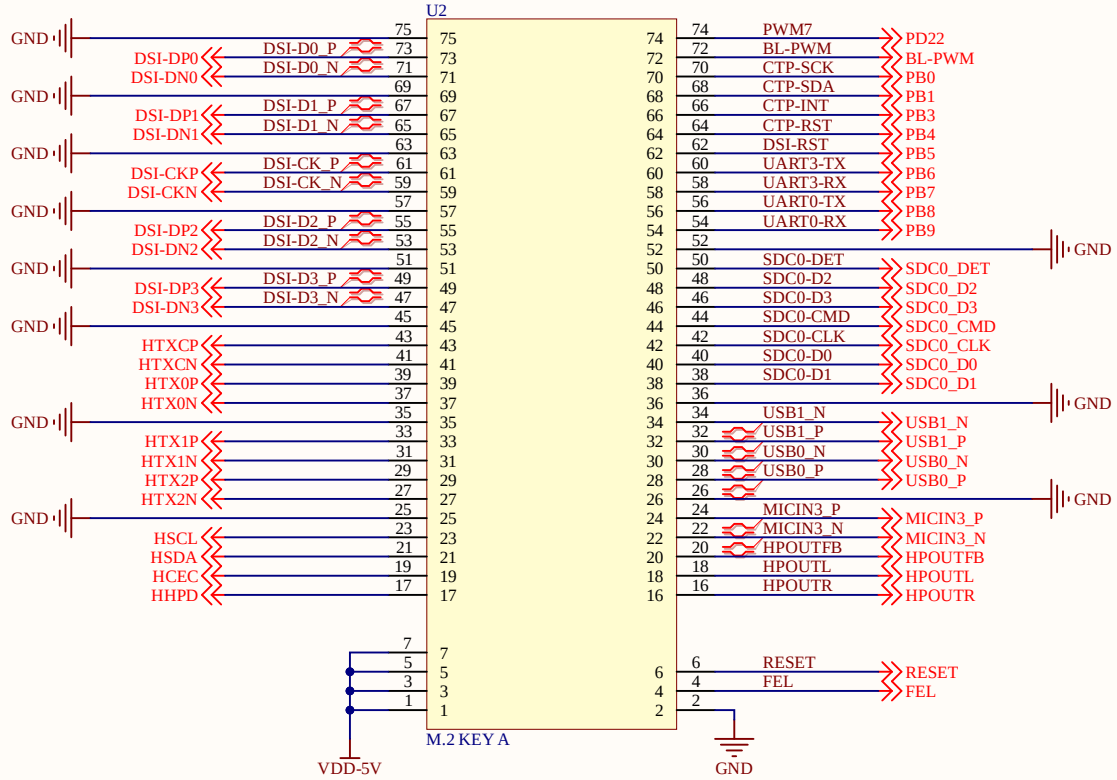


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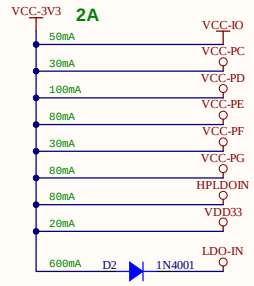


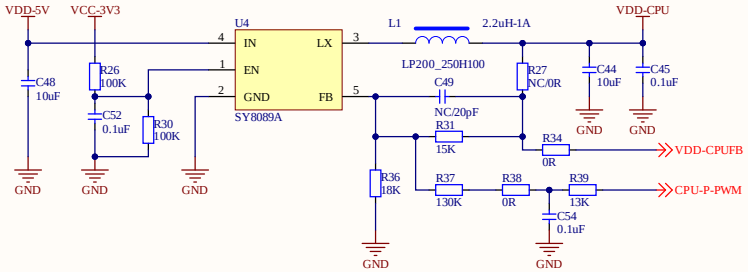


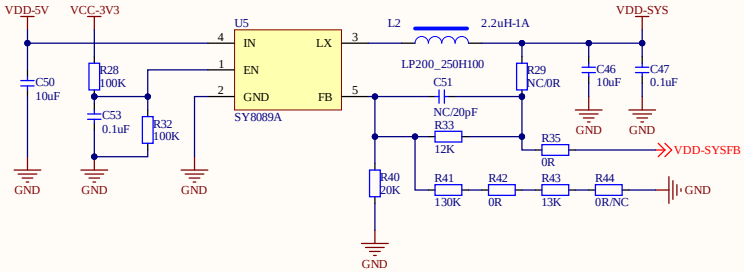


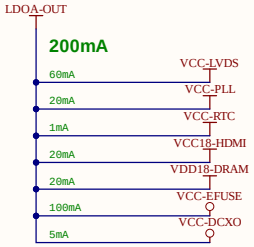


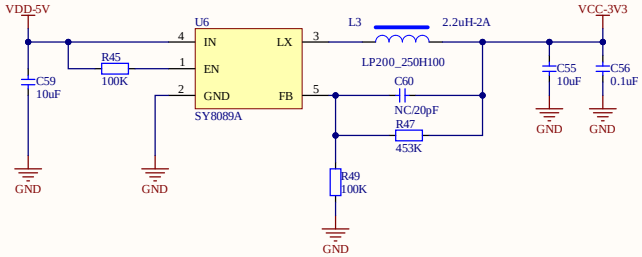


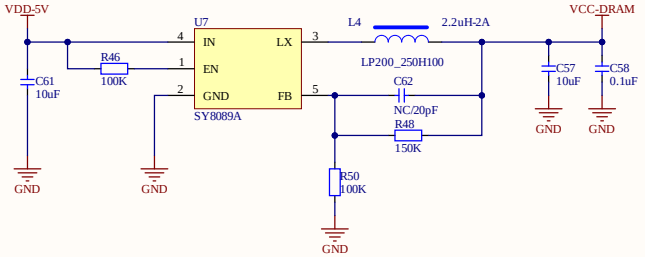


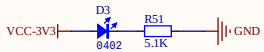


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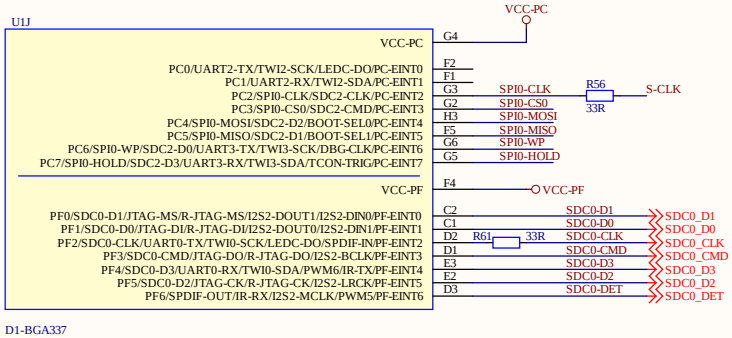
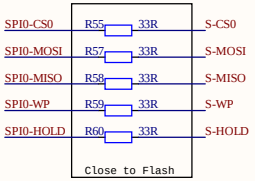
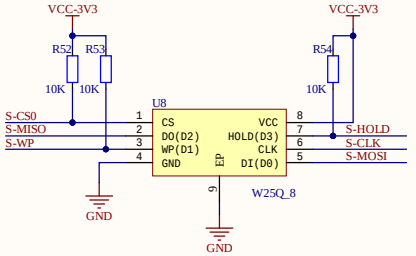
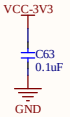
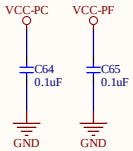


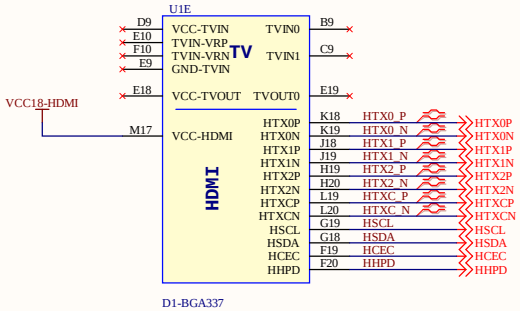

D1-BGA337



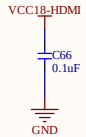




D1-BGA337

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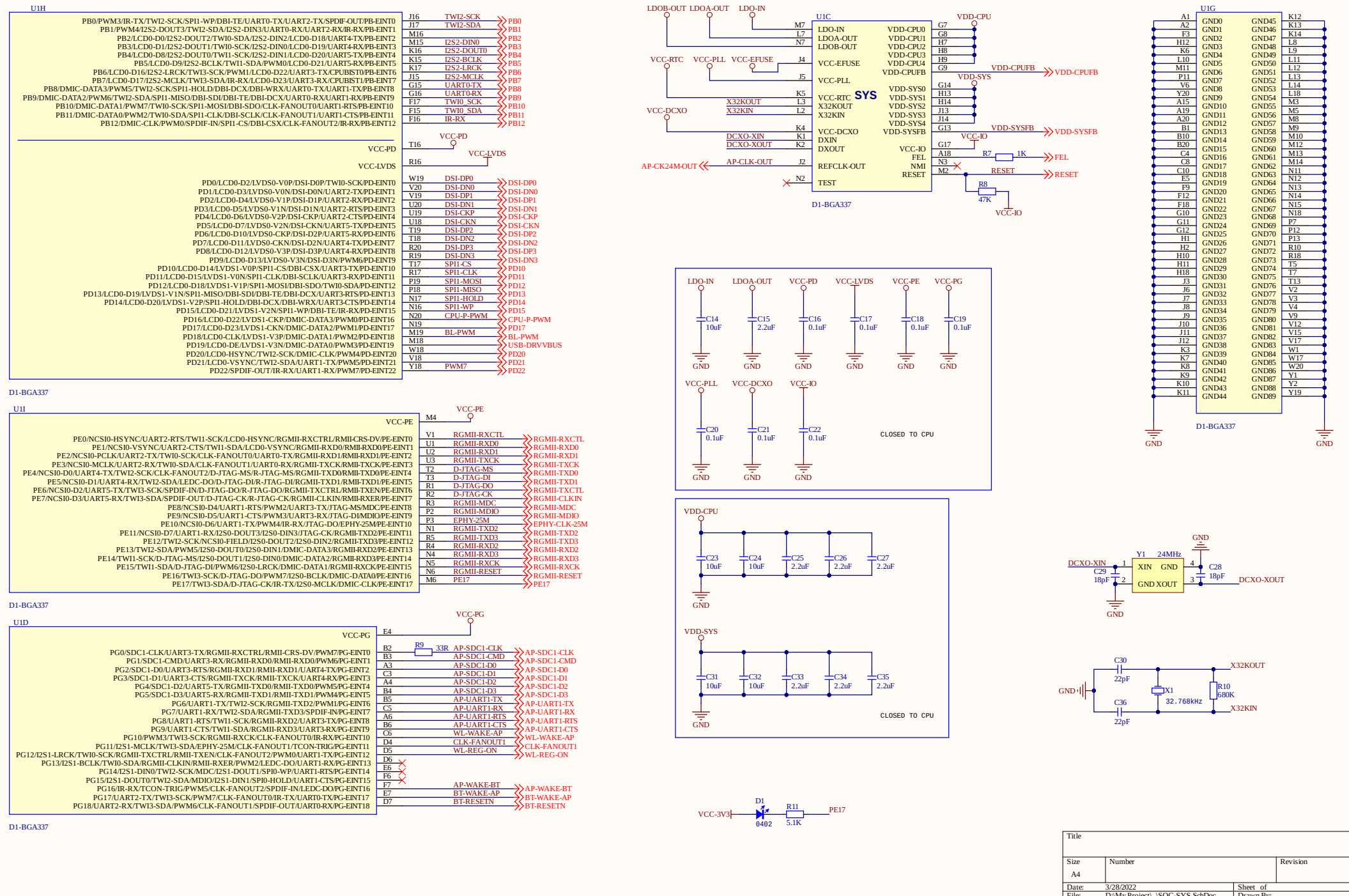
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PB1/PWM4/I2S2-DOUT3/TWI2-SDA/I2S2-DIN3/UART0-RX/UART2-RX/IR-RX/PB-EINT1
PB2/LCD0-D0/I2S2-DOUT2/TWI0-SDA/I2S2-DIN2/LCD0-D18/UART4-TX/PB-EINT2
PB3/LCD0-D1/I2S2-DOUT1/TWI0-SCK/I2S2-DIN0/LCD0-D19/UART4-RX/PB-EINT3
PB4/LCD0-D8/I2S2-DOUT0/TWI1-SCK/I2S2-DIN1/LCD0-D20/UART5-TX/PB-EINT4
PB5/LCD0-D9/I2S2-BCLK/TWI1-SDA/PWM0/LCD0-D21/UART5-RX/PB-EINT5
PB6/LCD0-D16/I2S2-LRCK/TWI3-SCK/PWM1/LCD0-D22/UART3-TX/CPUBIST0/PB-EINT6
PB7/LCD0-D17/I2S2-MCLK/TWI3-SDA/IR-RX/LCD0-D23/UART3-RX/CPUBIST1/PB-EINT7
PB8/DMIC-DATA3/PWM5/TWI2-SCK/SPI1-HOLD/DBI-DCX/DBI-WRX/UART0-TX/UART1-TX/PB-EINT8
PB9/DMIC-DATA2/PWM6/TWI2-SDA/SPI1-MISO/DBI-SDI/DBI-TE/DBI-DCX/UART0-RX/UART1-RX/PB-EINT9
PB10/DMIC-DATA1/PWM7/TWI0-SCK/SPI1-MOSI/DBI-SDO/CLK-FANOUT0/UART1-RTS/PB-EINT10
PB11/DMIC-DATA0/PWM2/TWI0-SDA/SPI1-CLK/DBI-SCLK/CLK-FANOUT1/UART1-CTS/PB-EINT11
PB12/DMIC-CLK/PWM0/SPDIF-IN/SPI1-CS/DBI-CSX/CLK-FANOUT2/IR-RX/PB-EINT12
VCC-PD
VCC-LVDS
PD0/LCD0-D2/LVDS0-V0P/DSI-D0P/TWI0-SCK/PD-EINT0
PD1/LCD0-D3/LVDS0-V0N/DSI-D0N/UART2-TX/PD-EINT1
PD2/LCD0-D4/LVDS0-V1P/DSI-D1P/UART2-RX/PD-EINT2
PD3/LCD0-D5/LVDS0-V1N/DSI-D1N/UART2-RTS/PD-EINT3
PD4/LCD0-D6/LVDS0-V2P/DSI-CKP/UART2-CTS/PD-EINT4
PD5/LCD0-D7/LVDS0-V2N/DSI-CKN/UART5-TX/PD-EINT5
PD6/LCD0-D10/LVDS0-CKP/DSI-D2P/UART5-RX/PD-EINT6
PD7/LCD0-D11/LVDS0-CKN/DSI-D2N/UART4-TX/PD-EINT7
PD8/LCD0-D12/LVDS0-V3P/DSI-D3P/UART4-RX/PD-EINT8
PD9/LCD0-D13/LVDS0-V3N/DSI-D3N/PWM6/PD-EINT9
PD10/LCD0-D14/LVDS1-V0P/SPI1-CS/DBI-CSX/UART3-TX/PD-EINT10
PD11/LCD0-D15/LVDS1-V0N/SPI1-CLK/DBI-SCLK/UART3-RX/PD-EINT11
PD12/LCD0-D18/LVDS1-V1P/SPI1-MOSI/DBI-SDO/TWI0-SDA/PD-EINT12
PD13/LCD0-D19/LVDS1-V1N/SPI1-MISO/DBI-SDI/DBI-TE/DBI-DCX/UART3-RTS/PD-EINT13
PD14/LCD0-D20/LVDS1-V2P/SPI1-HOLD/DBI-DCX/DBI-WRX/UART3-CTS/PD-EINT14
PD15/LCD0-D21/LVDS1-V2N/SPI1-WP/DBI-TE/IR-RX/PD-EINT15
PD16/LCD0-D22/LVDS1-CKP/DMIC-DATA3/PWM0/PD-EINT16
PD17/LCD0-D23/LVDS1-CKN/DMIC-DATA2/PWM1/PD-EINT17
PD18/LCD0-CLK/LVDS1-V3P/DMIC-DATA1/PWM2/PD-EINT18
PD19/LCD0-DE/LVDS1-V3N/DMIC-DATA0/PWM3/PD-EINT19
PD20/LCD0-HSYNC/TWI2-SCK/DMIC-CLK/PWM4/PD-EINT20
PD21/LCD0-VSYNC/TWI2-SDA/UART1-TX/PWM5/PD-EINT21
PD22/SPDIF-OUT/IR-RX/UART1-RX/PWM7/PD-EINT22
J16 TWI2-SCK
J17 TWI2-SDA
M16
M15 I2S2-DIN0
K16 I2S2-DOUT0
K15 I2S2-BCLK
K17 I2S2-LRCK
L15 I2S2-MCLK
G15 UART0-TX
G16 UART0-RX
F17 TWI0_SCK
F15 TWI0_SDA
F16 IR-RX
T16 VCC-PD
R16 VCC_LVDS
W19 DSI-DP0
V20 DSI-DN0
V19 DSI-DP1
U20 DSI-DN1
U19 DSI-CKP
U18 DSI-CKN
T19 DSI-DP2
T18 DSI-DN2
R20 DSI-DP3
R19 DSI-DN3
T17 SPI1-CS
R17 SPI1-CLK
P19 SPI1-MOSI
P18 SPI1-MISO
N17 SPI1-HOLD
N16 SPI1-WP
N20 CPU-P-PWM
N19
M19 BL-PWM
M18
W18
V18
Y18 PWM7
PB0 PB1 PB2 PB3 PB4 PB5 PB6 PB7 PB8 PB9 PB10 PB11 PB12
DSI-DP0 DSI-DN0 DSI-DP1 DSI-DN1 DSI-CKP DSI-CKN DSI-DP2 DSI-DN2 DSI-DP3 DSI-DN3
PD10 PD11 PD12 PD13 PD14 PD15
CPU-P-PWM PD17 BL-PWM USB-DRVBUS PD20 PD21 PD22
D1-BGA337
U1I
VCC-PE
M4
PE0/NCSI0-HSYNC/UART2-RTS/TWI1-SCK/LCD0-HSYNC/RGMII-RXCTRL/RMII-CRS-DV/PE-EINT0
PE1/NCSI0-VSYNC/UART2-CTS/TWI1-SDA/LCD0-VSYNC/RGMII-RXD0/RMII-RXD0/PE-EINT1
PE2/NCSI0-PCLK/UART2-TX/TWI0-SCK/CLK-FANOUT0/UART0-TX/RGMII-RXD1/RMII-RXD1/PE-EINT2
PE3/NCSI0-MCLK/UART2-RX/TWI0-SDA/CLK-FANOUT1/UART0-RX/RGMII-TXCK/RMII-TXCK/PE-EINT3
PE4/NCSI0-D0/UART4-TX/TWI2-SCK/CLK-FANOUT2/D-JTAG-MS/R-JTAG-MS/RGMII-TXD0/RMII-TXD0/PE-EINT4
PE5/NCSI0-D1/UART4-RX/TWI2-SDA/LEDC-DO/D-JTAG-DI/R-JTAG-DI/RGMII-TXD1/RMII-TXD1/PE-EINT5
PE6/NCSI0-D2/UART5-TX/TWI3-SCK/SPDIF-IN/D-JTAG-DO/R-JTAG-DO/RGMII-TXCTRL/RMII-TXEN/PE-EINT6
PE7/NCSI0-D3/UART5-RX/TWI3-SDA/SPDIF-OUT/D-JTAG-CK/R-JTAG-CK/RGMII-CLKIN/RMII-RXER/PE-EINT7
PE8/NCSI0-D4/UART1-RTS/PWM2/UART3-TX/JTAG-MS/MDC/PE-EINT8
PE9/NCSI0-D5/UART1-CTS/PWM3/UART3-RX/JTAG-DI/MDIO/PE-EINT9
PE10/NCSI0-D6/UART1-TX/PWM4/IR-RX/JTAG-DO/EPHY-25M/PE-EINT10
PE11/NCSI0-D7/UART1-RX/I2S0-DOUT3/I2S0-DIN3/JTAG-CK/RGMII-TXD2/PE-EINT11
PE12/TWI2-SCK/NCSI0-FIELD/I2S0-DOUT2/I2S0-DIN2/RGMII-TXD3/PE-EINT12
PE13/TWI2-SDA/PWM5/I2S0-DOUT0/I2S0-DIN1/DMIC-DATA3/RGMII-RXD2/PE-EINT13
PE14/TWI1-SCK/D-JTAG-MS/I2S0-DOUT1/I2S0-DIN0/DMIC-DATA2/RGMII-RXD3/PE-EINT14
PE15/TWI1-SDA/D-JTAG-DI/PWM6/I2S0-LRCK/DMIC-DATA1/RGMII-RXCK/PE-EINT15
PE16/TWI3-SCK/D-JTAG-DO/PWM7/I2S0-BCLK/DMIC-DATA0/PE-EINT16
PE17/TWI3-SDA/D-JTAG-CK/IR-TX/I2S0-MCLK/DMIC-CLK/PE-EINT17
V1 RGMII-RXCTL
U1 RGMII-RXD0
U2 RGMII-RXD1
U3 RGMII-TXCK
T2 D-JTAG-MS
T3 D-JTAG-DI
R1 D-JTAG-DO
R2 D-JTAG-CK
R3 RGMII-MDC
P2 RGMII-MDIO
P3 EPHY-25M
N1 RGMII-TXD2
R5 RGMII-TXD3
R4 RGMII-RXD2
N4 RGMII-RXD3
N5 RGMII-RXCK
N6 RGMII-RESET
M6 PE17
RGMII-RXCTL RGMII-RXD0 RGMII-RXD1 RGMII-TXCK RGMII-TXD0 RGMII-TXD1 RGMII-TXCTL RGMII-CLKIN RGMII-MDC RGMII-MDIO EPHY-CLK-25M RGMII-TXD2 RGMII-TXD3 RGMII-RXD2 RGMII-RXD3 RGMII-RXCK RGMII-RESET PE17
D1-BGA337
U1D
VCC-PG
E4
PG0/SDC1-CLK/UART3-TX/RGMII-RXCTRL/RMII-CRS-DV/PWM7/PG-EINT0
PG1/SDC1-CMD/UART3-RX/RGMII-RXD0/RMII-RXD0/PWM6/PG-EINT1
PG2/SDC1-D0/UART3-RTS/RGMII-RXD1/RMII-RXD1/UART4-TX/PG-EINT2
PG3/SDC1-D1/UART3-CTS/RGMII-TXCK/RMII-TXCK/UART4-RX/PG-EINT3
PG4/SDC1-D2/UART5-TX/RGMII-TXD0/RMII-TXD0/PWM5/PG-EINT4
PG5/SDC1-D3/UART5-RX/RGMII-TXD1/RMII-TXD1/PWM4/PG-EINT5
PG6/UART1-TX/TWI2-SCK/RGMII-TXD2/PWM1/PG-EINT6
PG7/UART1-RX/TWI2-SDA/RGMII-TXD3/SPDIF-IN/PG-EINT7
PG8/UART1-RTS/TWI1-SCK/RGMII-RXD2/UART3-TX/PG-EINT8
PG9/UART1-CTS/TWI1-SDA/RGMII-RXD3/UART3-RX/PG-EINT9
PG10/PWM3/TWI3-SCK/RGMII-RXCK/CLK-FANOUT0/IR-RX/PG-EINT10
PG11/I2S1-MCLK/TWI3-SDA/EPHY-25M/CLK-FANOUT1/TCON-TRIG/PG-EINT11
PG12/I2S1-LRCK/TWI0-SCK/RGMII-TXCTRL/RMII-TXEN/CLK-FANOUT2/PWM0/UART1-TX/PG-EINT12
PG13/I2S1-BCLK/TWI0-SDA/RGMII-CLKIN/RMII-RXER/PWM2/LEDC-DO/UART1-RX/PG-EINT13
PG14/I2S1-DIN0/TWI2-SCK/MDC/I2S1-DOUT1/SPI0-WP/UART1-RTS/PG-EINT14
PG15/I2S1-DOUT0/TWI2-SDA/MDIO/I2S1-DIN1/SPI0-HOLD/UART1-CTS/PG-EINT15
PG16/IR-RX/TCON-TRIG/PWM5/CLK-FANOUT2/SPDIF-IN/LEDC-DO/PG-EINT16
PG17/UART2-TX/TWI3-SCK/PWM7/CLK-FANOUT0/IR-TX/UART0-TX/PG-EINT17
PG18/UART2-RX/TWI3-SDA/PWM6/CLK-FANOUT1/SPDIF-OUT/UART0-RX/PG-EINT18
B2 R9 33R AP-SDC1-CLK
B3 AP-SDC1-CMD
A3 AP-SDC1-D0
C3 AP-SDC1-D1
A4 AP-SDC1-D2
B4 AP-SDC1-D3
B5 AP-UART1-TX
C5 AP-UART1-RX
A6 AP-UART1-RTS
B6 AP-UART1-CTS
C6 WL-WAKE-AP
D4 CLK-FANOUT1
D5 WL-REG-ON
D6
E6
F6
E7 AP-WAKE-BT
E7 BT-WAKE-AP
D7 BT-RESETN
AP-SDC1-CLK AP-SDC1-CMD AP-SDC1-D0 AP-SDC1-D1 AP-SDC1-D2 AP-SDC1-D3 AP-UART1-TX AP-UART1-RX AP-UART1-RTS AP-UART1-CTS WL-WAKE-AP CLK-FANOUT1 WL-REG-ON
AP-WAKE-BT BT-WAKE-AP BT-RESETN
D1-BGA337
LDOB-OUT LDOA-OUT LDO-IN
U1C
LDO-IN M7
LDOA-OUT L7
LDOB-OUT N7
VCC-EFUSE J4
VCC-PLL J5
X32KOUT L3
X32KIN L2
VCC-RTC K5
VCC-DCXO K4
DCXO-XIN K1
DCXO-XOUT K2
DXIN
DXOUT
REFCLK-OUT J2
TEST N2
SYS
VDD-CPU0 G7
VDD-CPU1 G8
VDD-CPU2 H7
VDD-CPU3 H8
VDD-CPU4 H9
VDD-CPUFB G9
VDD-SYS0 G14
VDD-SYS1 H13
VDD-SYS2 H14
VDD-SYS3 J13
VDD-SYS4 J14
VDD-SYSFB G13
VCC-IO G17
FEL A18
NMI N3
RESET M2
VDD-CPU VDD-CPUFB VDD-SYS VDD-SYSFB VCC-IO FEL RESET
VCC-RTC VCC-PLL VCC-EFUSE VCC-DCXO
X32KOUT X32KIN DCXO-XIN DCXO-XOUT
AP-CK24M-OUT AP-CLK-OUT
R7 1K
R8 47K
D1-BGA337
U1G
GND0 GND1 GND2 GND3 GND4 GND5 GND6 GND7 GND8 GND9 GND10 GND11 GND12 GND13 GND14 GND15 GND16 GND17 GND18 GND19 GND20 GND21 GND22 GND23 GND24 GND25 GND26 GND27 GND28 GND29 GND30 GND31 GND32 GND33 GND34 GND35 GND36 GND37 GND38 GND39 GND40 GND41 GND42 GND43 GND44
GND45 GND46 GND47 GND48 GND49 GND50 GND51 GND52 GND53 GND54 GND55 GND56 GND57 GND58 GND59 GND60 GND61 GND62 GND63 GND64 GND65 GND66 GND67 GND68 GND69 GND70 GND71 GND72 GND73 GND74 GND75 GND76 GND77 GND78 GND79 GND80 GND81 GND82 GND83 GND84 GND85 GND86 GND87 GND88 GND89
D1-BGA337
LDO-IN LDOA-OUT VCC-PD VCC-LVDS VCC-PE VCC-PG
C14 10uF
C15 2.2uF
C16 0.1uF
C17 0.1uF
C18 0.1uF
C19 0.1uF
VCC-PLL VCC-DCXO VCC-IO
C20 0.1uF
C21 0.1uF
C22 0.1uF
CLOSED TO CPU
VDD-CPU
C23 10uF
C24 10uF
C25 2.2uF
C26 2.2uF
C27 2.2uF
VDD-SYS
C31 10uF
C32 10uF
C33 2.2uF
C34 2.2uF
C35 2.2uF
CLOSED TO CPU
Y1 24MHz
C29 18pF
C28 18pF
XIN GND GND XOUT
DCXO-XIN DCXO-XOUT
C30 22pF
C36 22pF
X1 32.768kHz
R10 680K
X32KOUT X32KIN
D1 0402
R11 5.1K
VCC-3V3 PE17
Title
Size A4
Number
Revision
Date: 3/28/2022
File: D:\My Project\..\SOC-SYS.SchDoc
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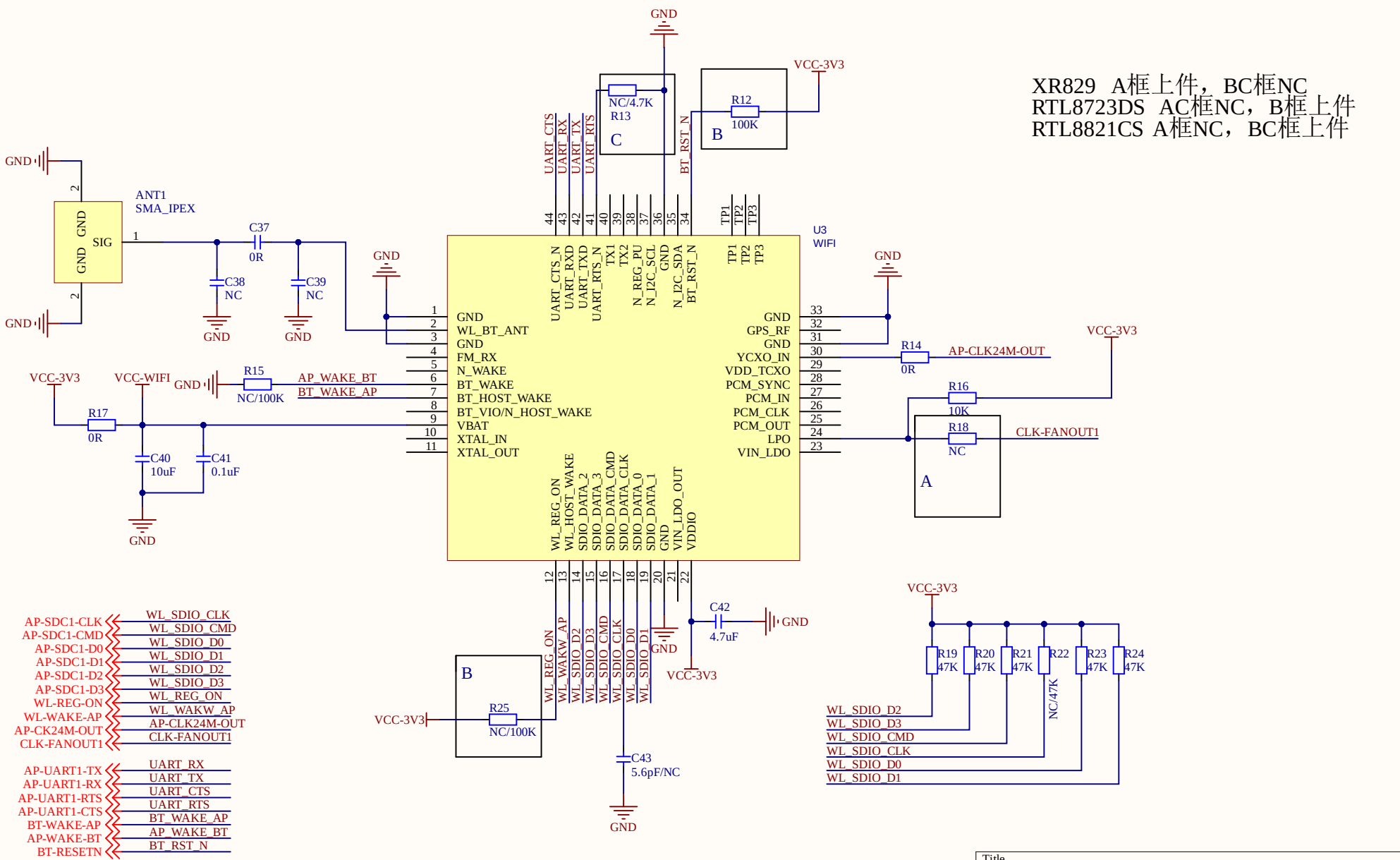
XR829 A框上件，BC框NC
RTL8723DS AC框NC，B框上件
RTL8821CS A框NC，BC框上件
U3
WIFI
ANT1
SMA_IPEX
C37
0R
C38
NC
C39
NC
R12
100K
NC/4.7K
R13
R14
0R
AP-CLK24M-OUT
R16
10K
R18
NC
CLK-FANOUT1
R15
NC/100K
AP_WAKE_BT
BT_WAKE_AP
R17
0R
C40
10uF
C41
0.1uF
C42
4.7uF
R25
NC/100K
C43
5.6pF/NC
R19
47K
R20
47K
R21
47K
R22
NC/47K
R23
47K
R24
47K
GND
VCC-3V3
VCC-WIFI
UART_CTS
UART_RX
UART_TX
UART_RTS
BT_RST_N
TP1
TP2
TP3
UART_CTS_N
UART_RXD
UART_TXD
UART_RTS_N
TX1
TX2
N_REG_PU
N_I2C_SCL
N_I2C_SDA
BT_RST_N
GND
WL_BT_ANT
GND
FM_RX
N_WAKE
BT_WAKE
BT_HOST_WAKE
BT_VIO/N_HOST_WAKE
VBAT
XTAL_IN
XTAL_OUT
GND
GPS_RF
GND
YCXO_IN
VDD_TCXO
PCM_SYNC
PCM_IN
PCM_CLK
PCM_OUT
LPO
VIN_LDO
WL_REG_ON
WL_HOST_WAKE
SDIO_DATA_2
SDIO_DATA_3
SDIO_DATA_CMD
SDIO_DATA_CLK
SDIO_DATA_0
SDIO_DATA_1
GND
VIN_LDO_OUT
VDDIO
WL_REG_ON
WL_WAKW_AP
WL_SDIO_D2
WL_SDIO_D3
WL_SDIO_CMD
WL_SDIO_CLK
WL_SDIO_D0
WL_SDIO_D1
AP-SDC1-CLK
AP-SDC1-CMD
AP-SDC1-D0
AP-SDC1-D1
AP-SDC1-D2
AP-SDC1-D3
WL-REG-ON
WL-WAKE-AP
AP-CK24M-OUT
CLK-FANOUT1
WL_SDIO_CLK
WL_SDIO_CMD
WL_SDIO_D0
WL_SDIO_D1
WL_SDIO_D2
WL_SDIO_D3
WL_REG_ON
WL_WAKW_AP
AP-CLK24M-OUT
CLK-FANOUT1
AP-UART1-TX
AP-UART1-RX
AP-UART1-RTS
AP-UART1-CTS
BT-WAKE-AP
AP-WAKE-BT
BT-RESETN
UART_RX
UART_TX
UART_CTS
UART_RTS
BT_WAKE_AP
AP_WAKE_BT
BT_RST_N
Title
Size
A4
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Date:
3/28/2022
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