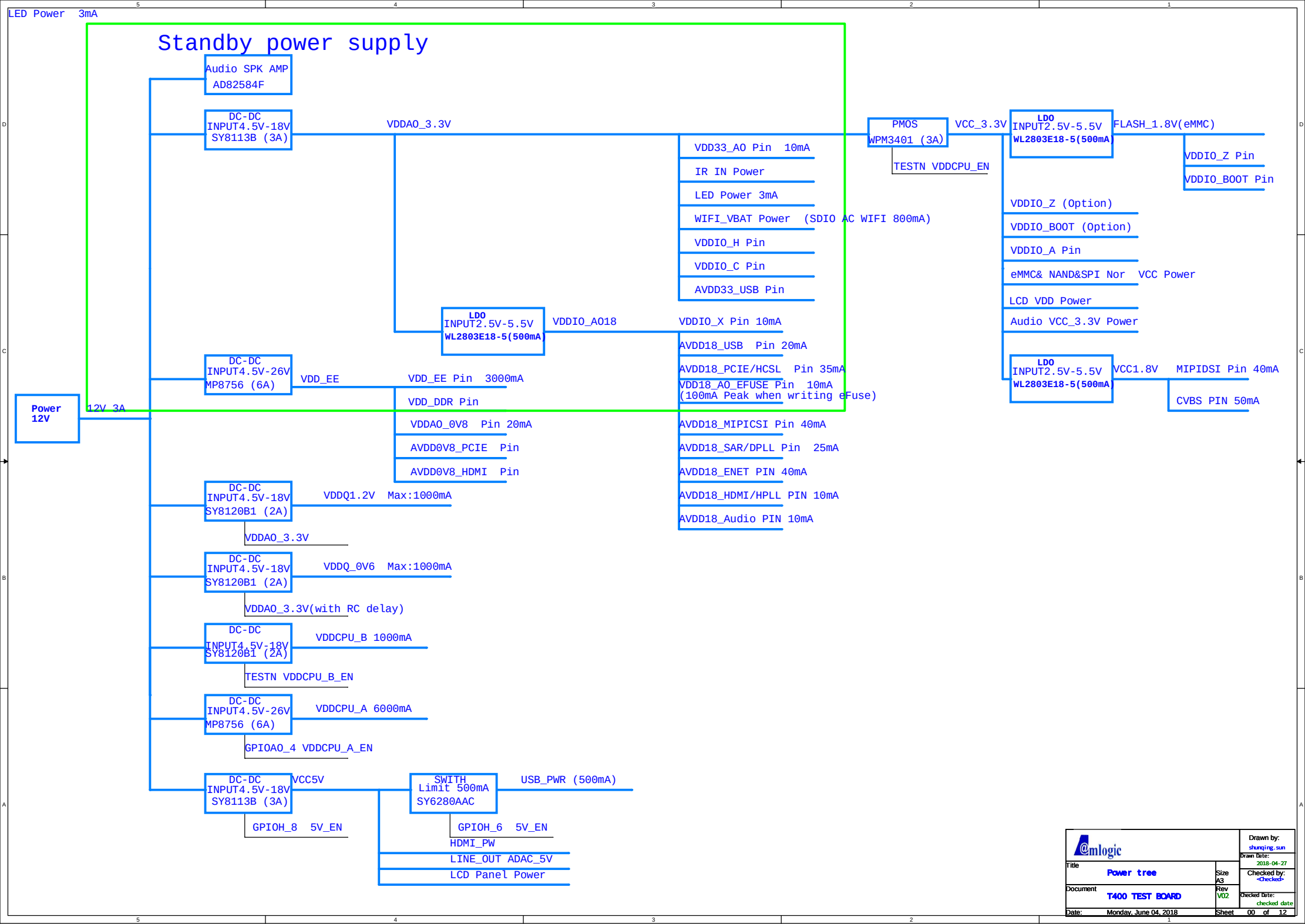
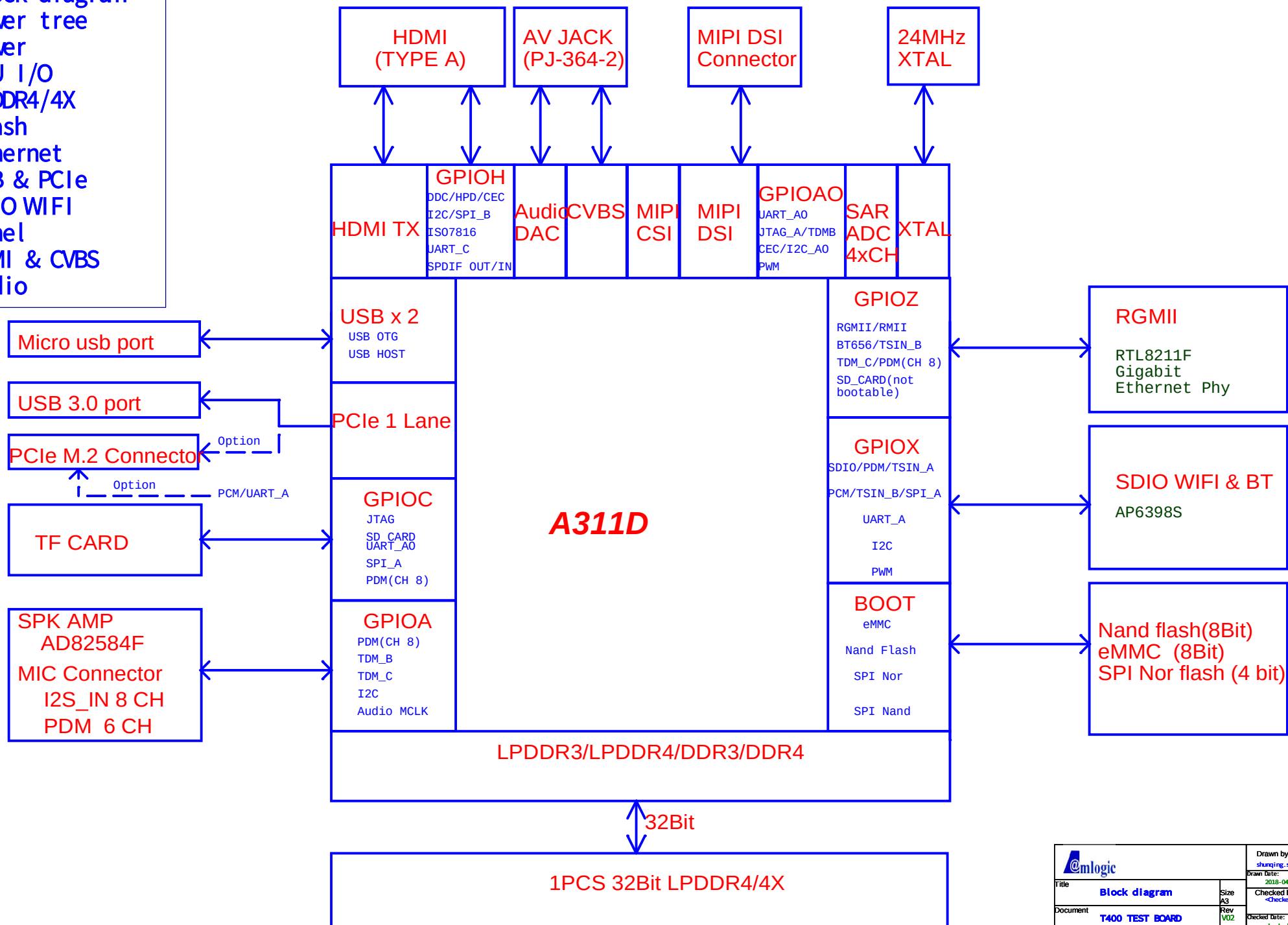




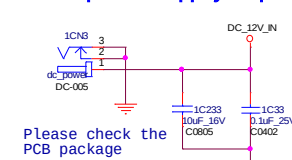
PAGE INDEX

- 00- Block diagram
- 00- Power tree
- 01- Power
- 02- CPU I/O
- 03- LPDDR4/4X
- 04- Flash
- 05- Ethernet
- 06- USB & PCIe
- 07- SDIO WIFI
- 08- Panel
- 09- HDMI & CVBS
- 10- Audio

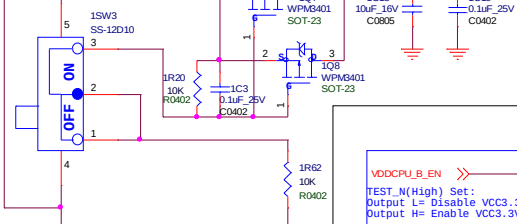
A311D Diagram



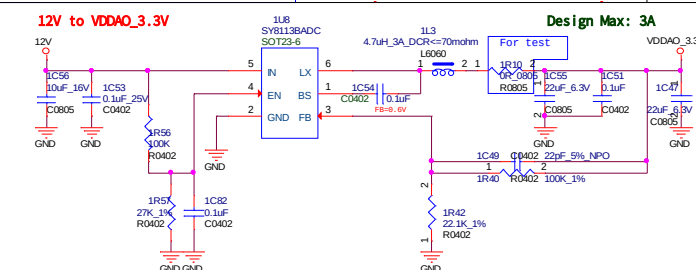
DC 12V power supply input



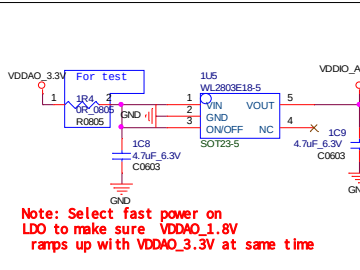
Power switch



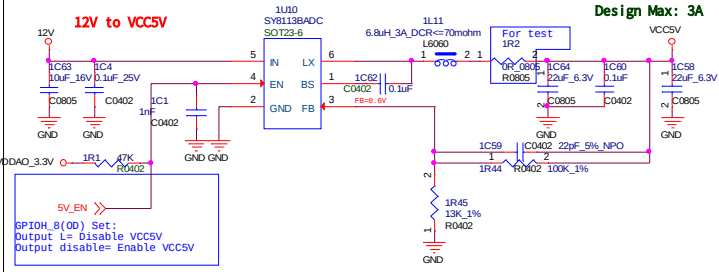
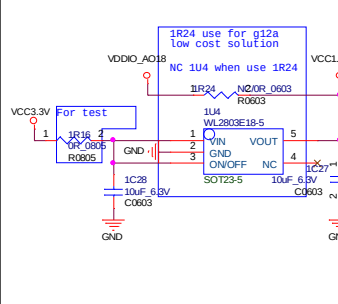
VDDCPU_B_EN
TEST_N(High) Set:
Output L= Disable VCC3.3V
Output H= Enable VCC3.3V



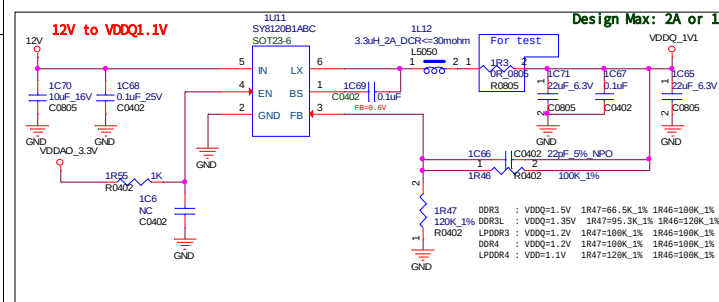
Design Max: 3A



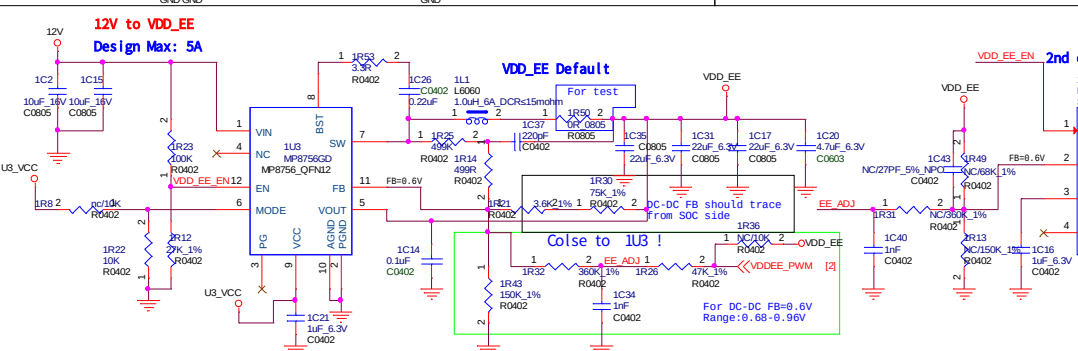
Note: Select fast power on LDO to make sure VDDAO_1.8V ramps up with VDDAO_3.3V at same time



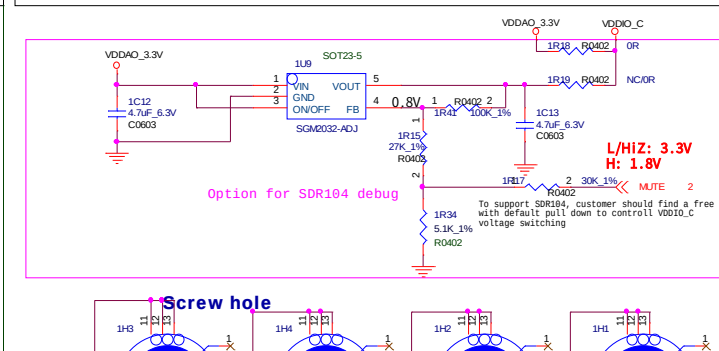
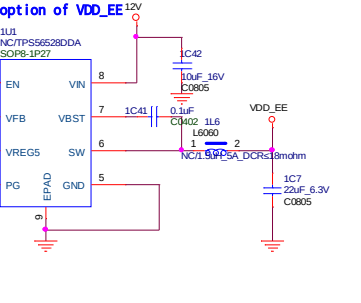
Design Max: 3A



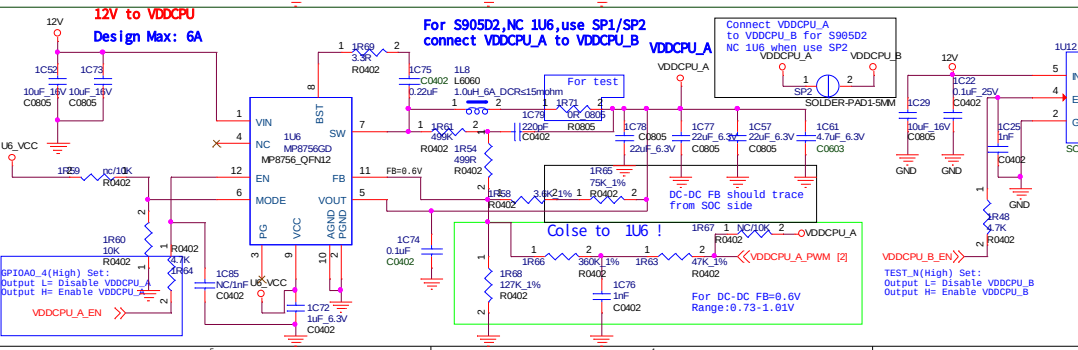
Design Max: 2A or 1A



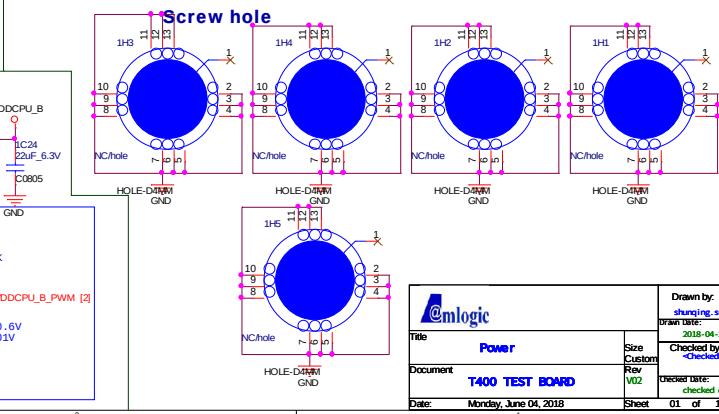
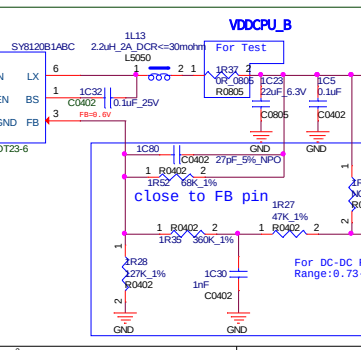
VDD_EE Default



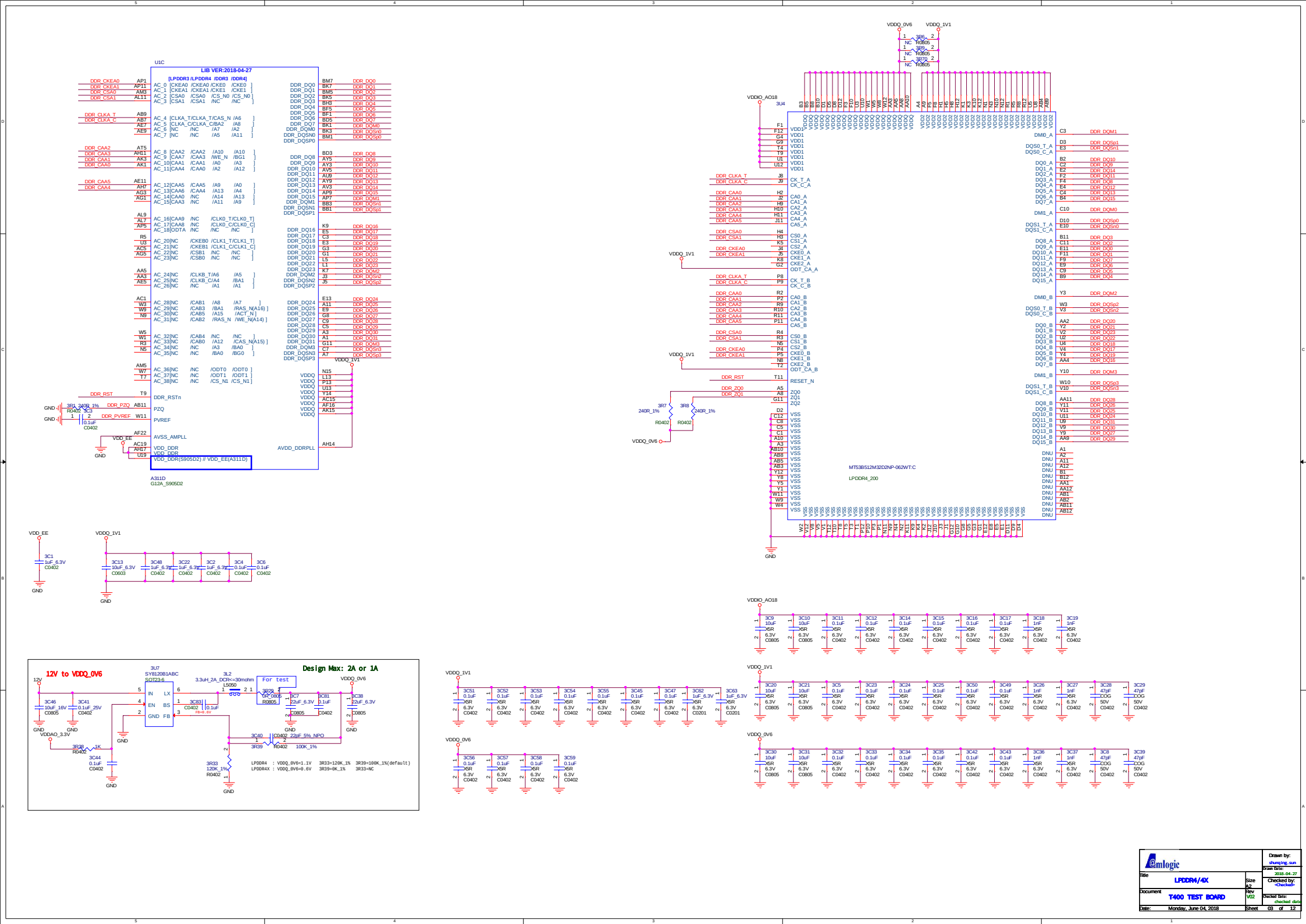
L/HIZ: 3.3V H: 1.8V

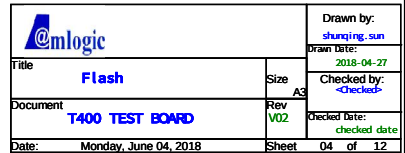


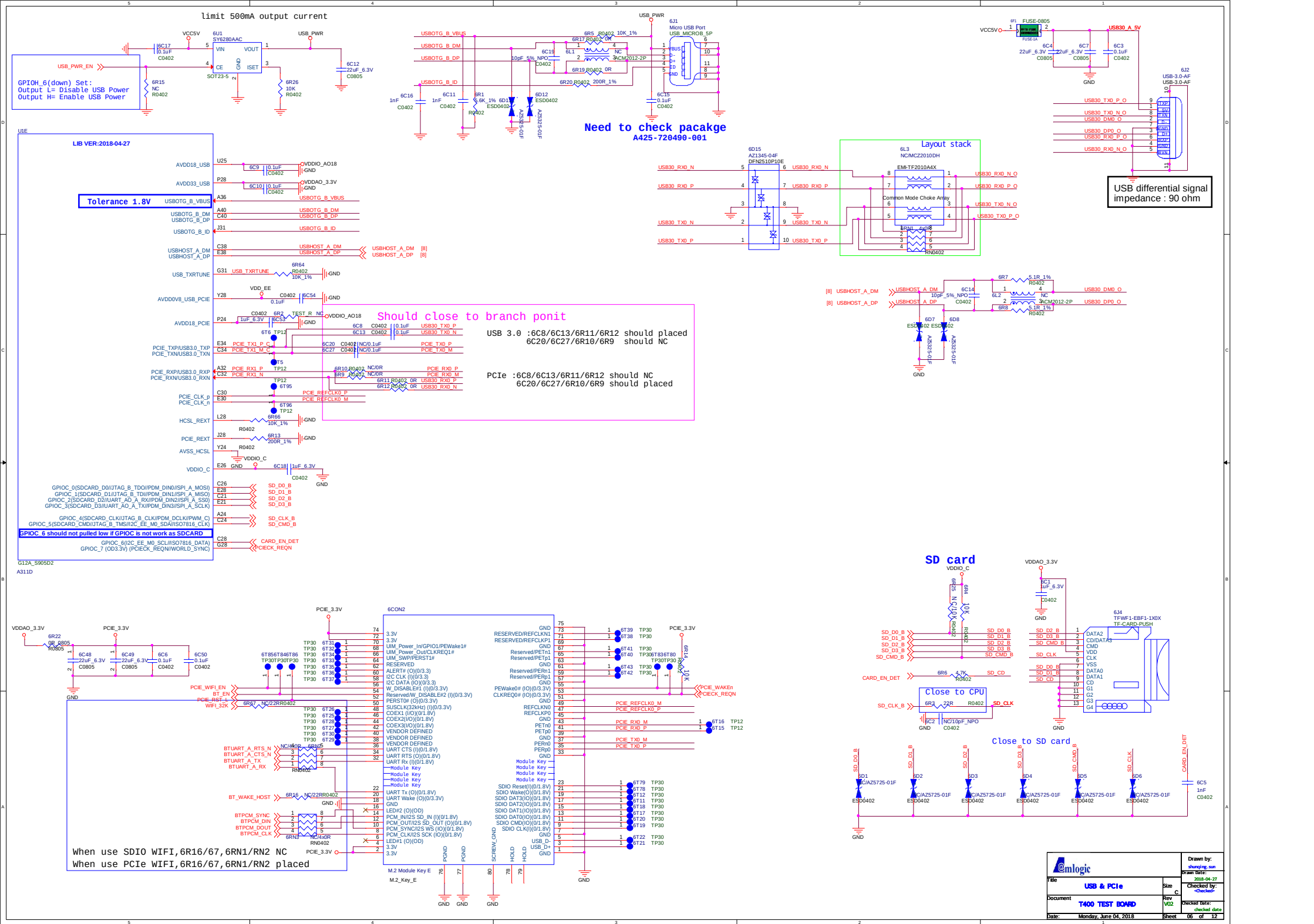
Design Max: 6A

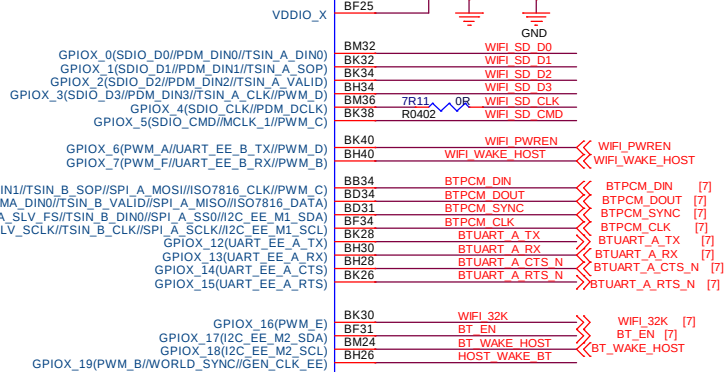


		Drawn by:	shunqing_sun
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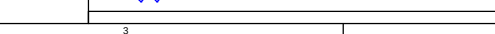


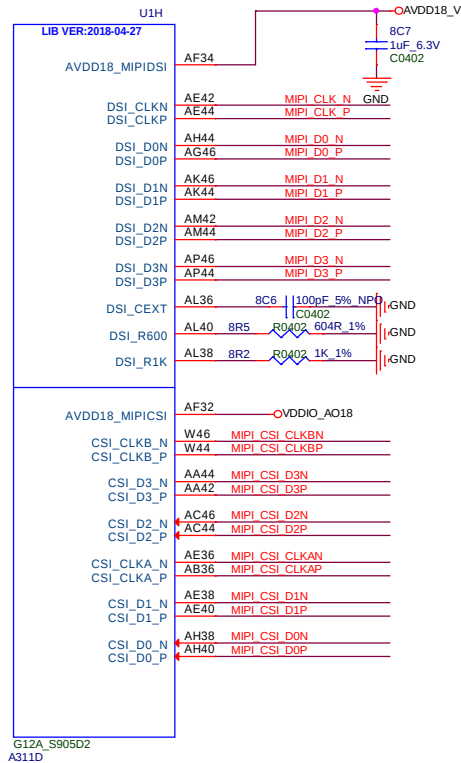






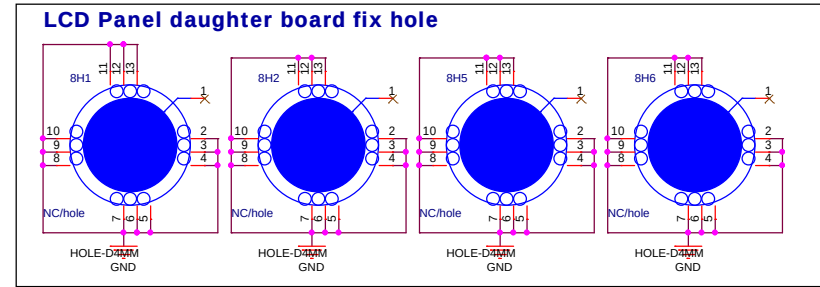
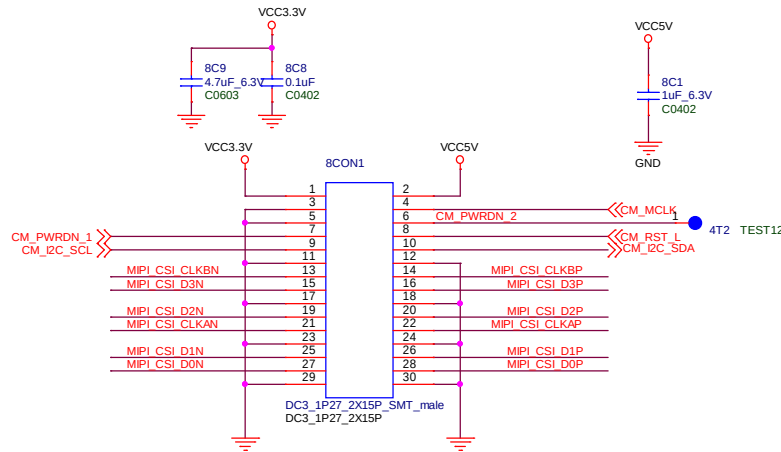
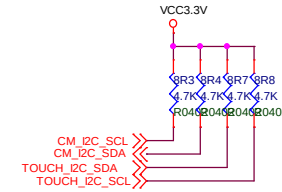
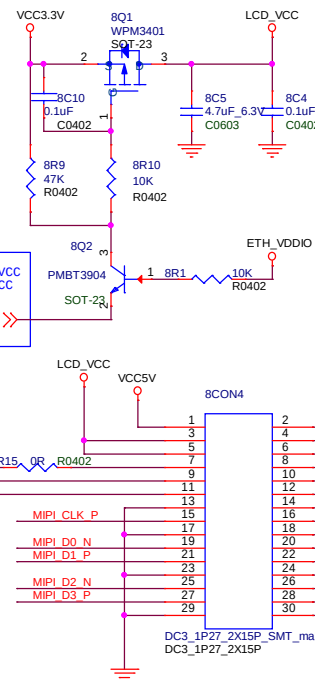
A311D





GPIO2_8(High) Set:
Output = Disable LCD_VCC
LCD_VDD_EN

LCD_BL_EN
TOUCH_INT
TOUCH_I2C_SCL



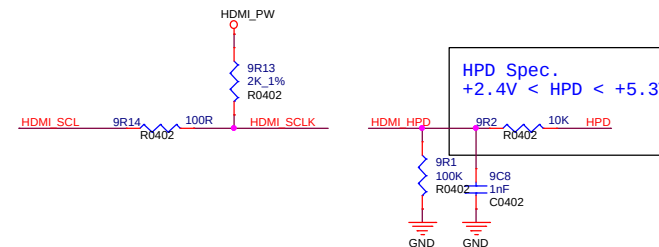
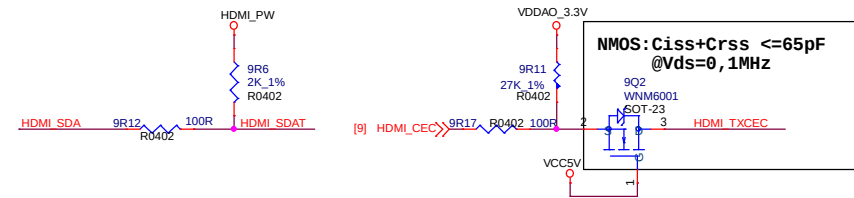
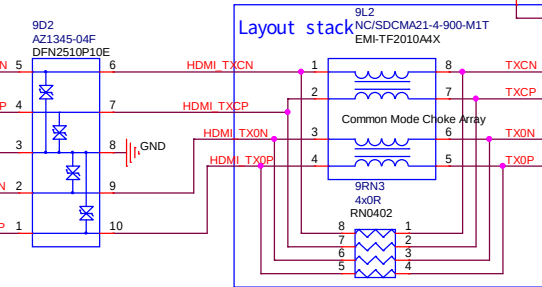
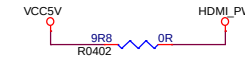
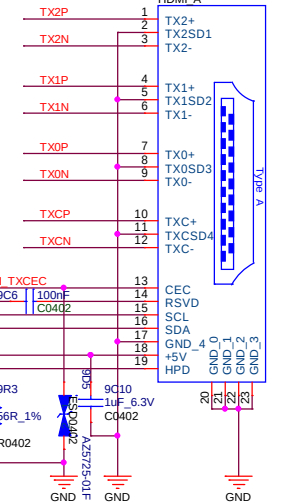
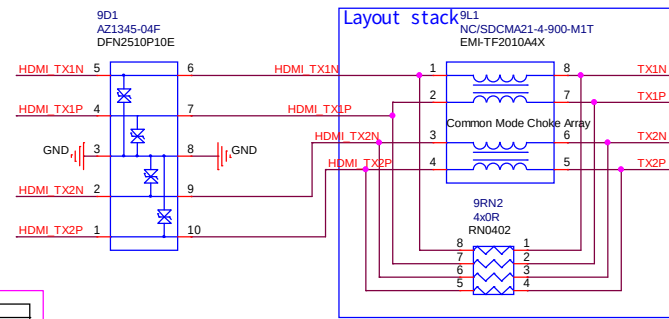
U1J
LIB VER:2018-03-15

A28	DVSS	BD22
A46	DVSS	BD25
A5	DVSS	BF10
AB40	DVSS	BF3
AC13	DVSS	BP7
AC17	DVSS	BH1
AC21	DVSS	BH19
AC23	DVSS	BH32
AC25	DVSS	BH36
AC29	DVSS	BH38
AC3	DVSS	BH44
AC42	DVSS	BH5
AE3	DVSS	BH7
AF14	DVSS	BK13
AF18	DVSS	BK15
AF20	DVSS	BK24
AF24	DVSS	BM11
AF30	DVSS	BM28
AG42	DVSS	BM3
AH29	DVSS	BM40
AH32	DVSS	BM46
AH36	DVSS	C1
AH9	DVSS	C13
AK13	DVSS	C36
AK17	DVSS	E46
AK23	DVSS	E7
AK25	DVSS	G13
AK31	DVSS	G42
AK42	DVSS	G5
AK5	DVSS	J25
AN14	DVSS	J42
AN16	DVSS	K11
AN18	DVSS	L19
AN24	DVSS	L25
AN30	DVSS	L3
AN34	DVSS	L31
AP3	DVSS	L34
AP40	DVSS	N11
AT13	DVSS	N3
AT15	DVSS	N44
AT23	DVSS	N7
AT25	DVSS	P18
AT3	DVSS	P22
AT33	DVSS	P30
AU11	DVSS	P34
AU29	DVSS	R1
AU31	DVSS	R15
AV1	DVSS	T11
AW14	DVSS	U15
AW24	DVSS	U17
AW30	DVSS	U21
AW28	DVSS	U23
AY11	DVSS	U33
AY44	DVSS	U42
AY7	DVSS	U5
BB10	DVSS	W42
BB13	DVSS	Y16
BB16	DVSS	Y18
BB22	DVSS	Y22
BB25	DVSS	Y26
BB31	DVSS	
BB46	DVSS	
BB5	DVSS	
BC7	DVSS	
BD10	DVSS	

Preliminary Symbol

G12A_S905D2
A311D

		Drawn by: shunping.sun
Title Panel		Drawn date: 2018-04-27
Document T400 TEST BOARD		Checked by: <Checked>
Rev V02		Checked date: checked date
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HPD Spec.
+2.4V < HPD < +5.3

